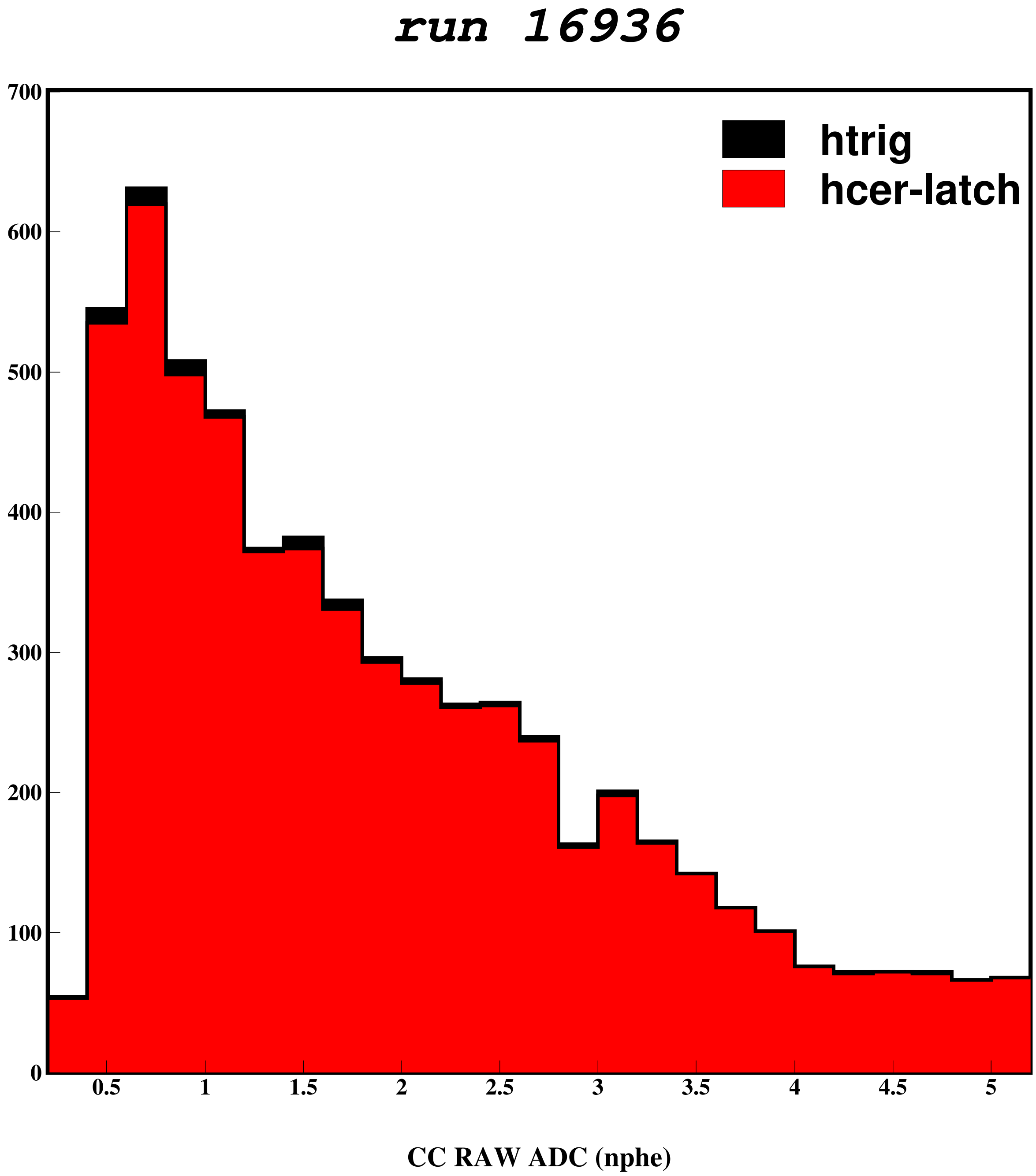
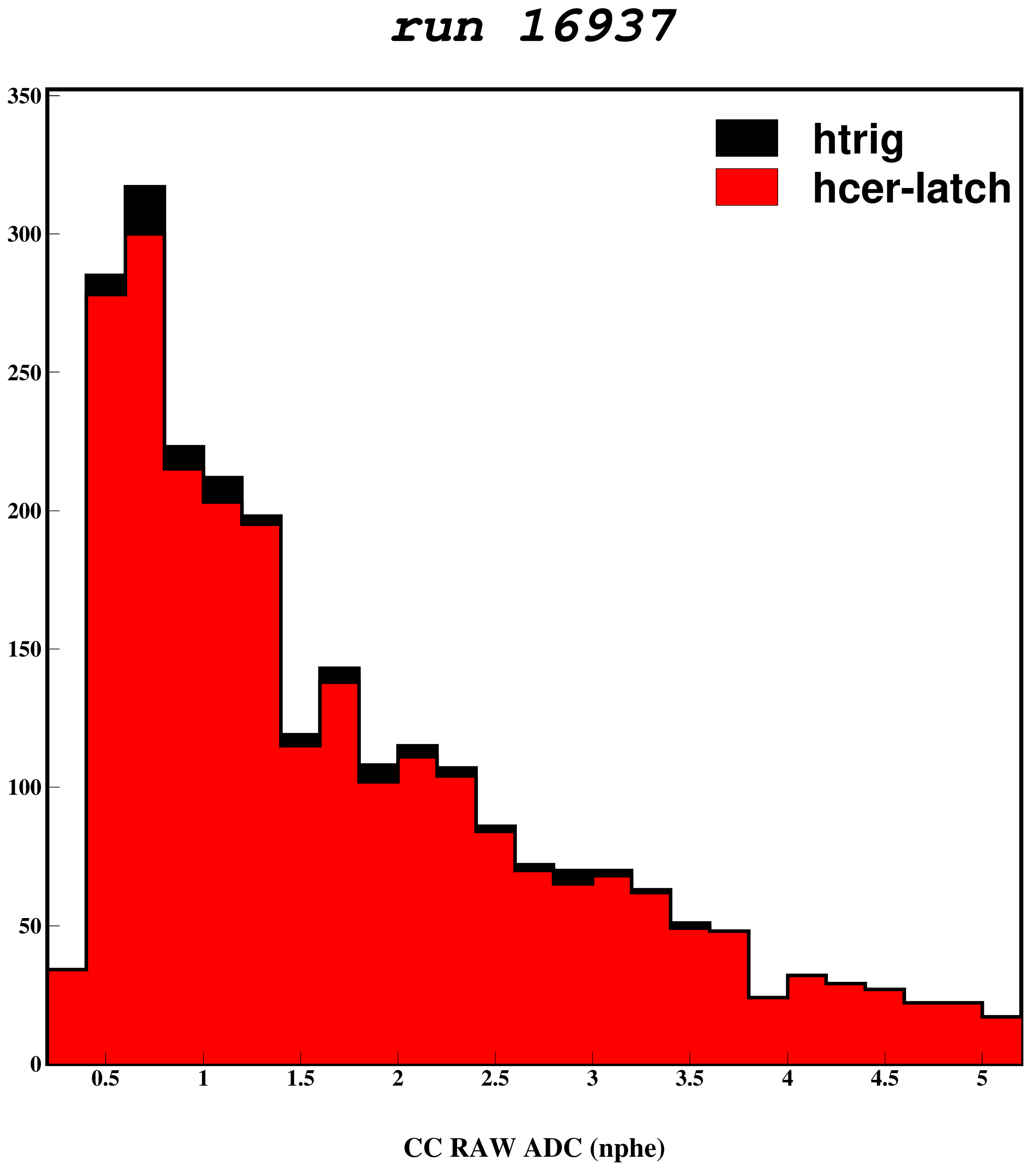
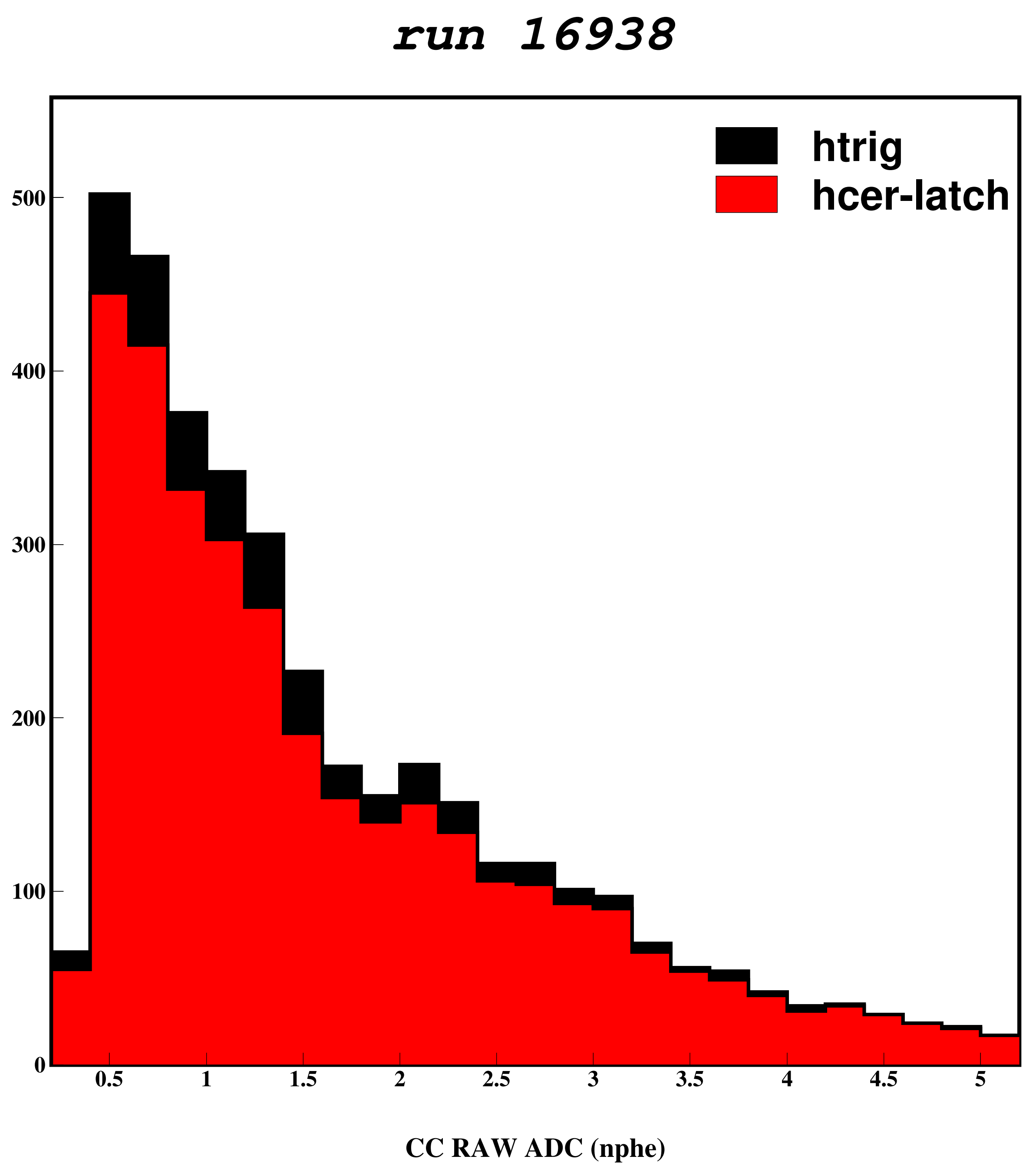
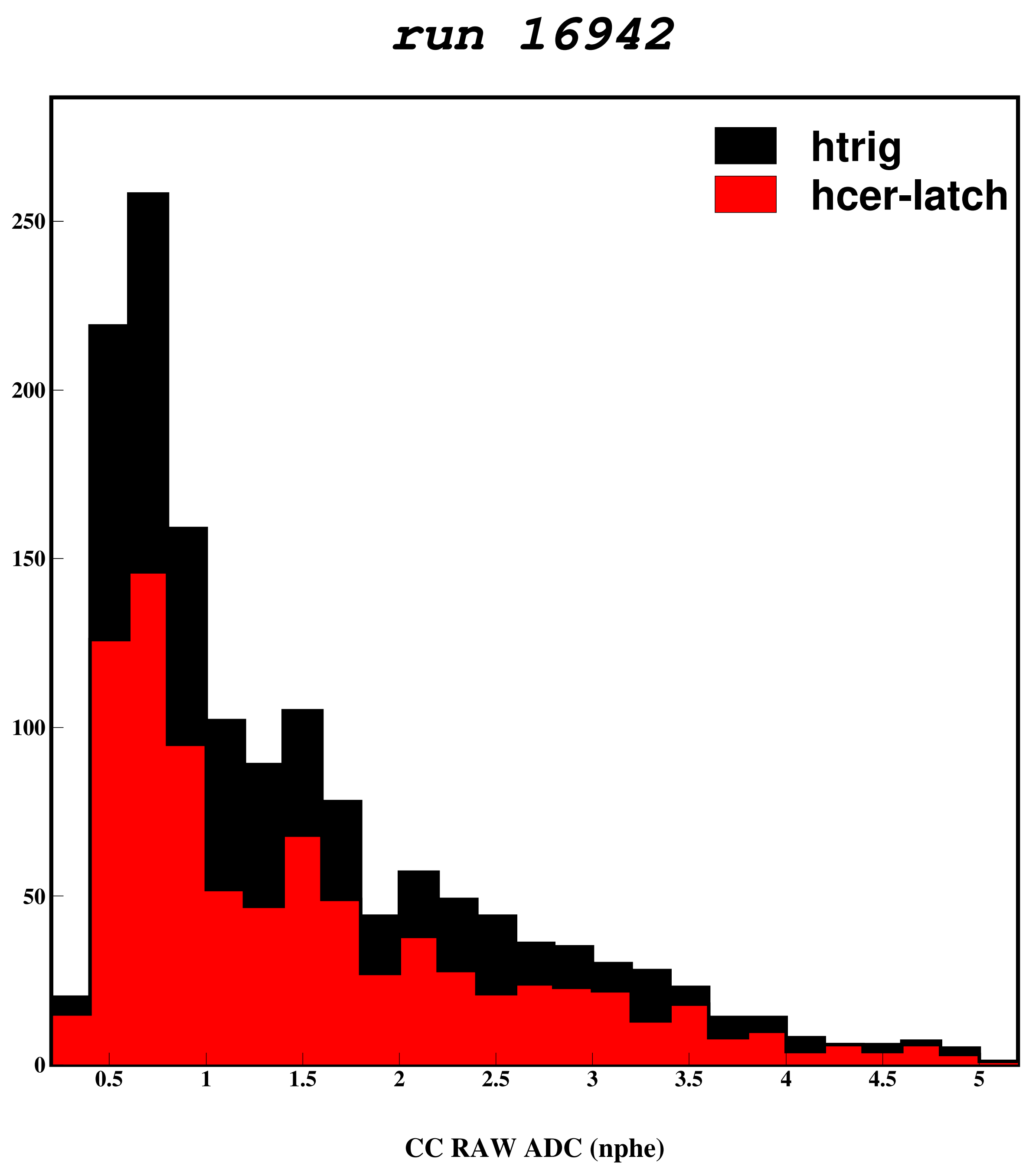
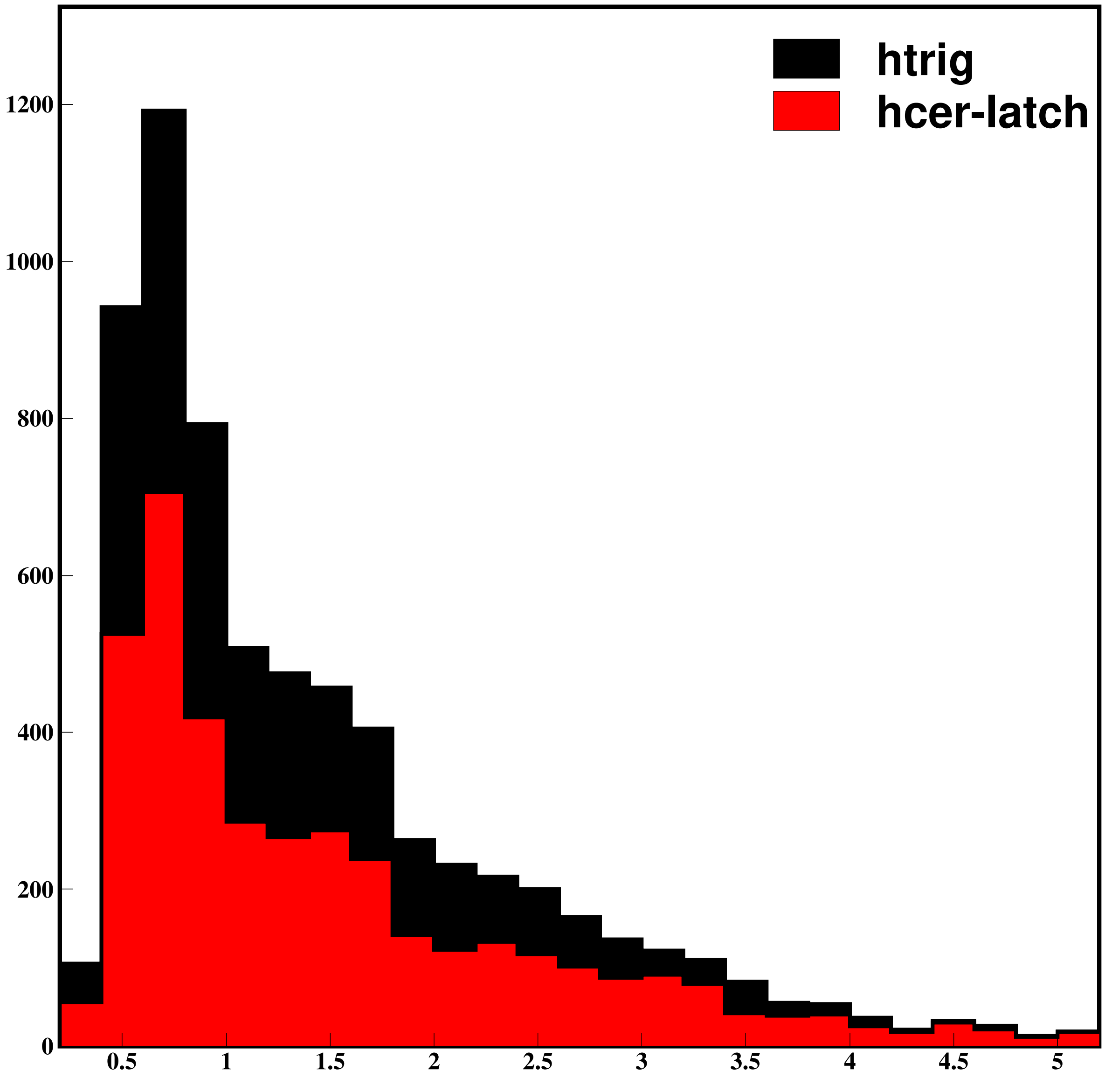

run 16936
htrig
hcer-latch
0
100
200
300
400
500
600
700
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)


run 16937
htrig
hcer-latch
0
50
100
150
200
250
300
350
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

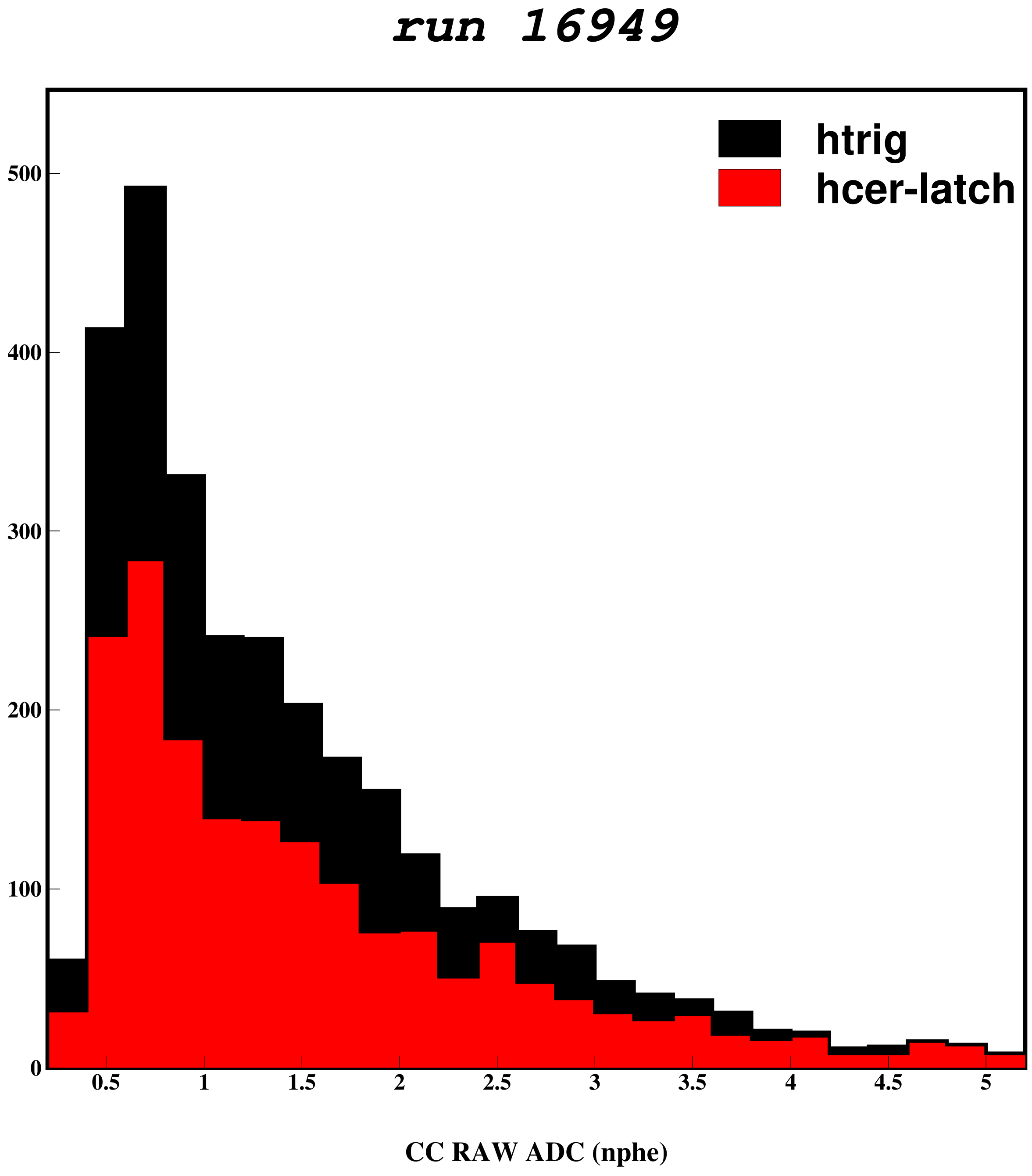

run 16938
htrig
hcer-latch
0
100
200
300
400
500
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)


run 16942
htrig
hcer-latch
0
50
100
150
200
250
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

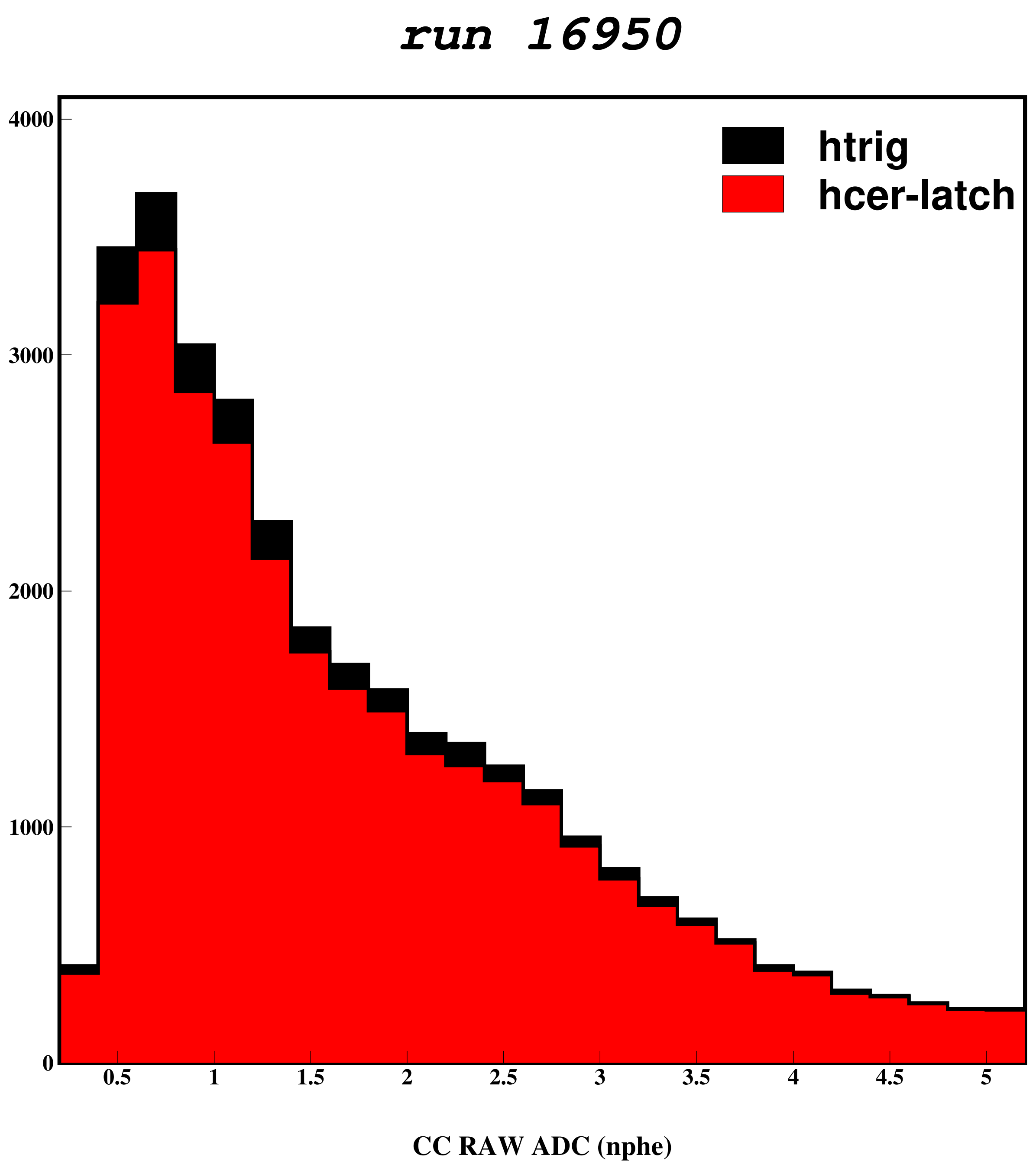
run 16948

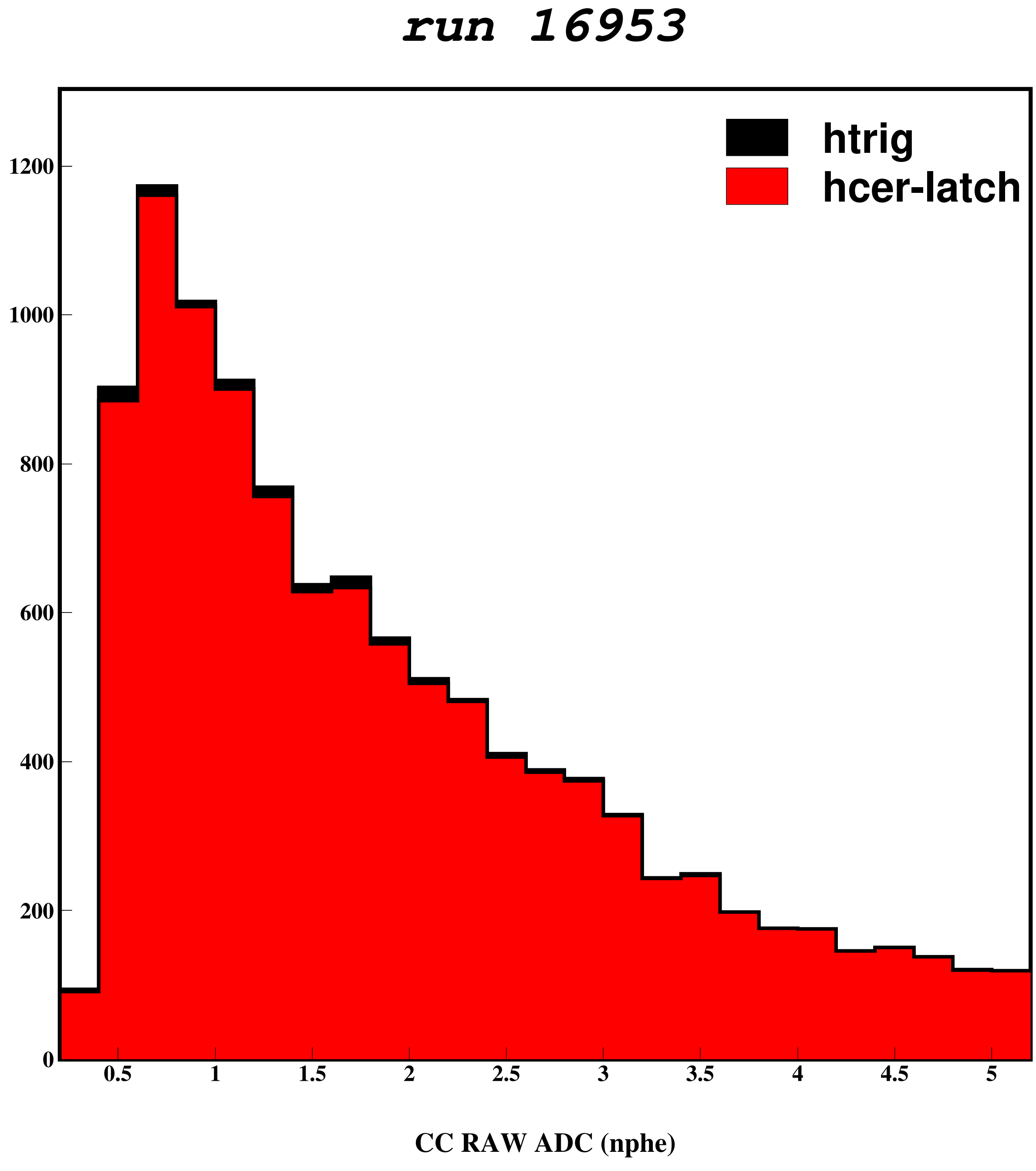


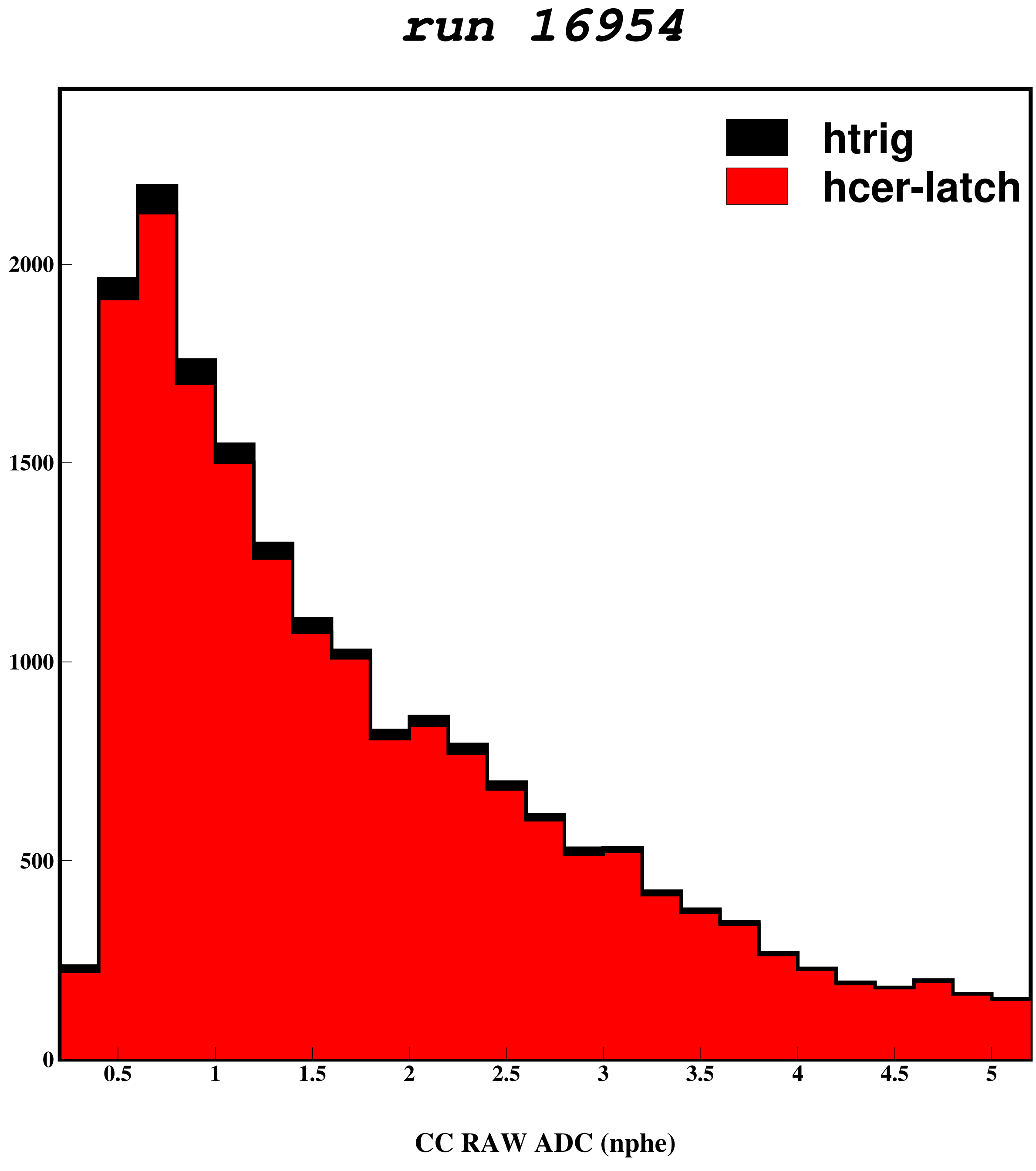
CC RAW ADC (nphe)



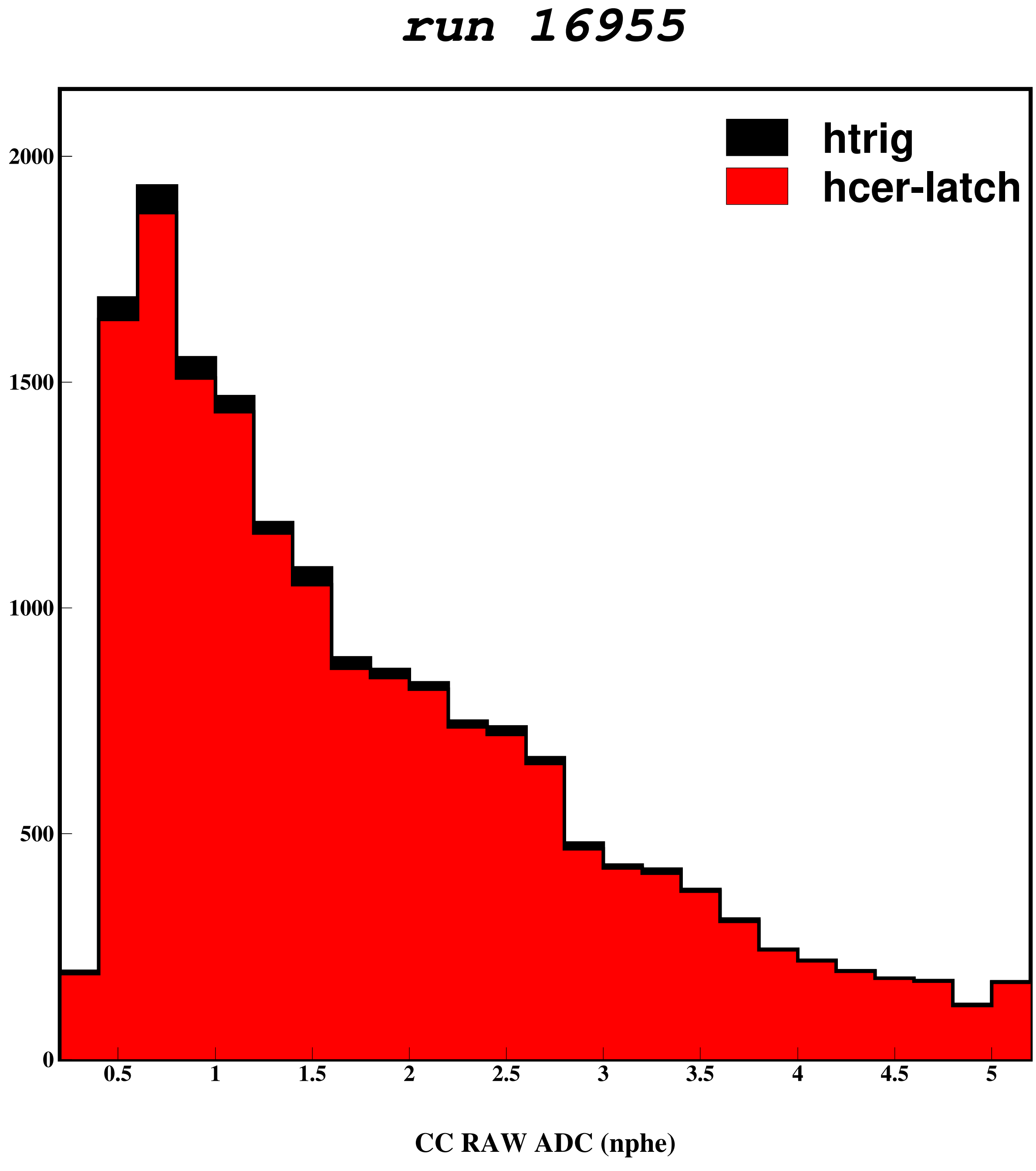
run 16949
htrig
hcer-latch
500
400
300
200
100
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

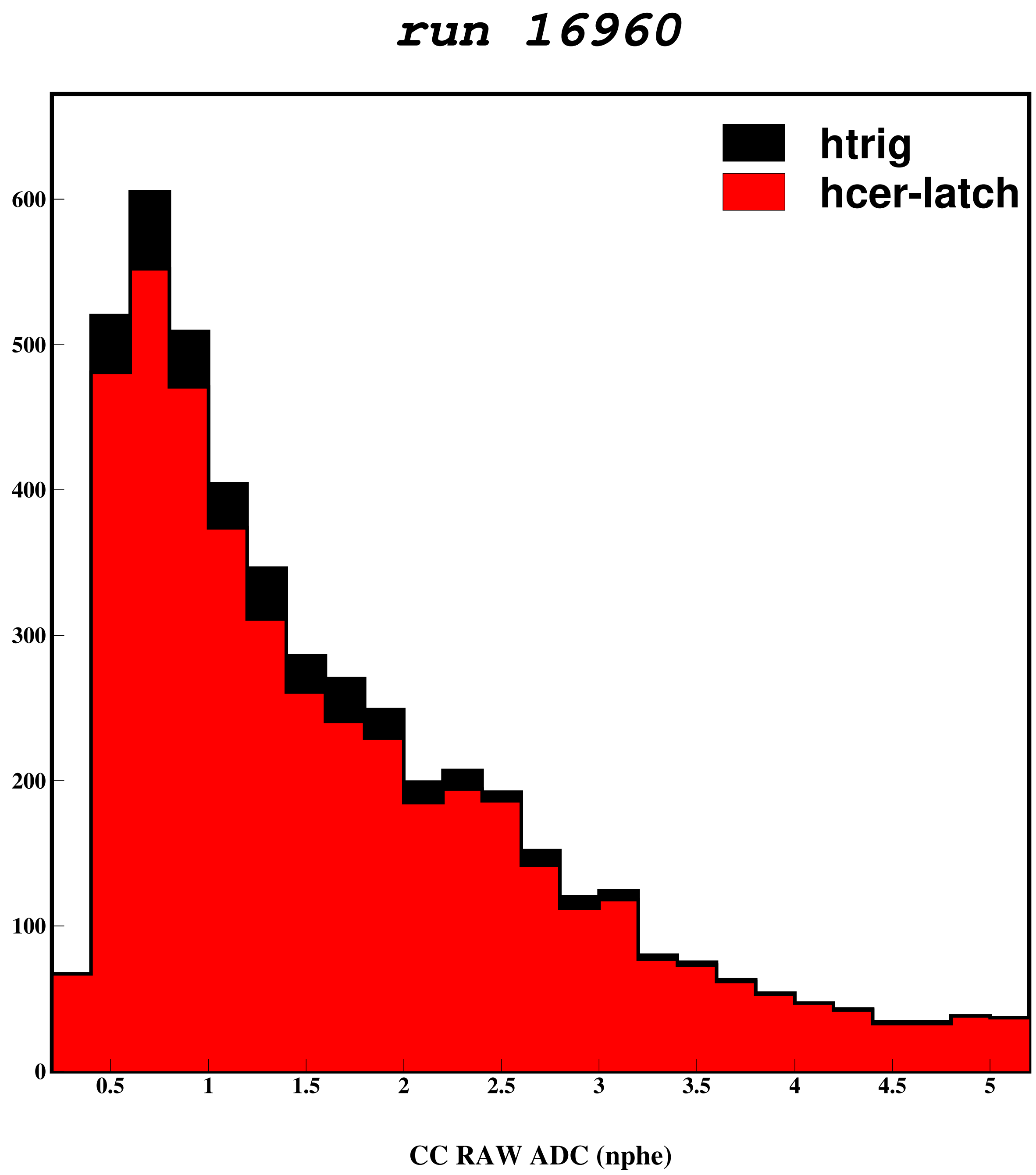

run 16950
htrig
hcer-latch
4000
3000
2000
1000
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)


run 16953
htrig
hcer-latch
1200
1000
800
600
400
200
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

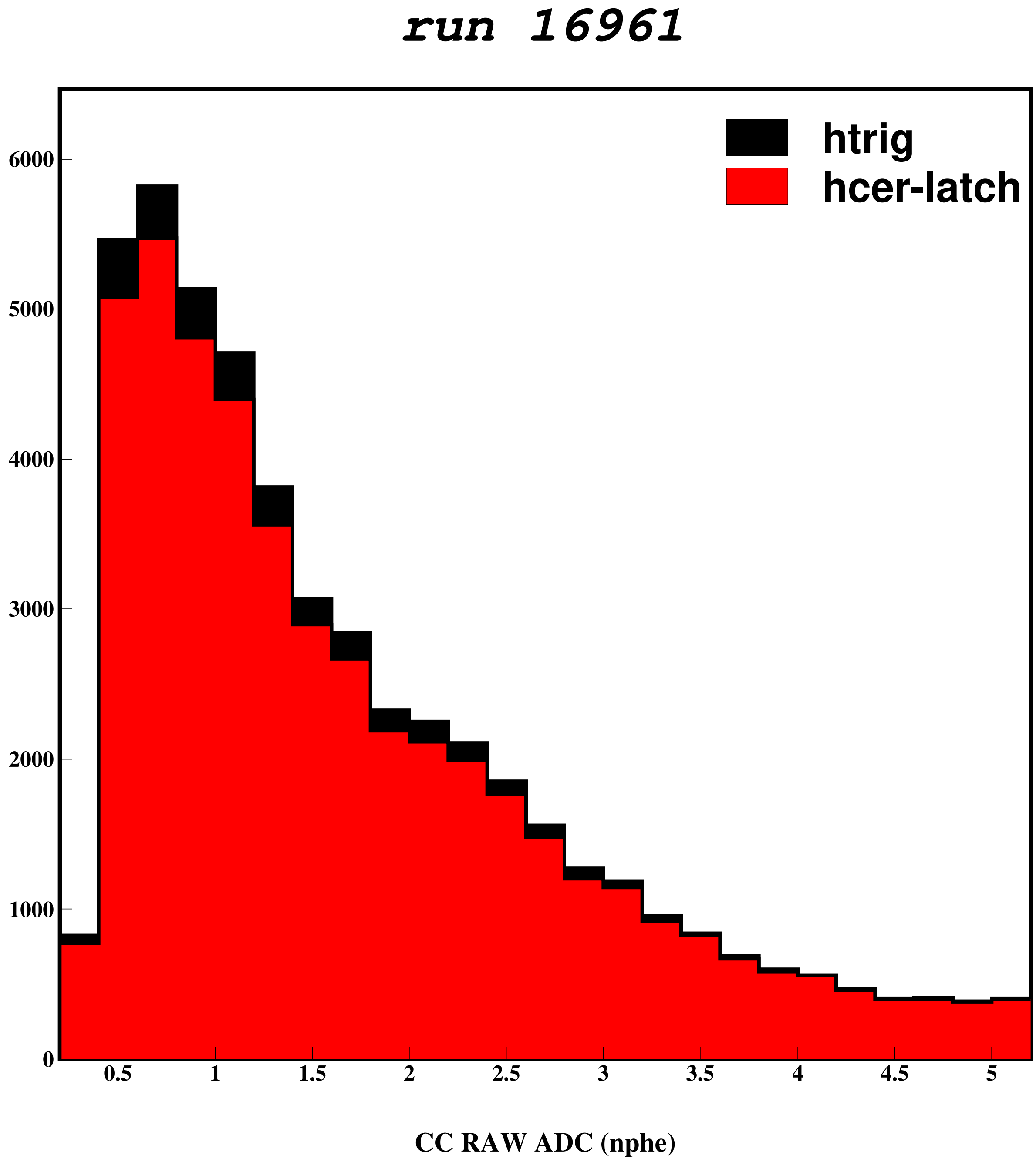


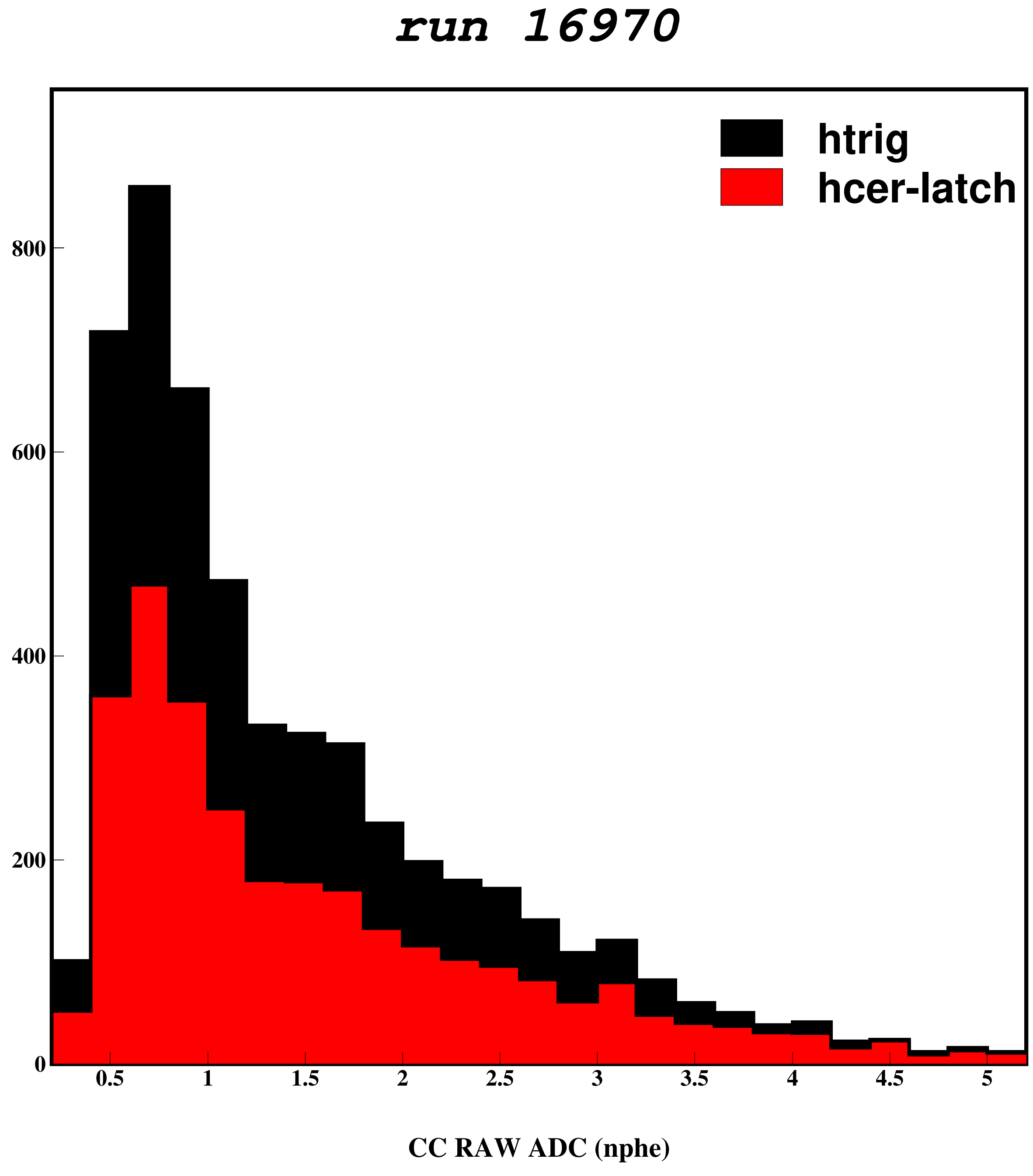
run 16954
htrig
hcer-latch
2000
1500
1000
500
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

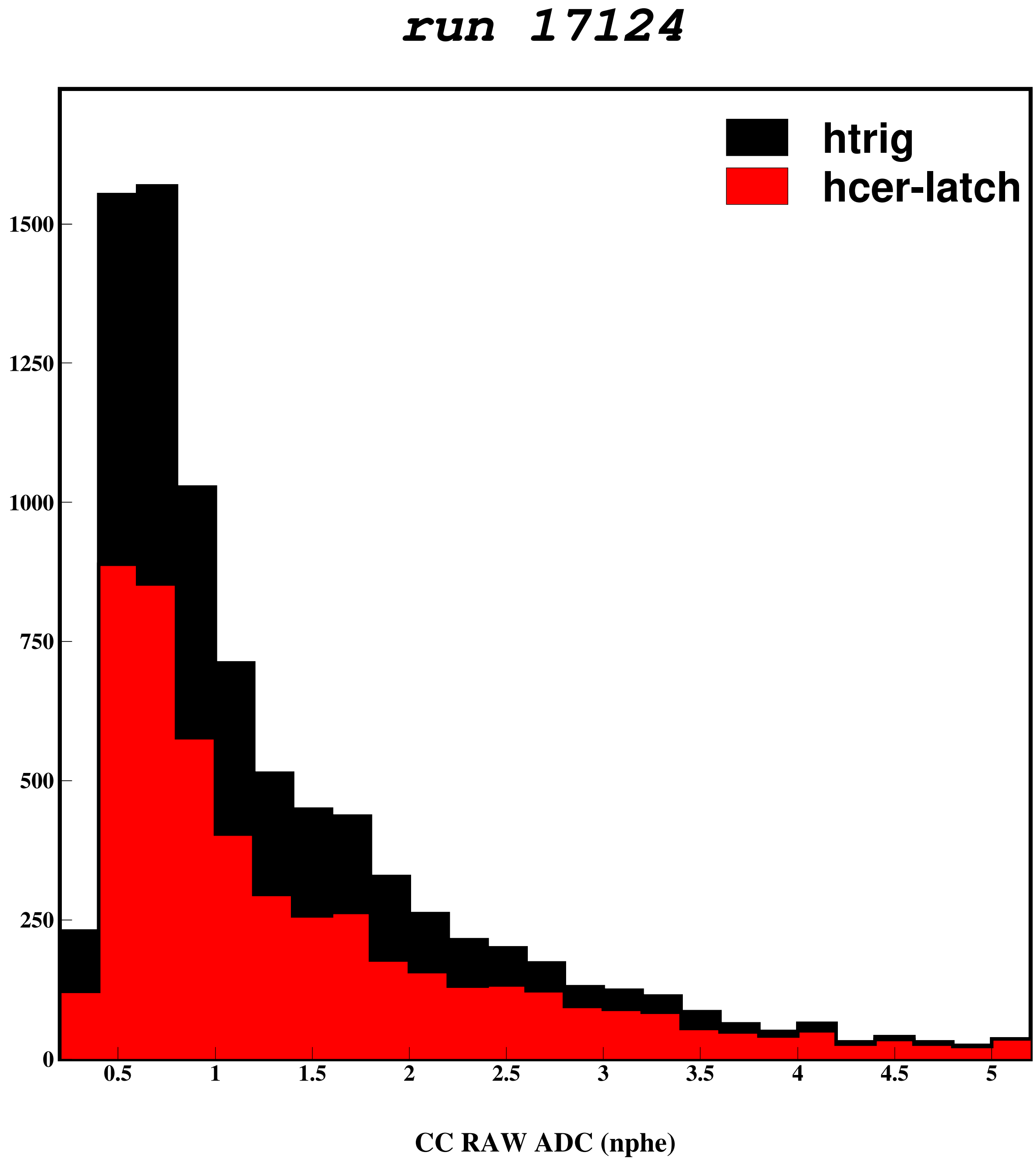

run 16955
htrig
hcer-latch
2000
1500
1000
500
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

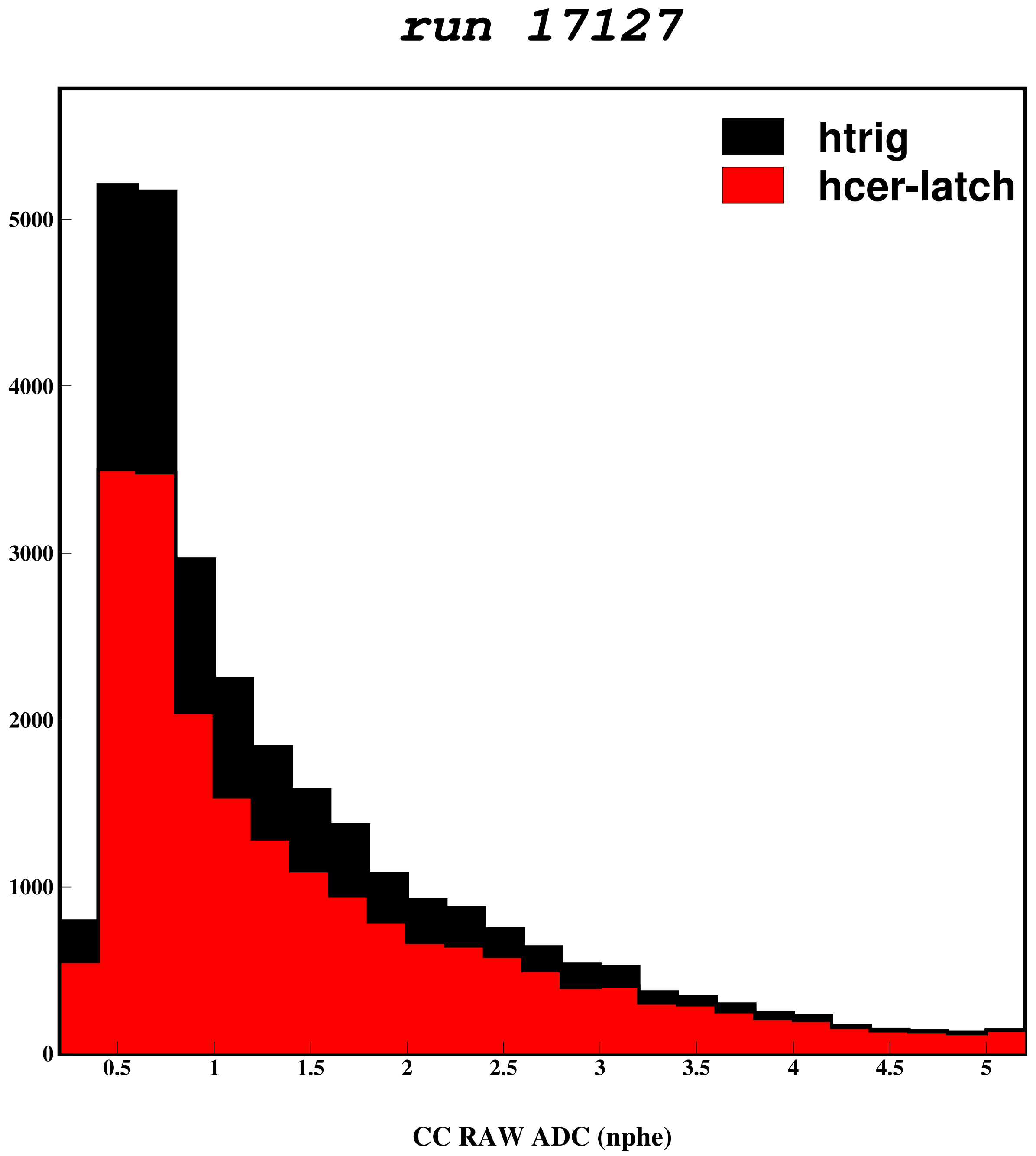


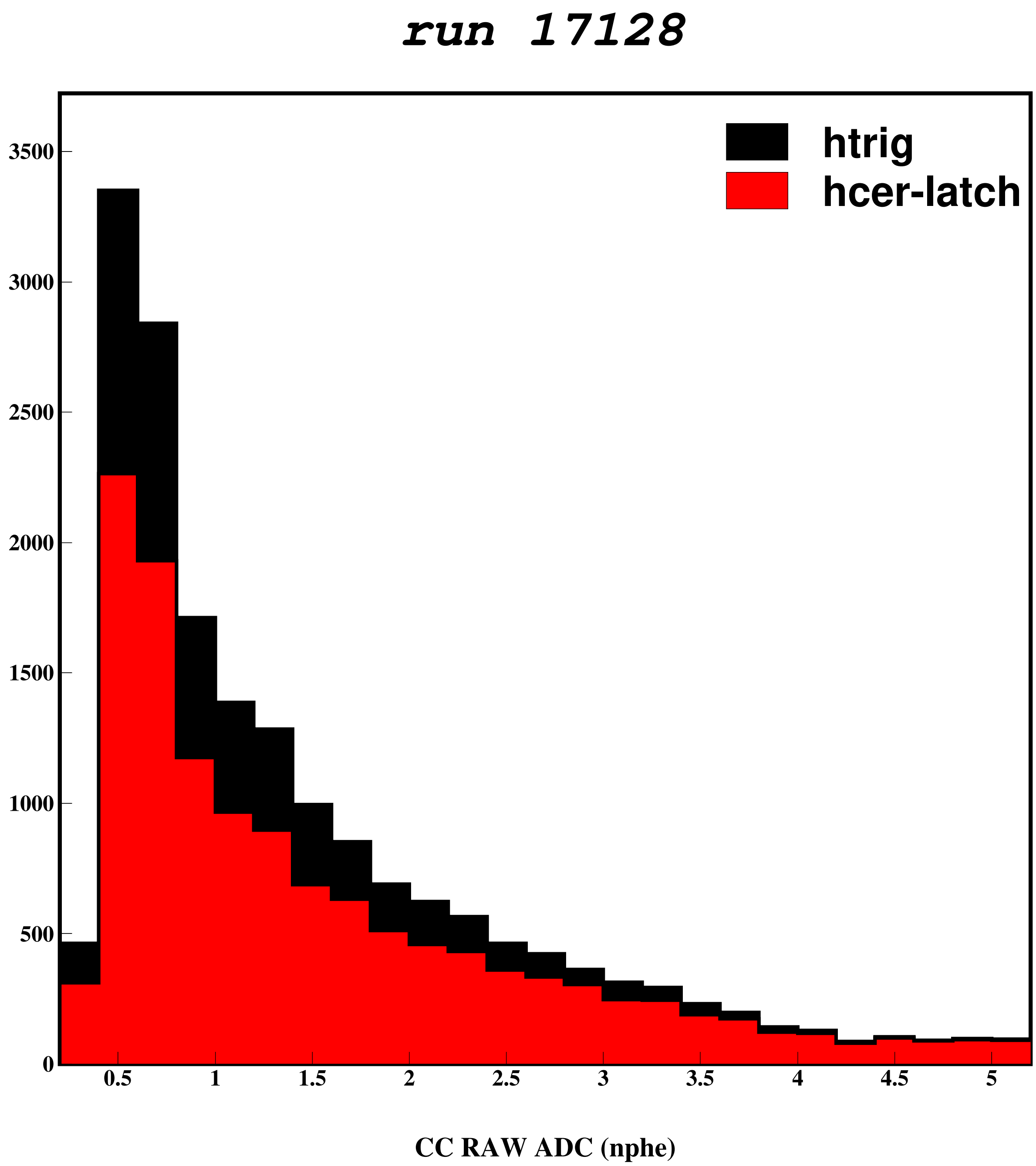
run 16960
htrig
hcer-latch
0
100
200
300
400
500
600
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

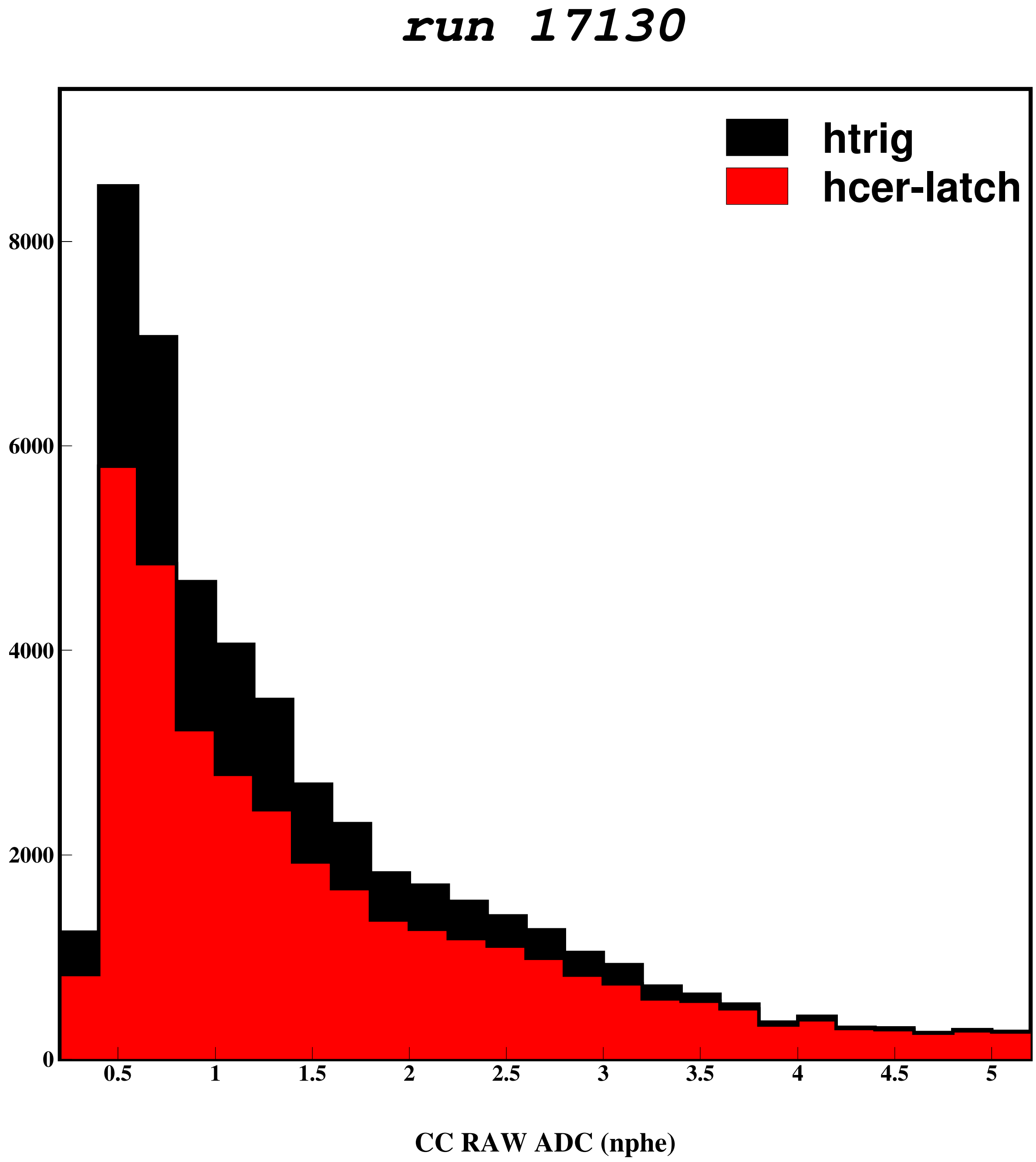

run 16961
htrig
hcer-latch
6000
5000
4000
3000
2000
1000
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)


run 16970
htrig
hcer-latch
0
200
400
600
800
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

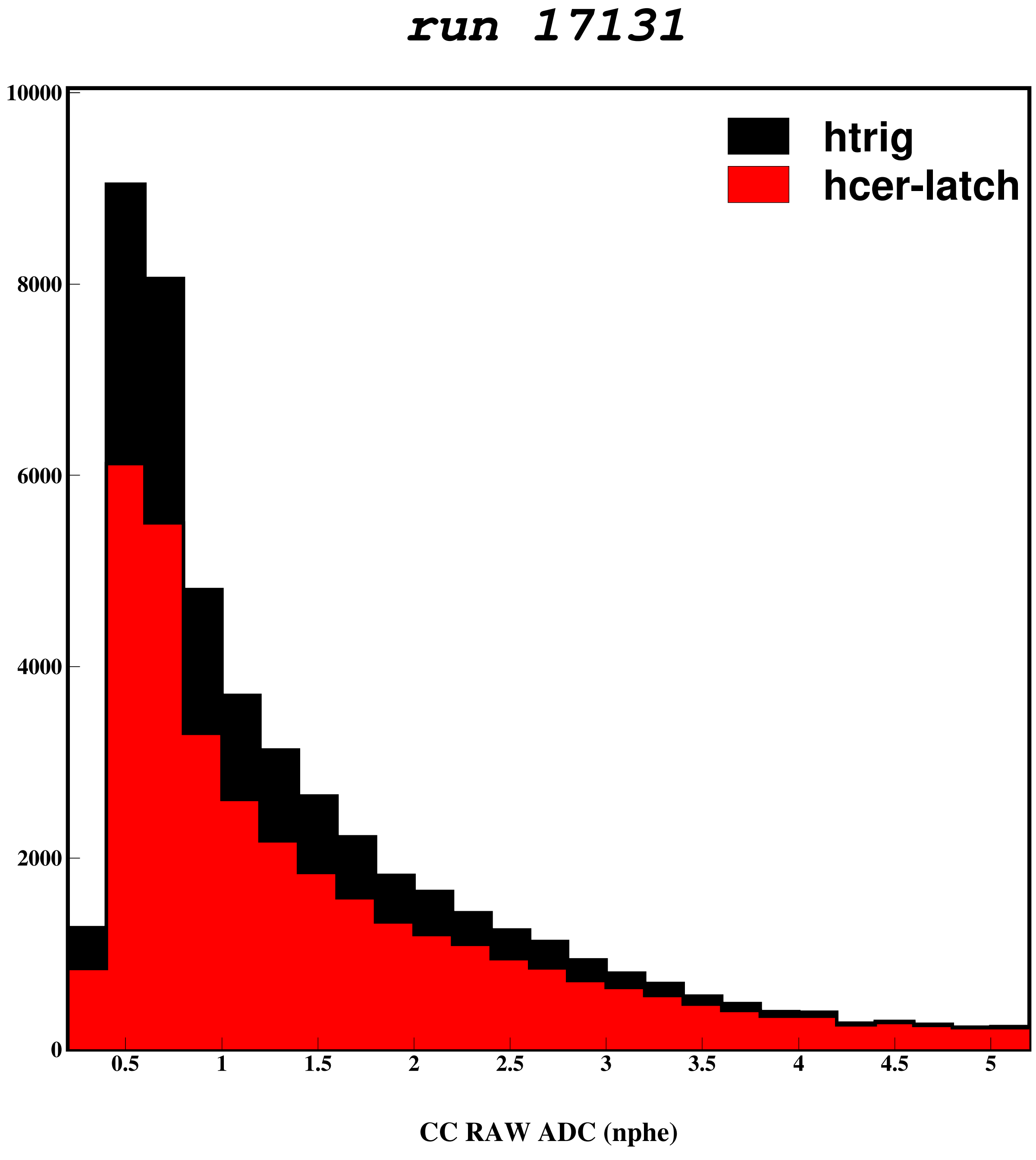

run 17124
htrig
hcer-latch
0
250
500
750
1000
1250
1500
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

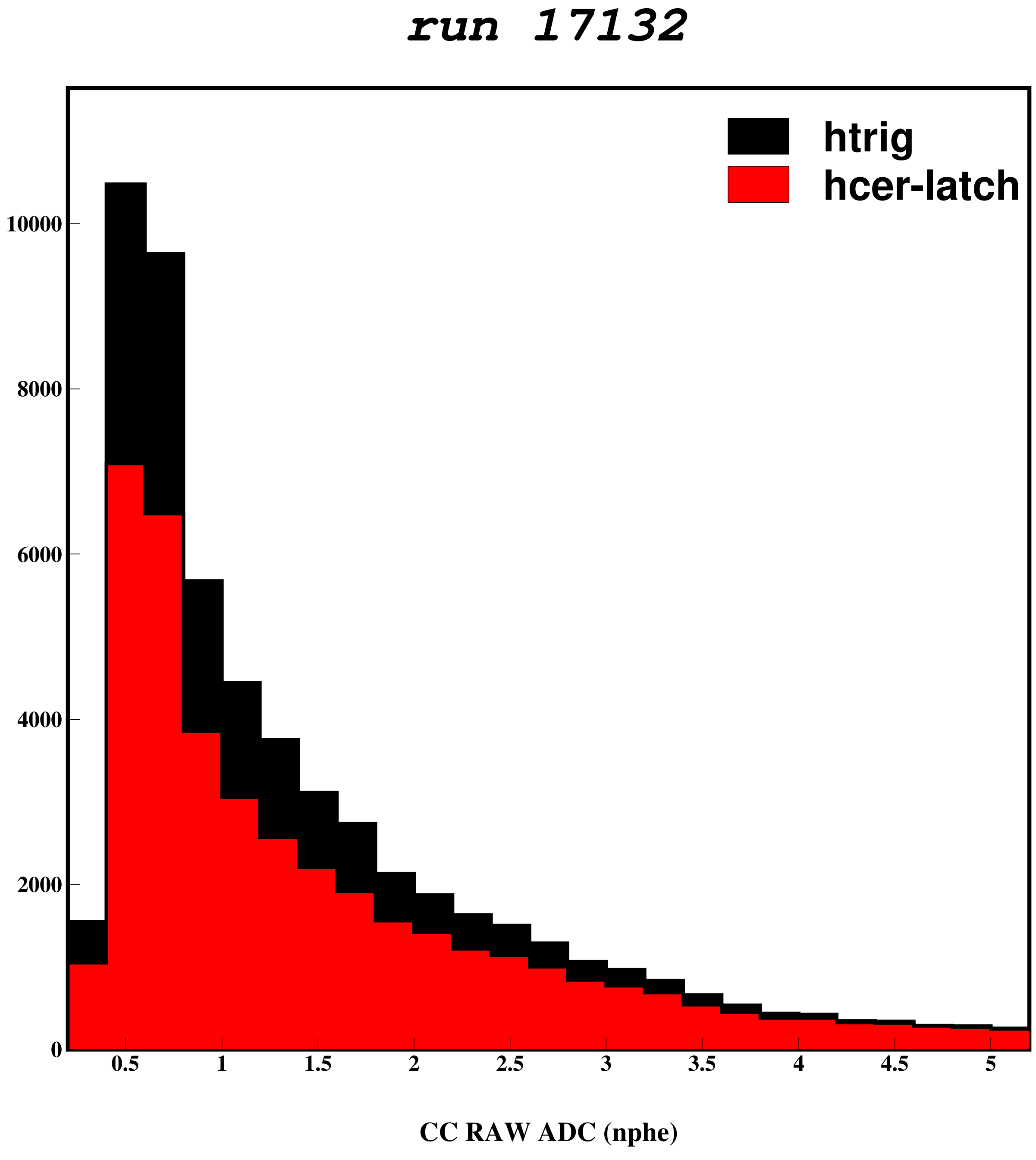

run 17127
htrig
hcer-latch
0
1000
2000
3000
4000
5000
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)


run 17128
htrig
hcer-latch
3500
3000
2500
2000
1500
1000
500
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

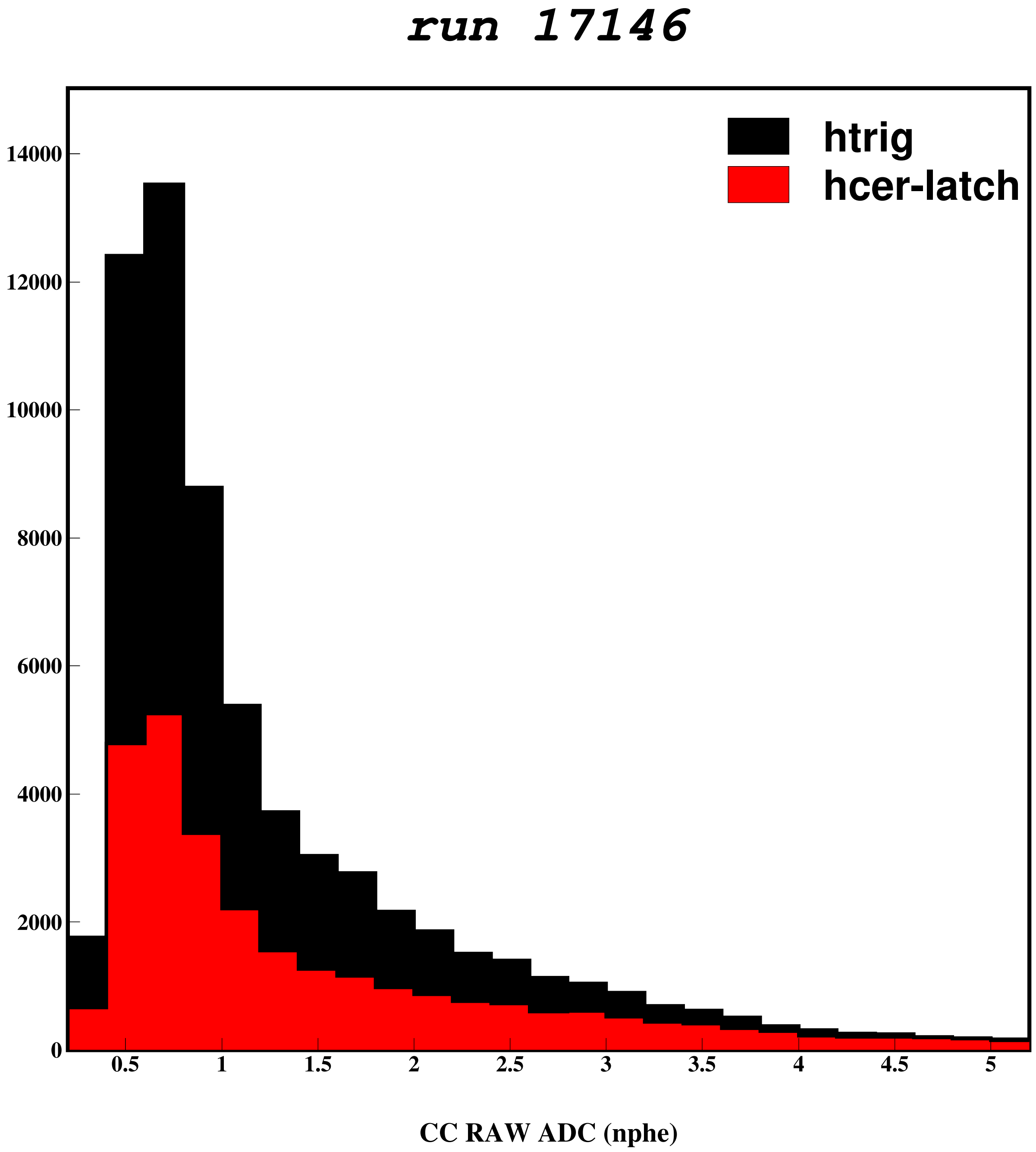


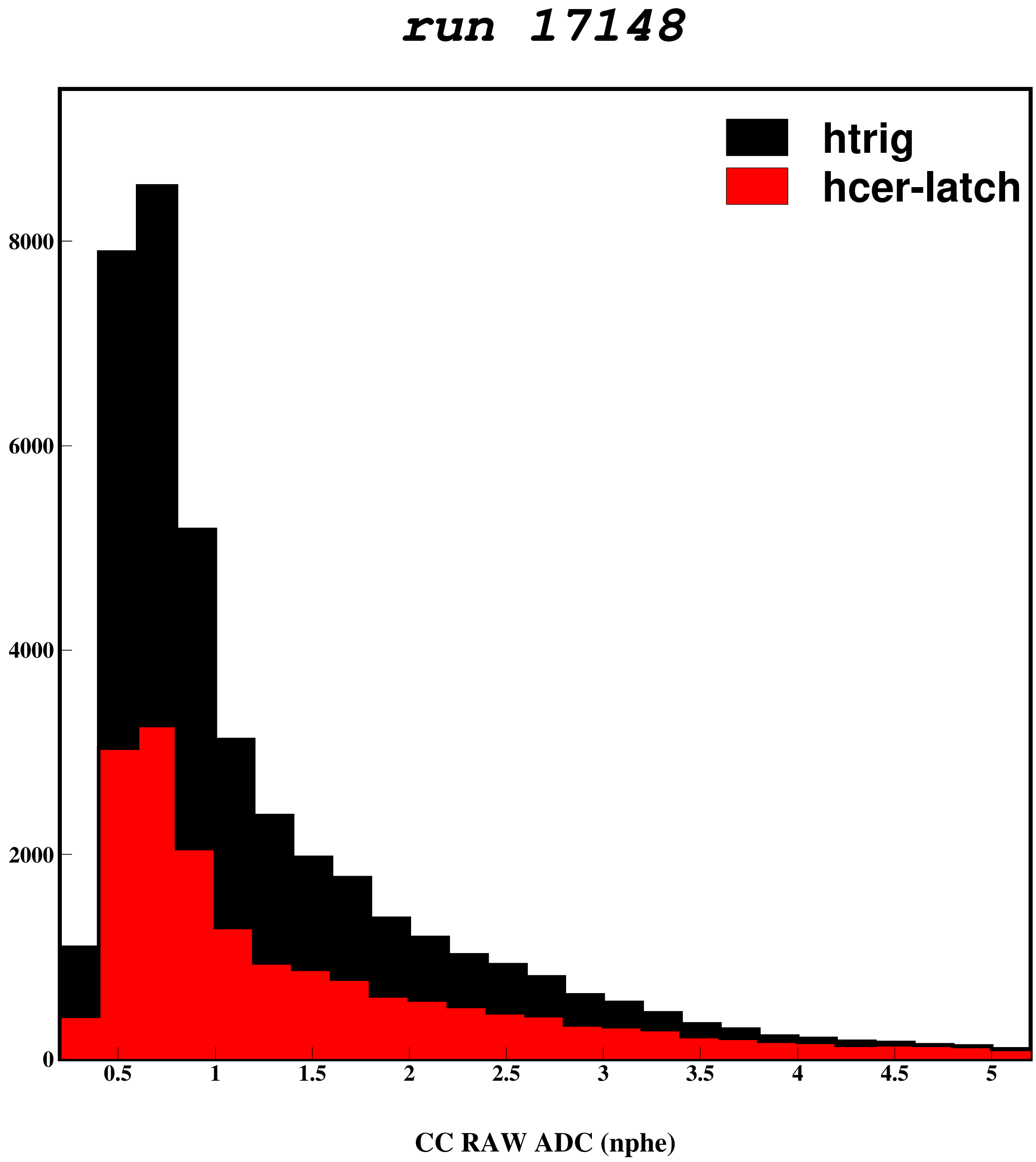
run 17130
htrig
hcer-latch
8000
6000
4000
2000
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)


run 17131
htrig
hcer-latch
10000
8000
6000
4000
2000
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

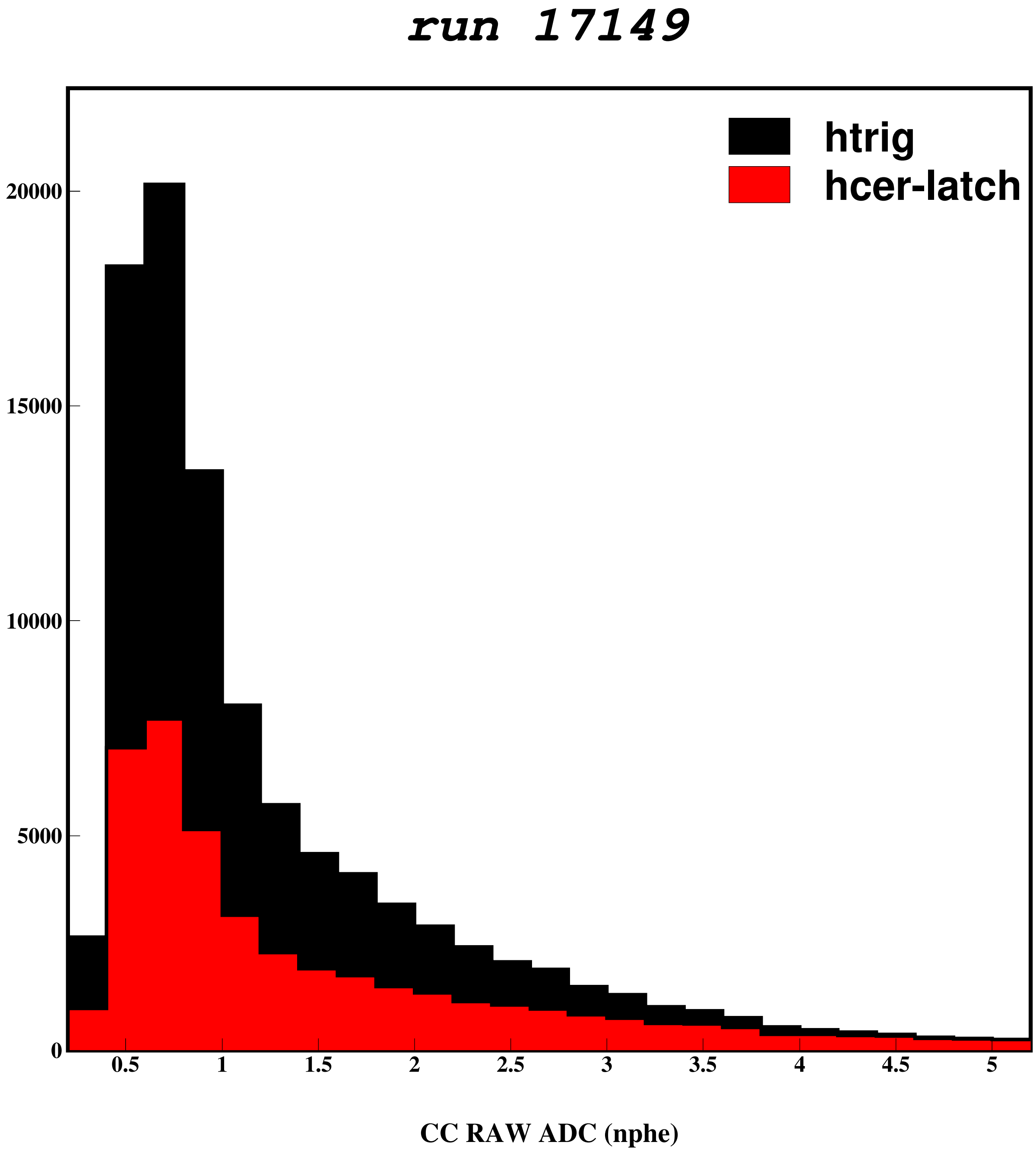


run 17132
htrig
hcer-latch
10000
8000
6000
4000
2000
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

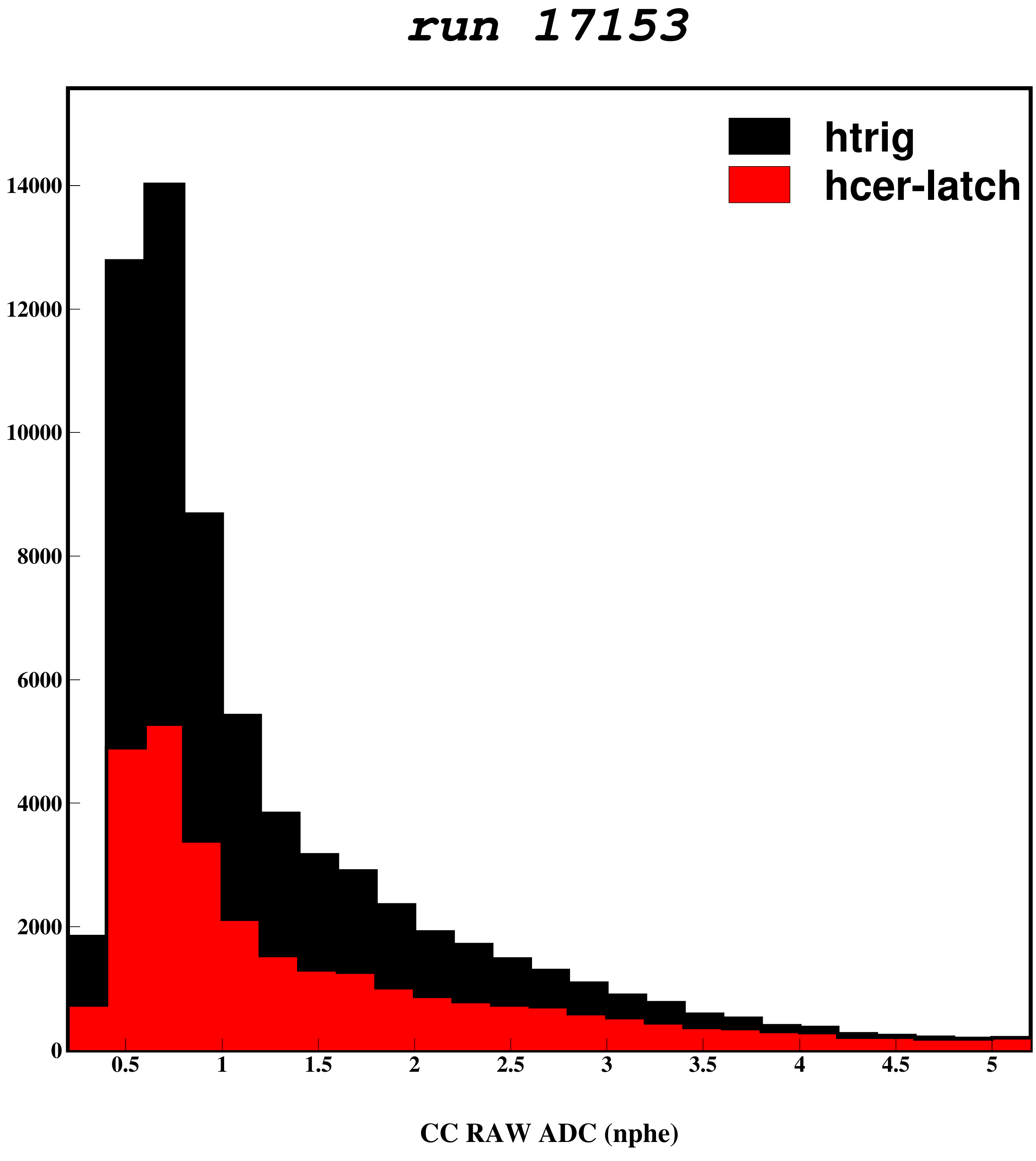

run 17146
htrig
hcer-latch
14000
12000
10000
8000
6000
4000
2000
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

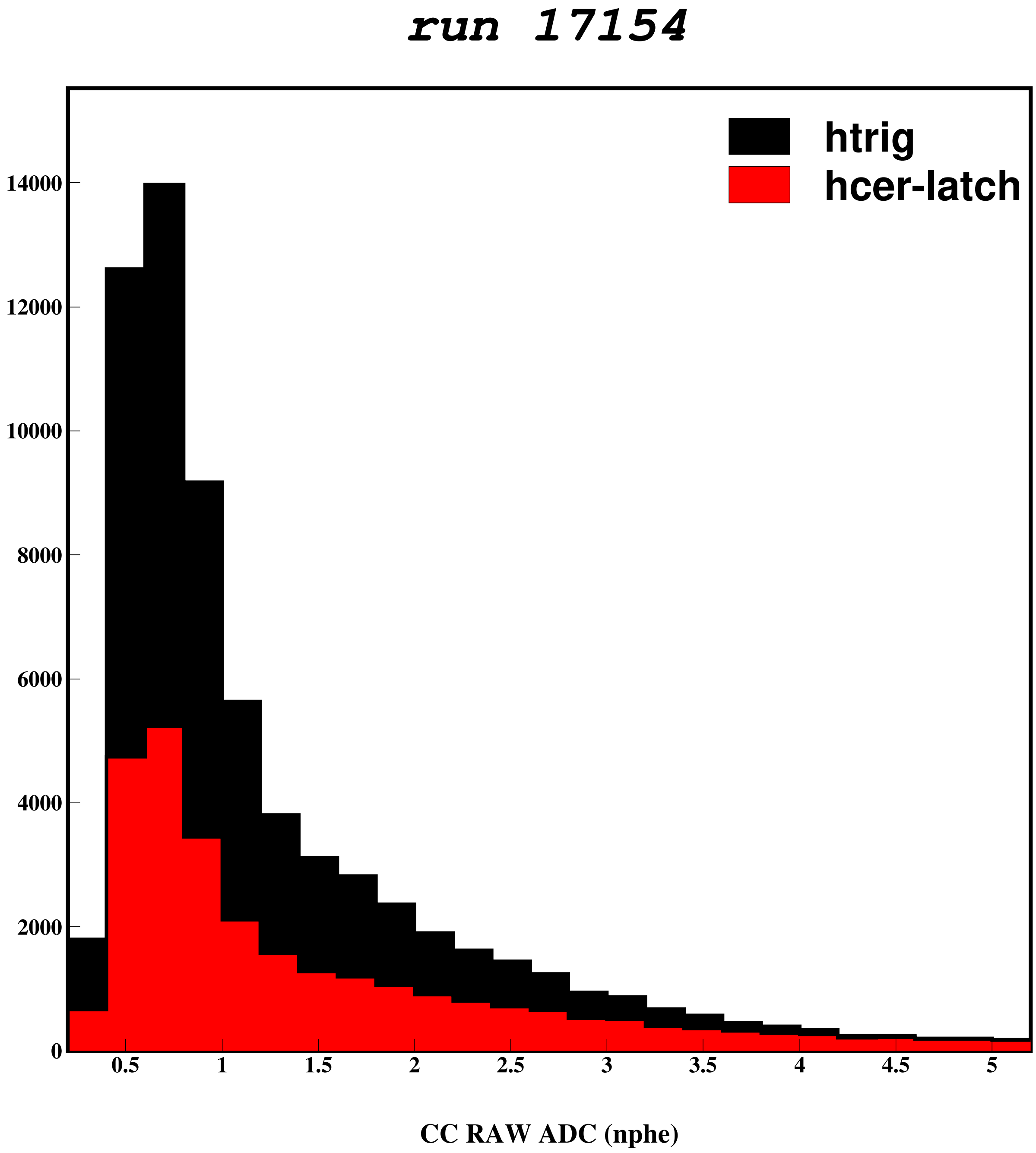


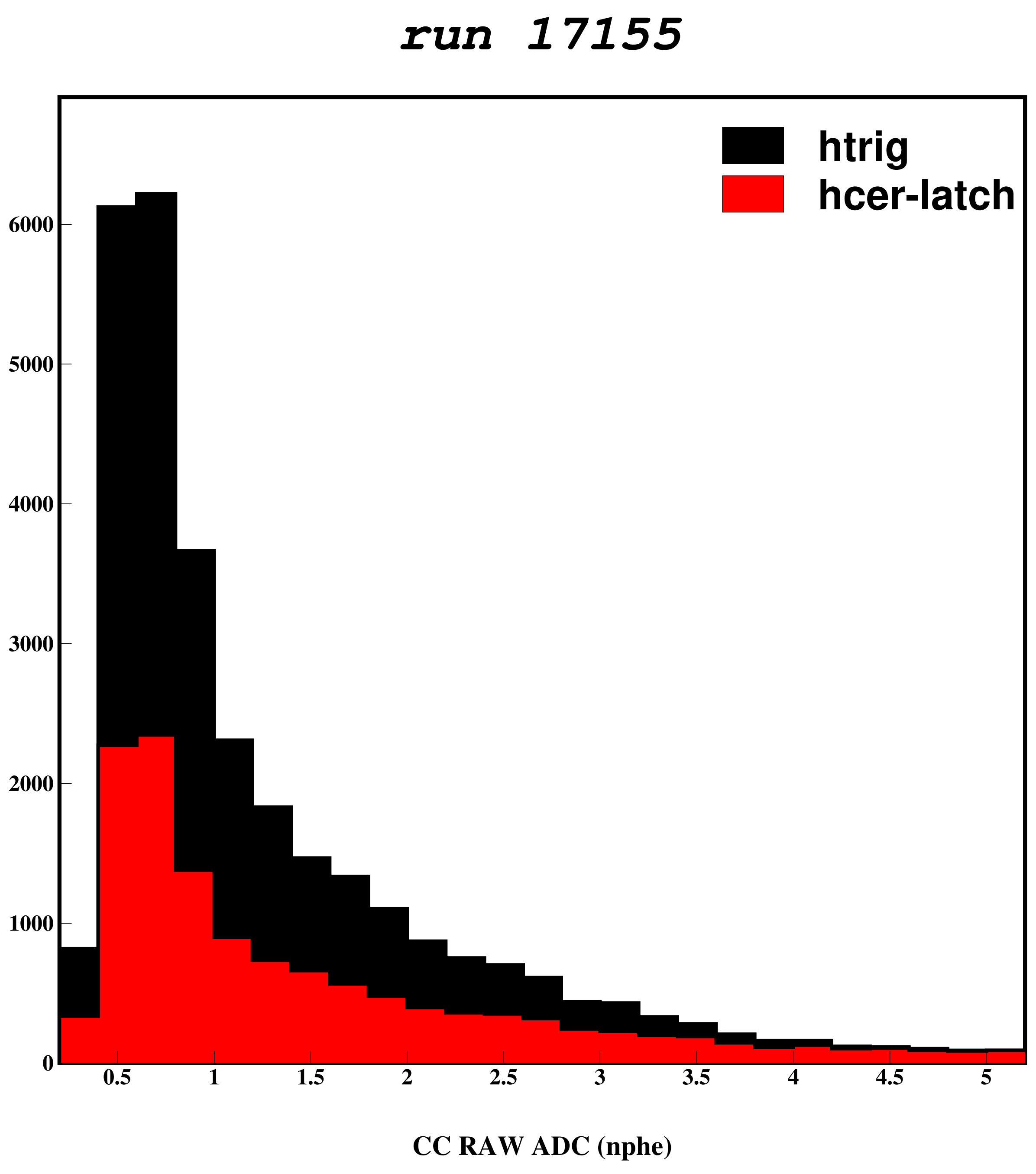
run 17148
htrig
hcer-latch
8000
6000
4000
2000
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

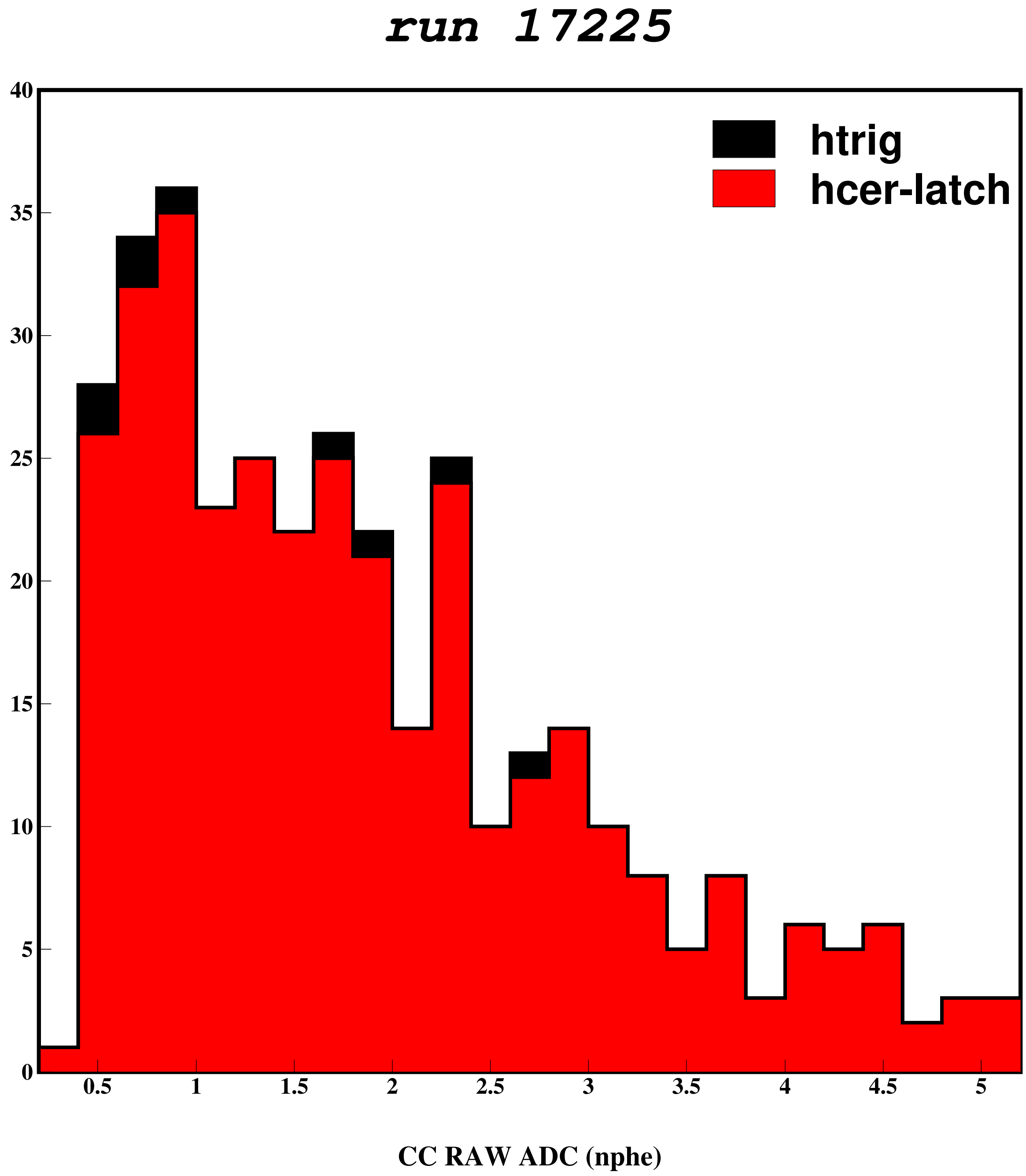


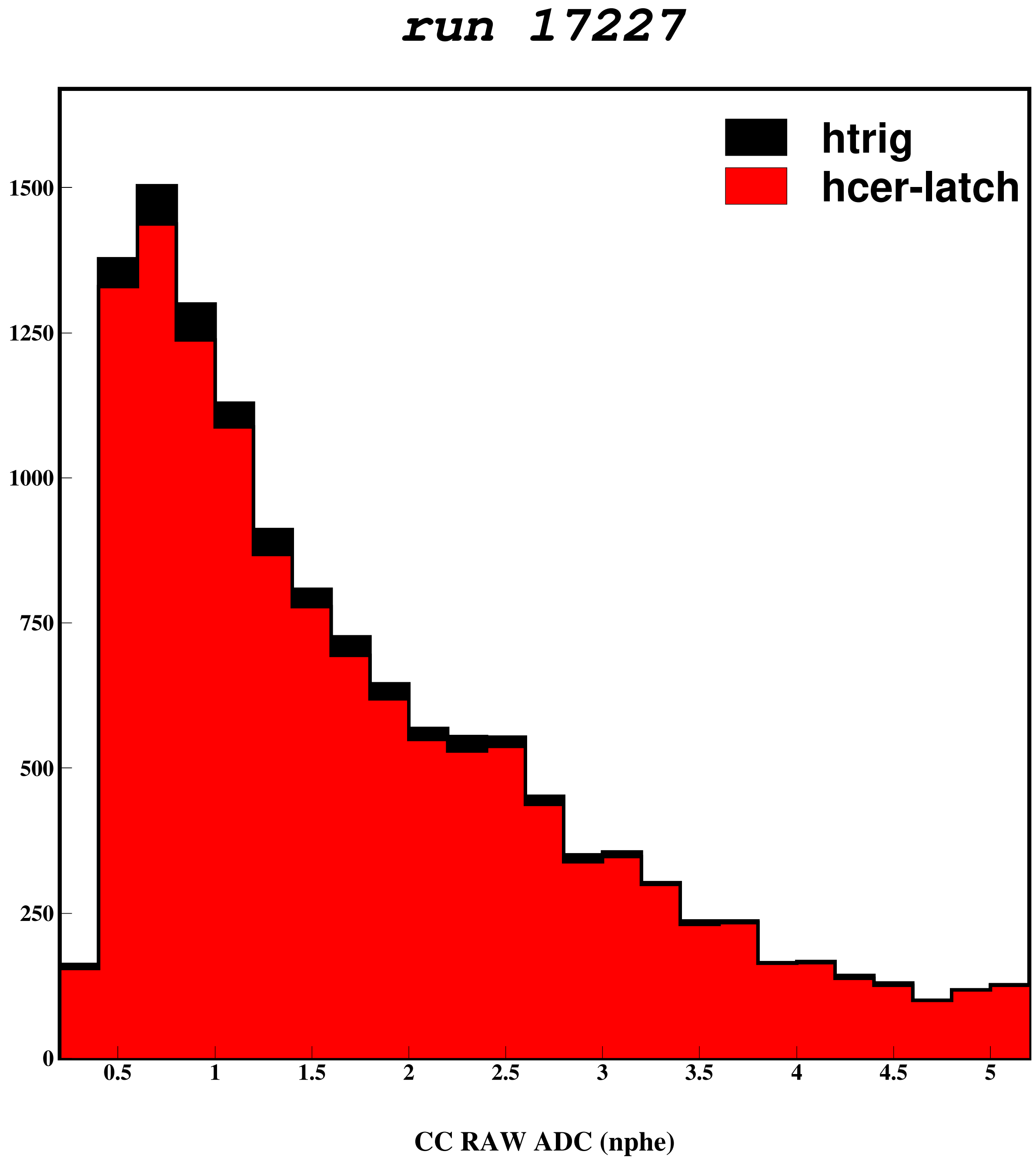
run 17149
htrig
hcer-latch
20000
15000
10000
5000
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)


run 17153
htrig
hcer-latch
14000
12000
10000
8000
6000
4000
2000
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

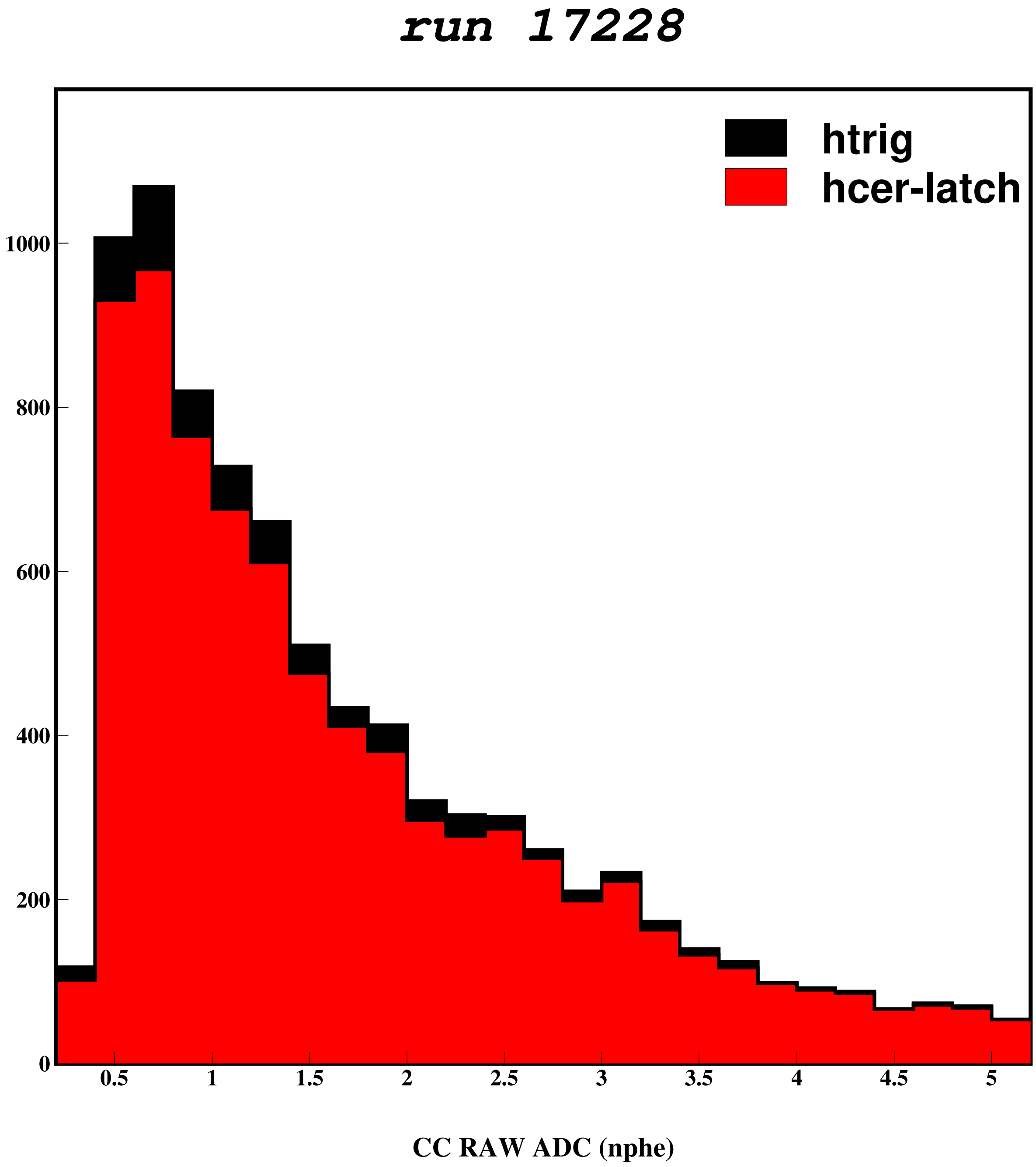

run 17154
htrig
hcer-latch
14000
12000
10000
8000
6000
4000
2000
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)


run 17155
htrig
hcer-latch
6000
5000
4000
3000
2000
1000
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

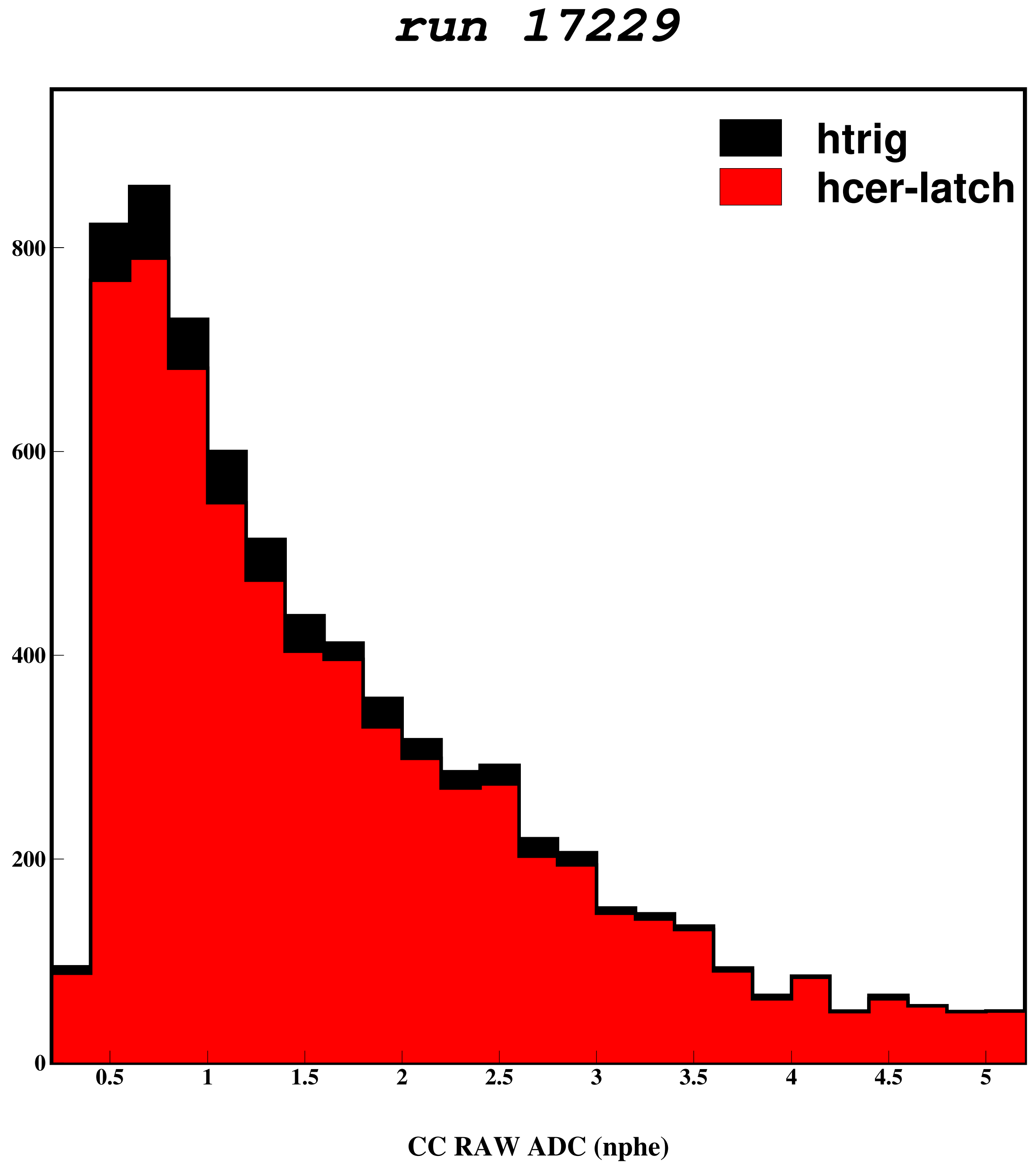

run 17225
htrig
hcer-latch
40
35
30
25
20
15
10
5
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

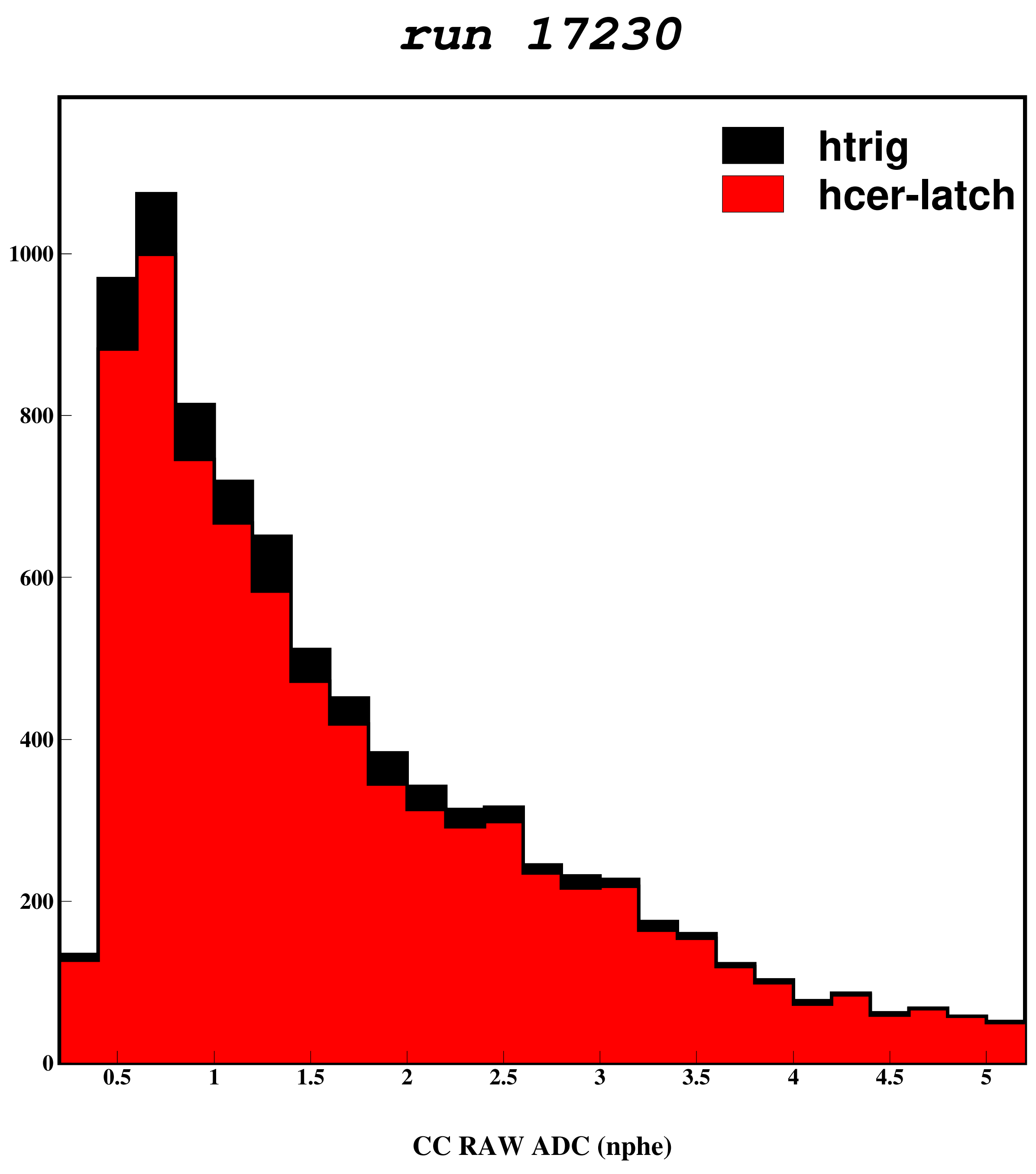


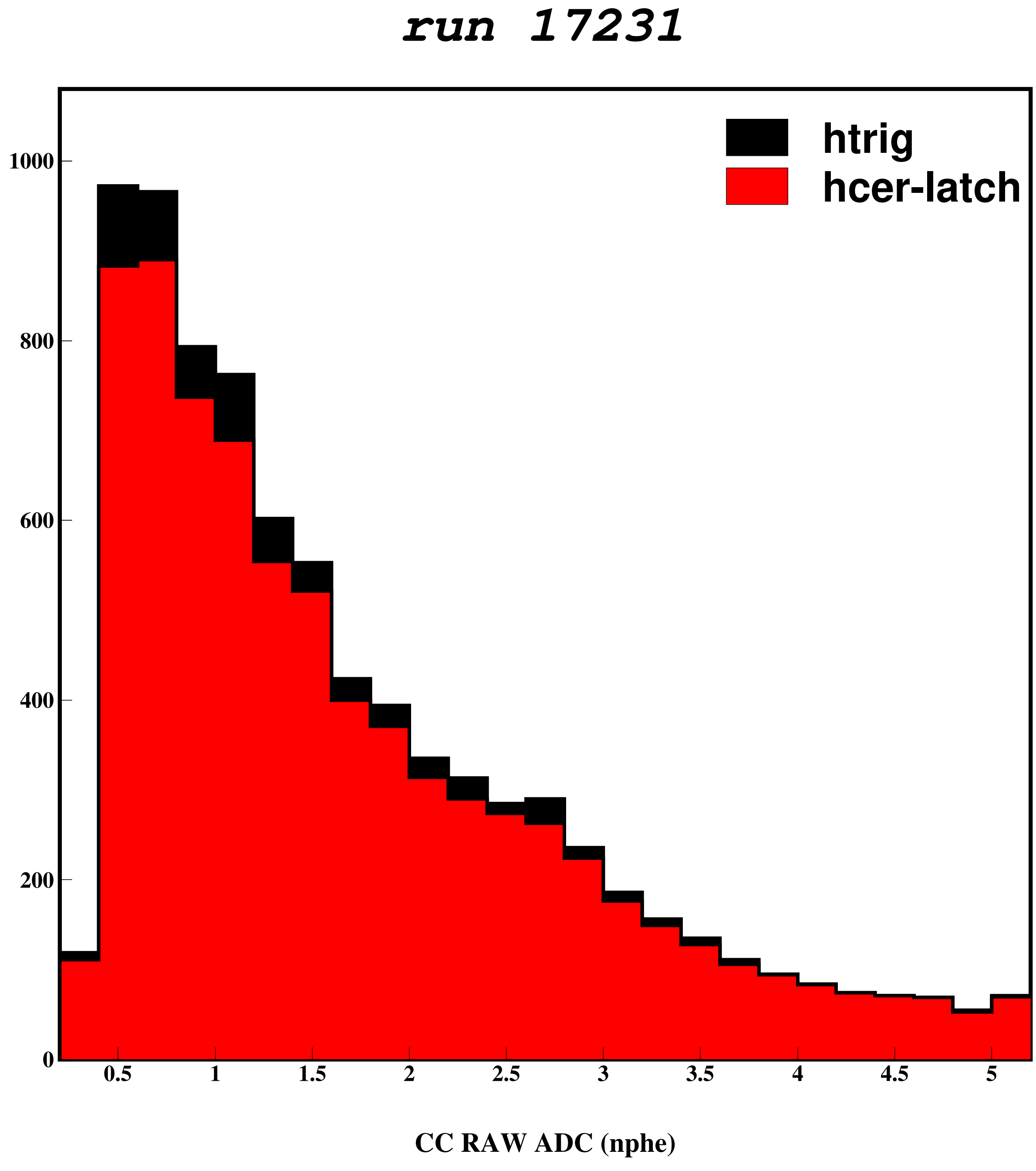
run 17227
htrig
hcer-latch
1500
1250
1000
750
500
250
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

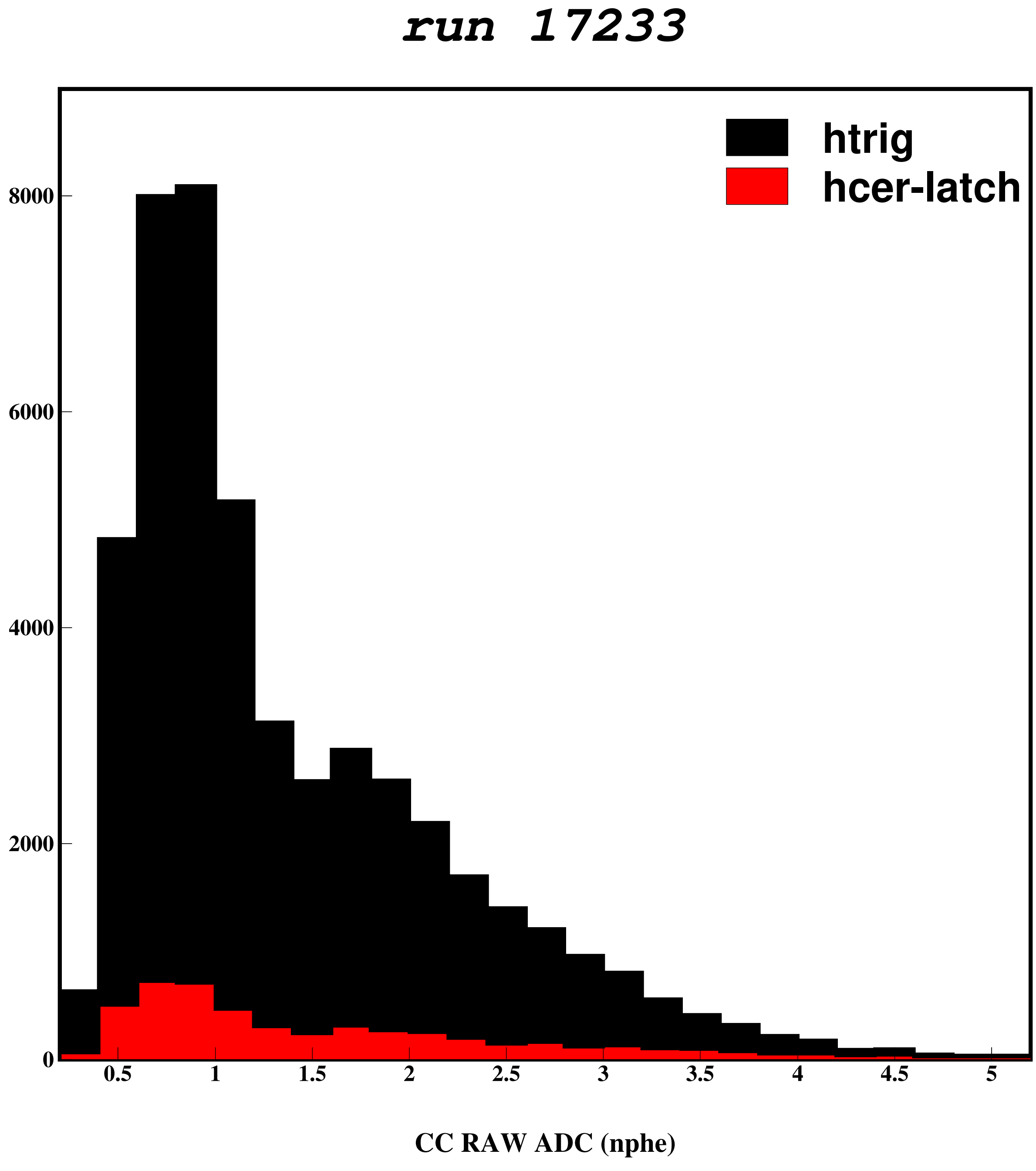


run 17228
htrig
hcer-latch
1000
800
600
400
200
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

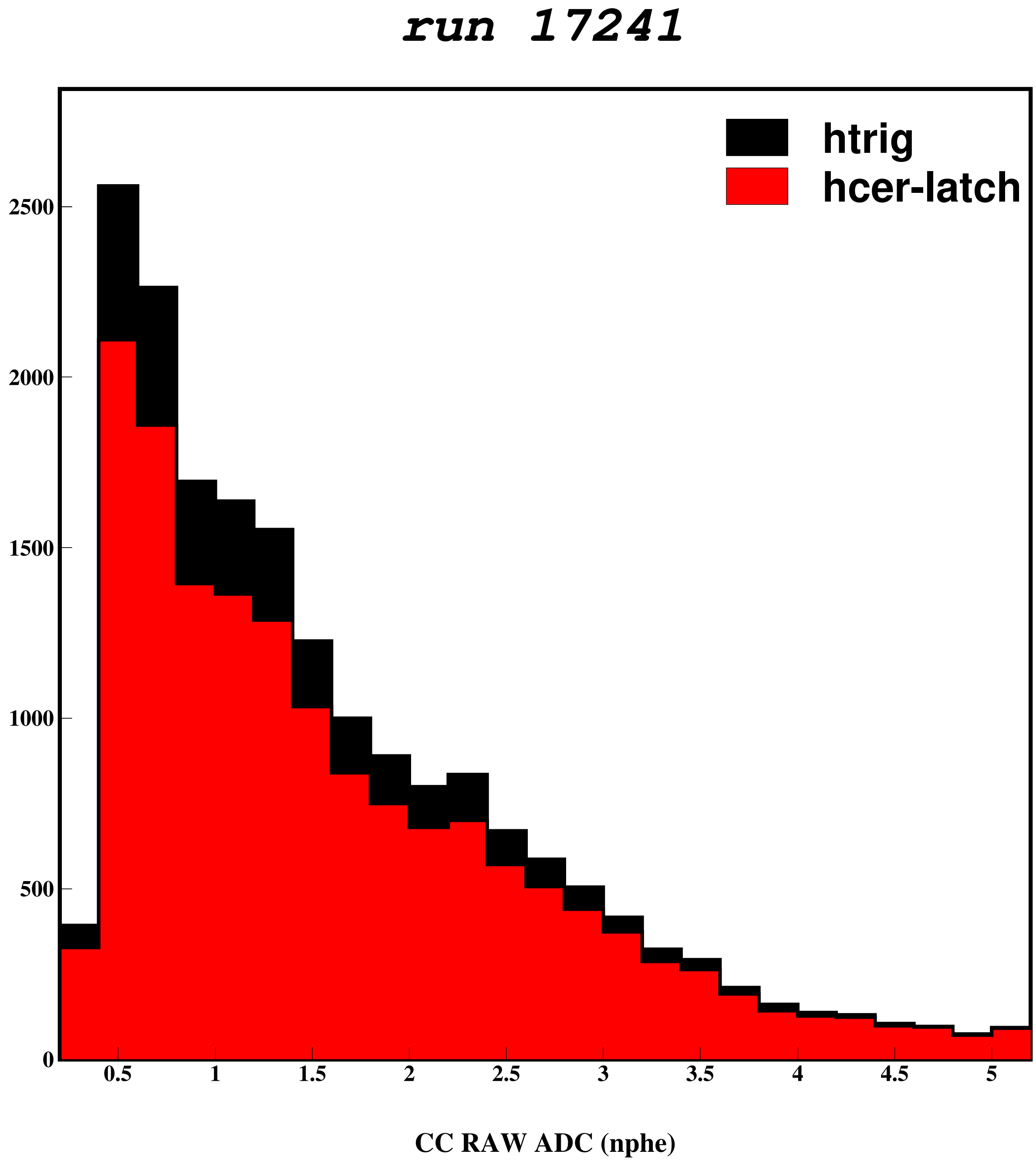

run 17229
htrig
hcer-latch
800
600
400
200
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)


run 17230
htrig
hcer-latch
0
200
400
600
800
1000
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

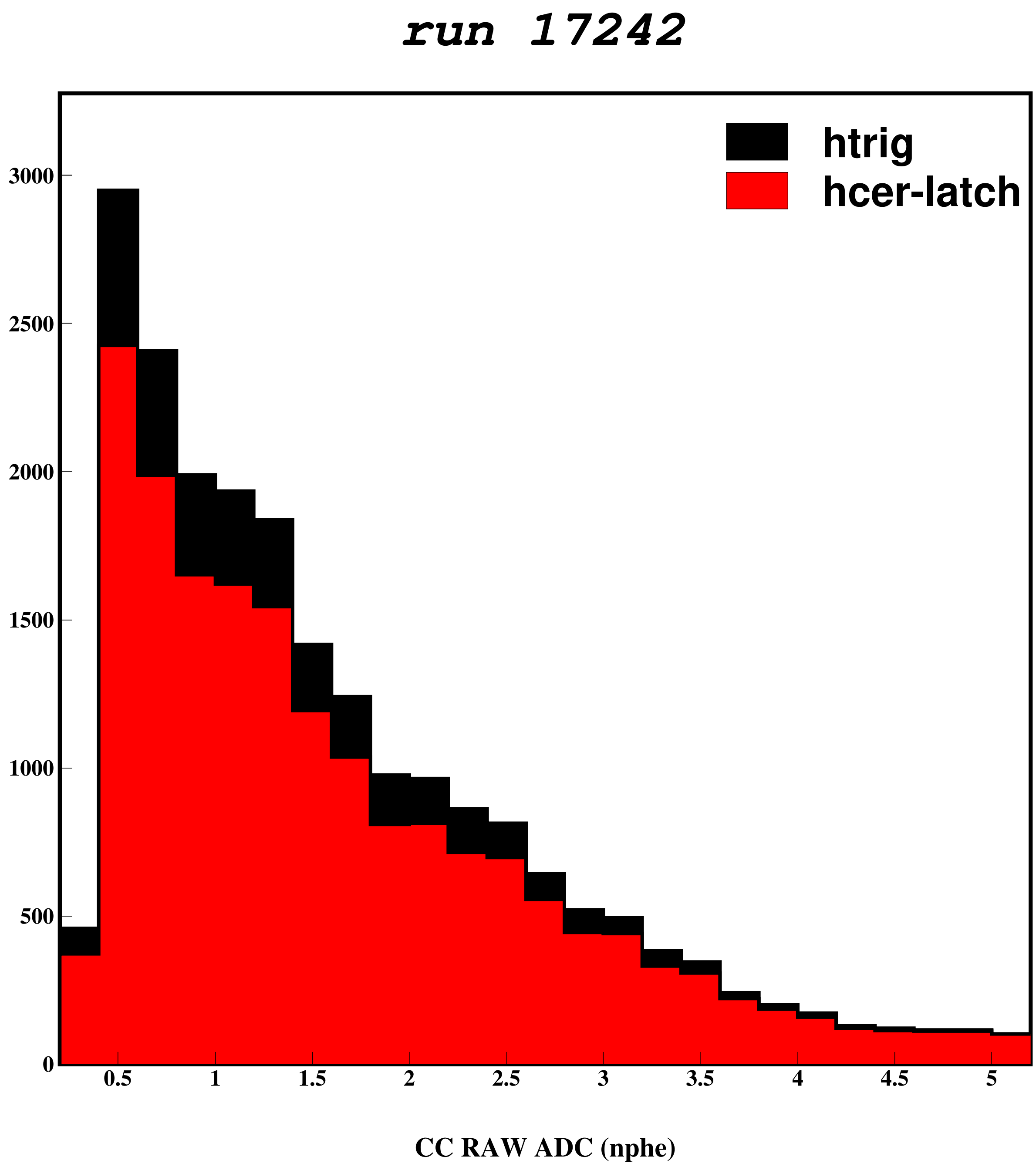

run 17231
htrig
hcer-latch
1000
800
600
400
200
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

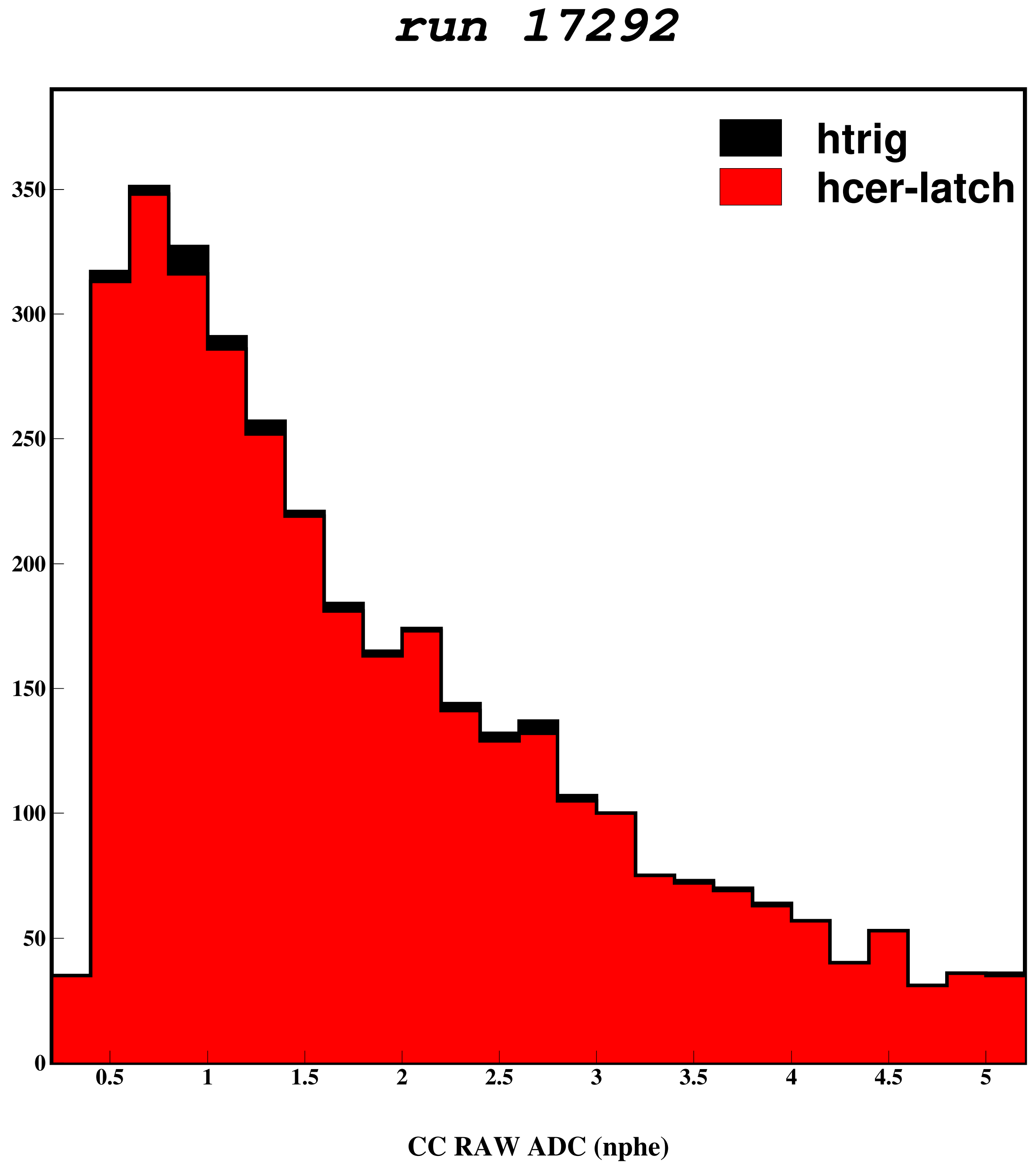


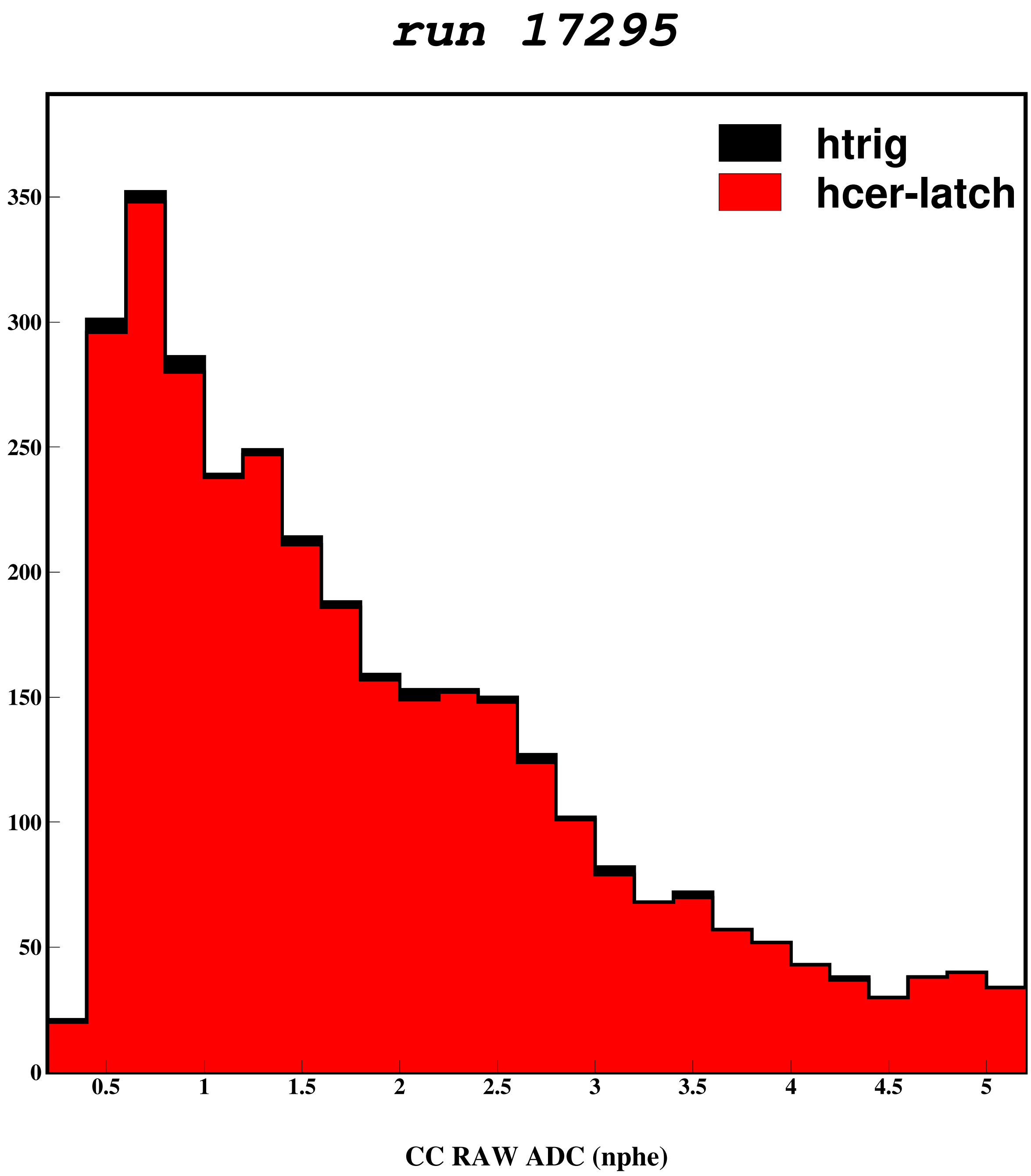
run 17233
htrig
hcer-latch
8000
6000
4000
2000
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

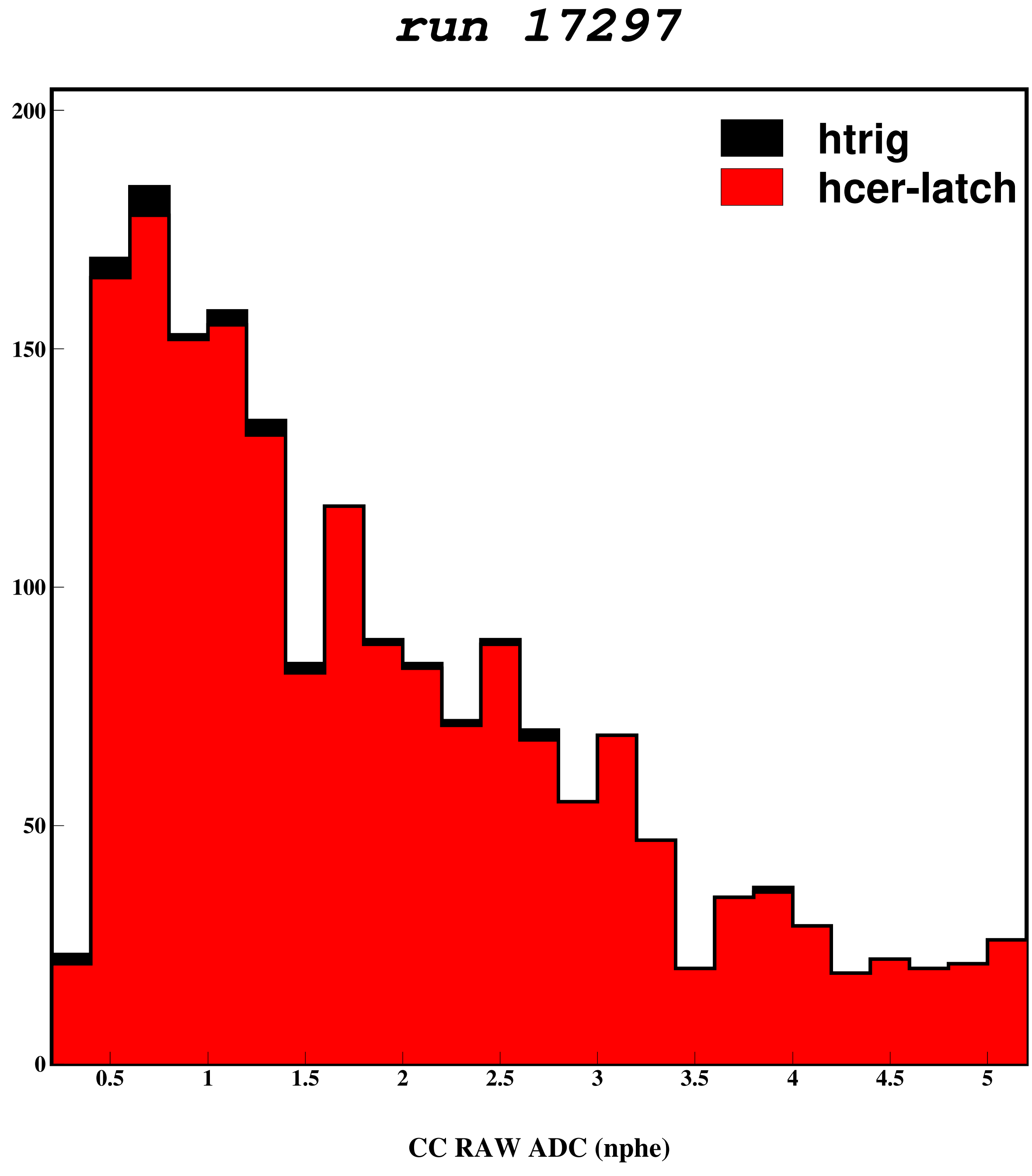


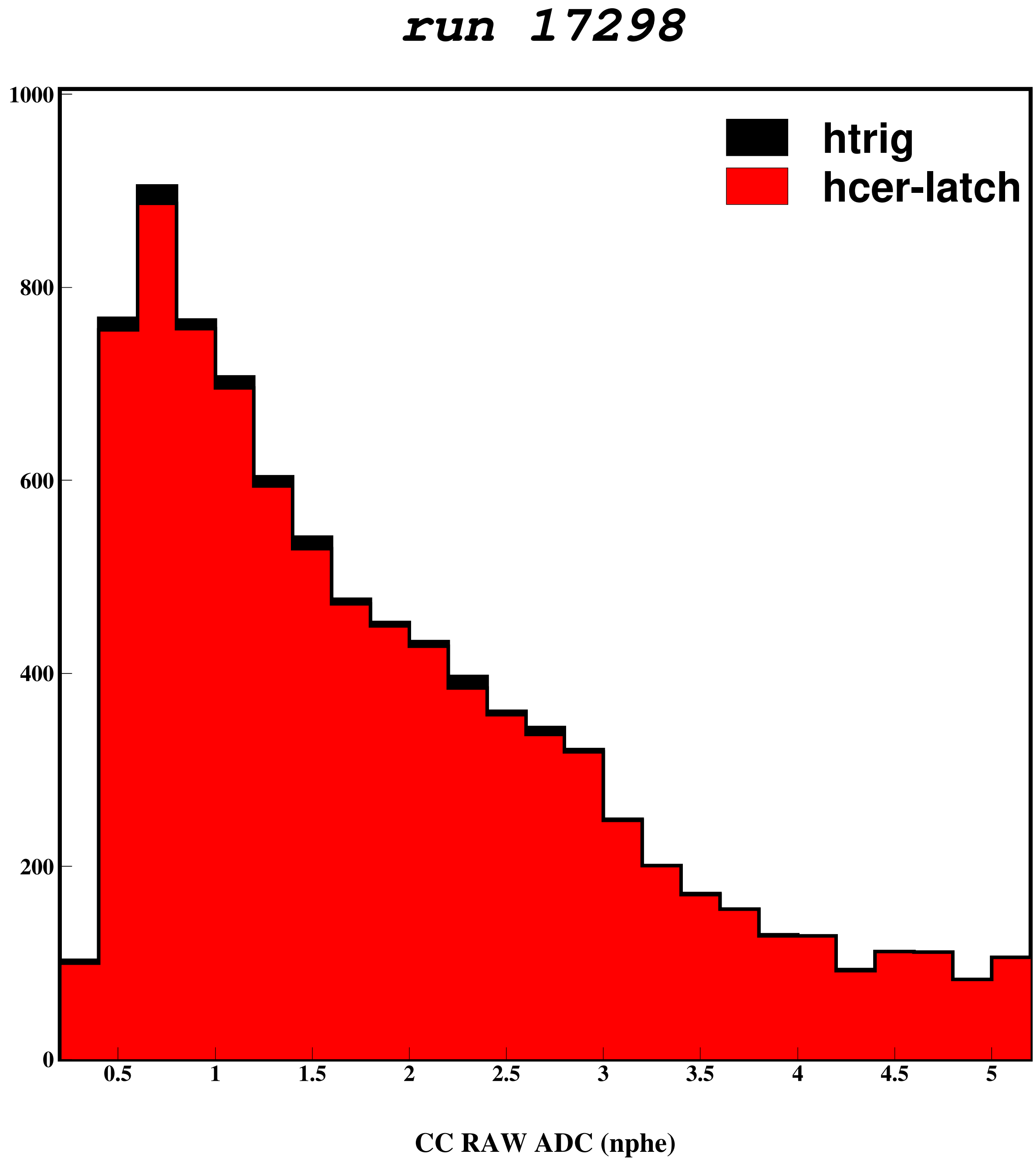
run 17241
htrig
hcer-latch
0
500
1000
1500
2000
2500
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

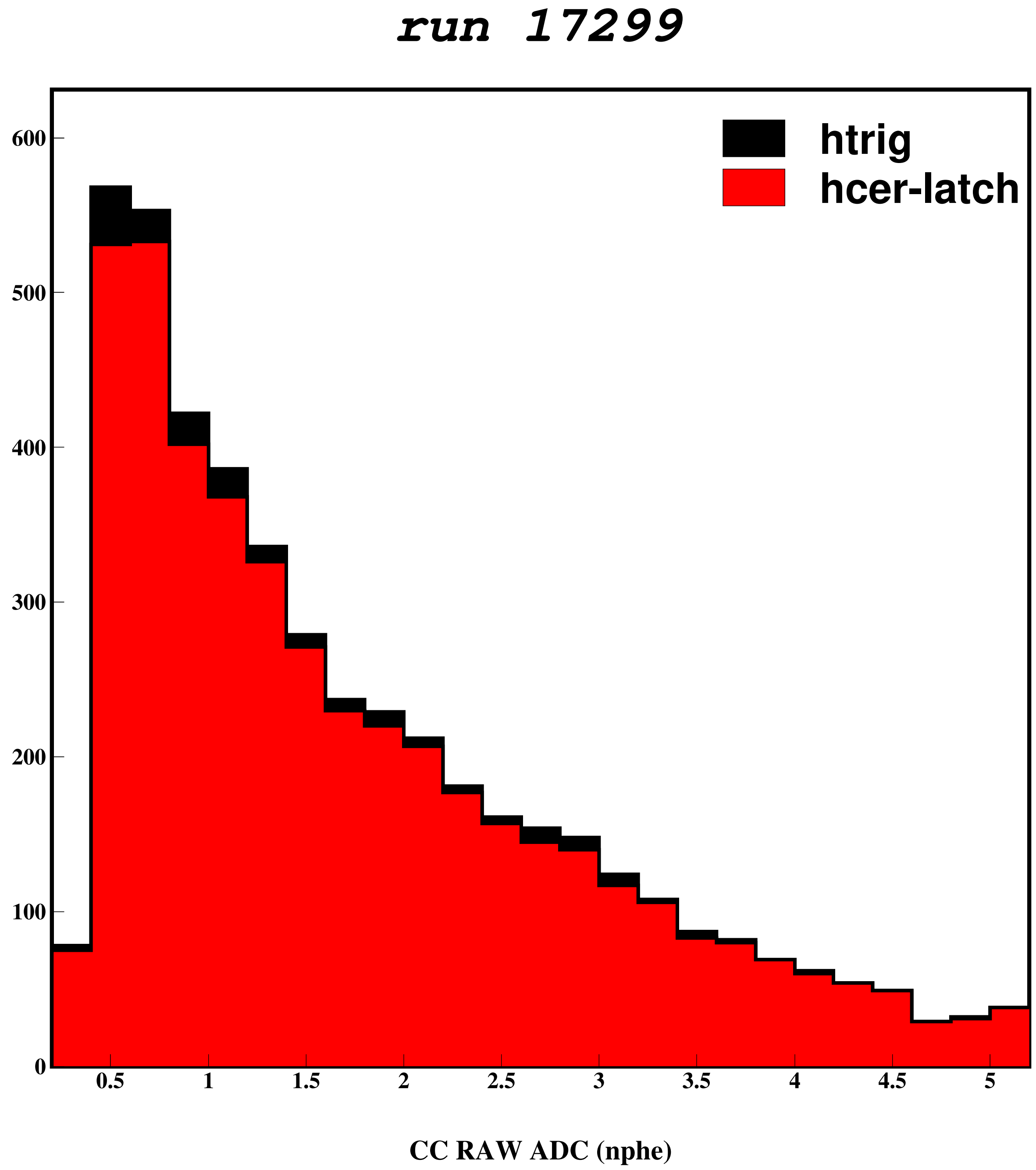

run 17242
htrig
hcer-latch
3000
2500
2000
1500
1000
500
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)


run 17292
htrig
hcer-latch
350
300
250
200
150
100
50
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

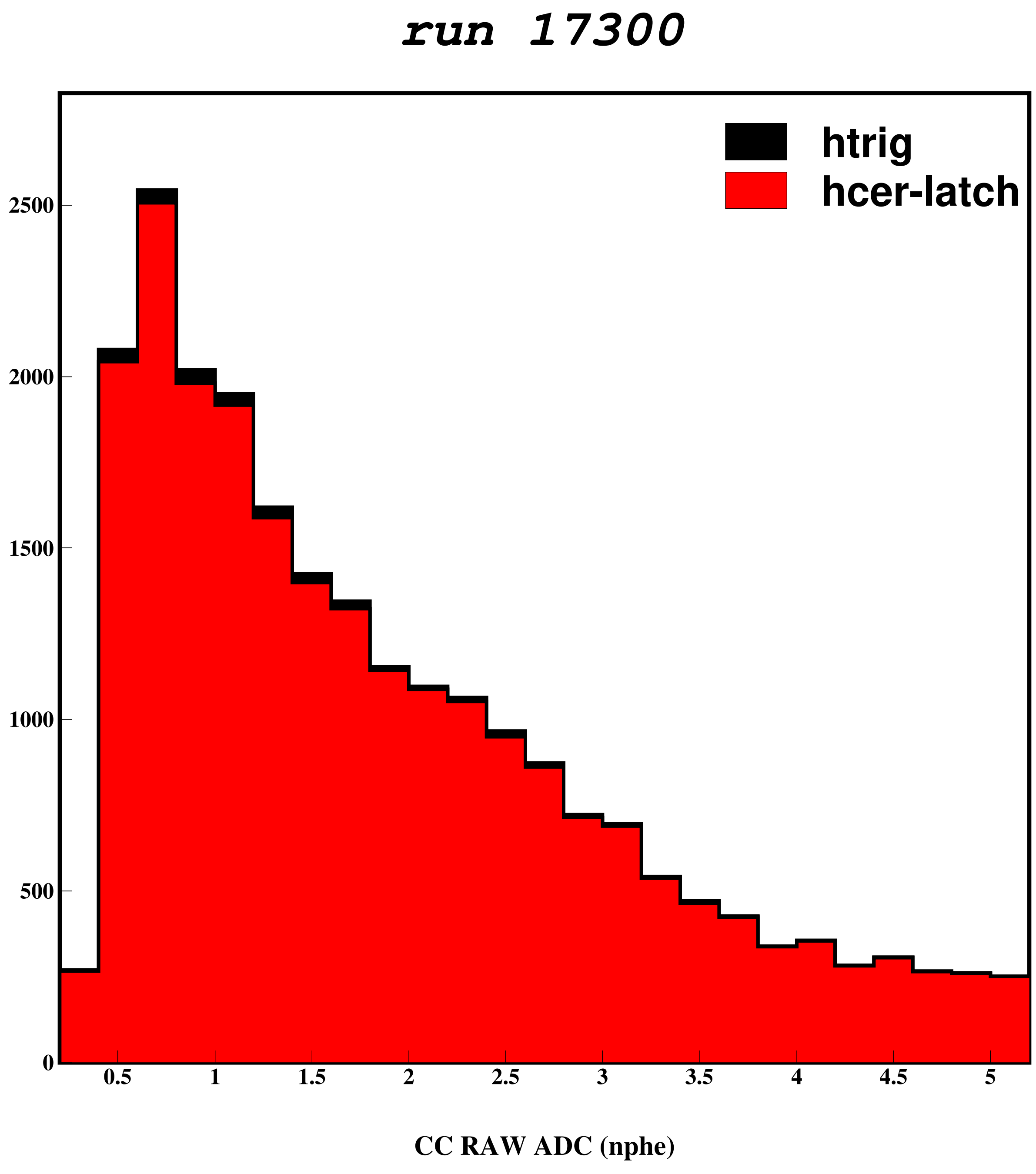

run 17295
htrig
hcer-latch
350
300
250
200
150
100
50
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

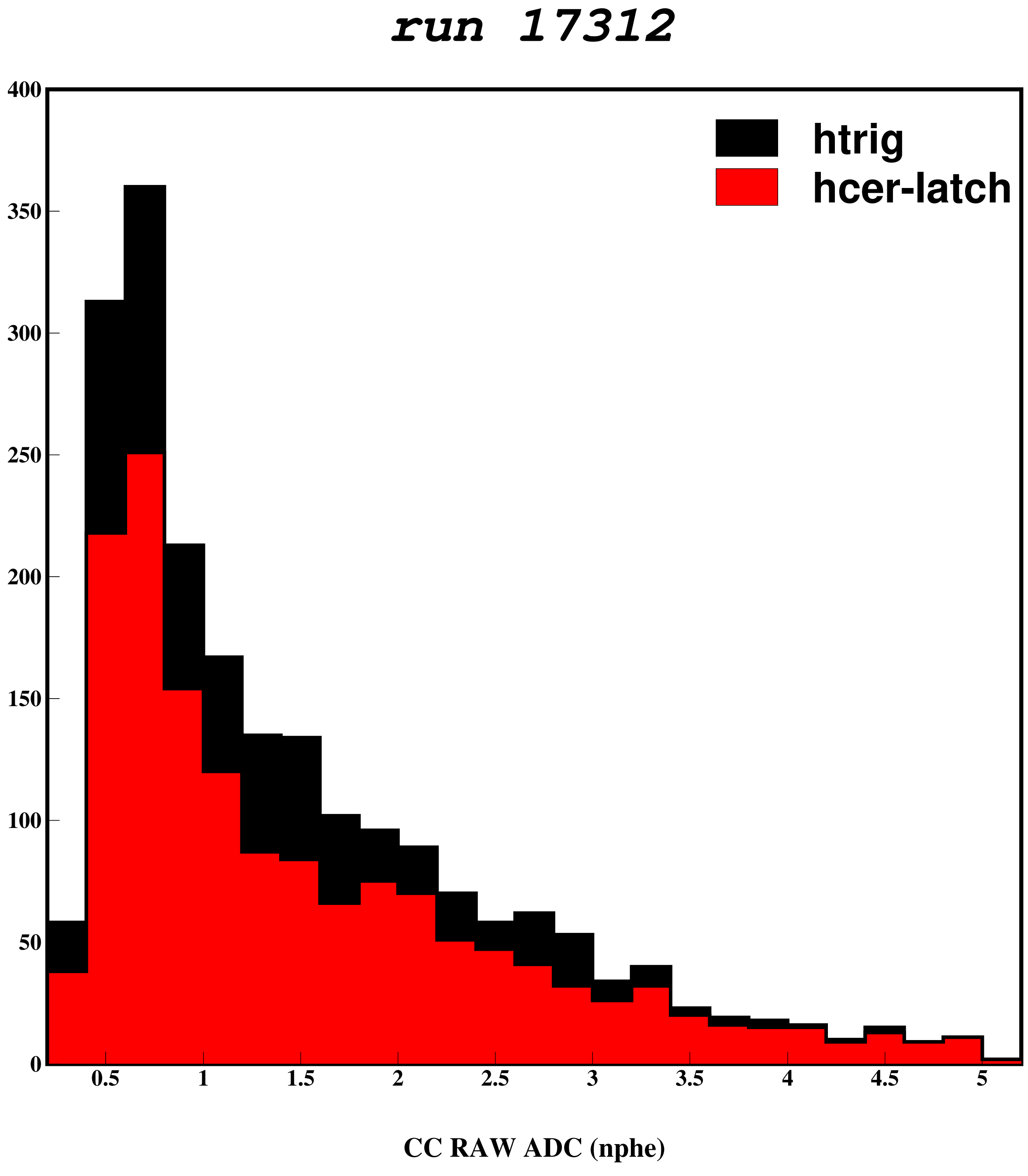

run 17297
htrig
hcer-latch
200
150
100
50
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

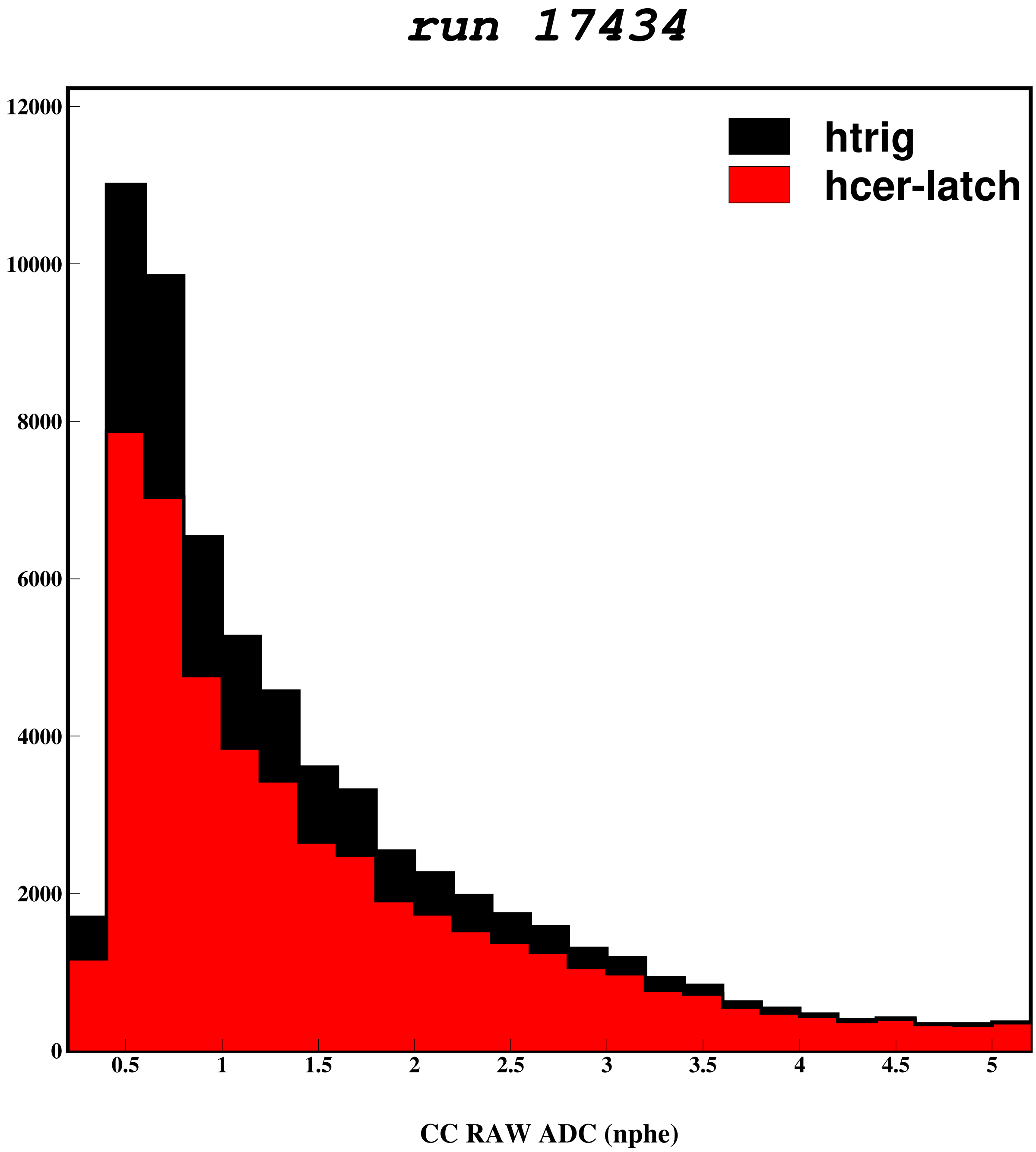

run 17298
htrig
hcer-latch
0
200
400
600
800
1000
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)



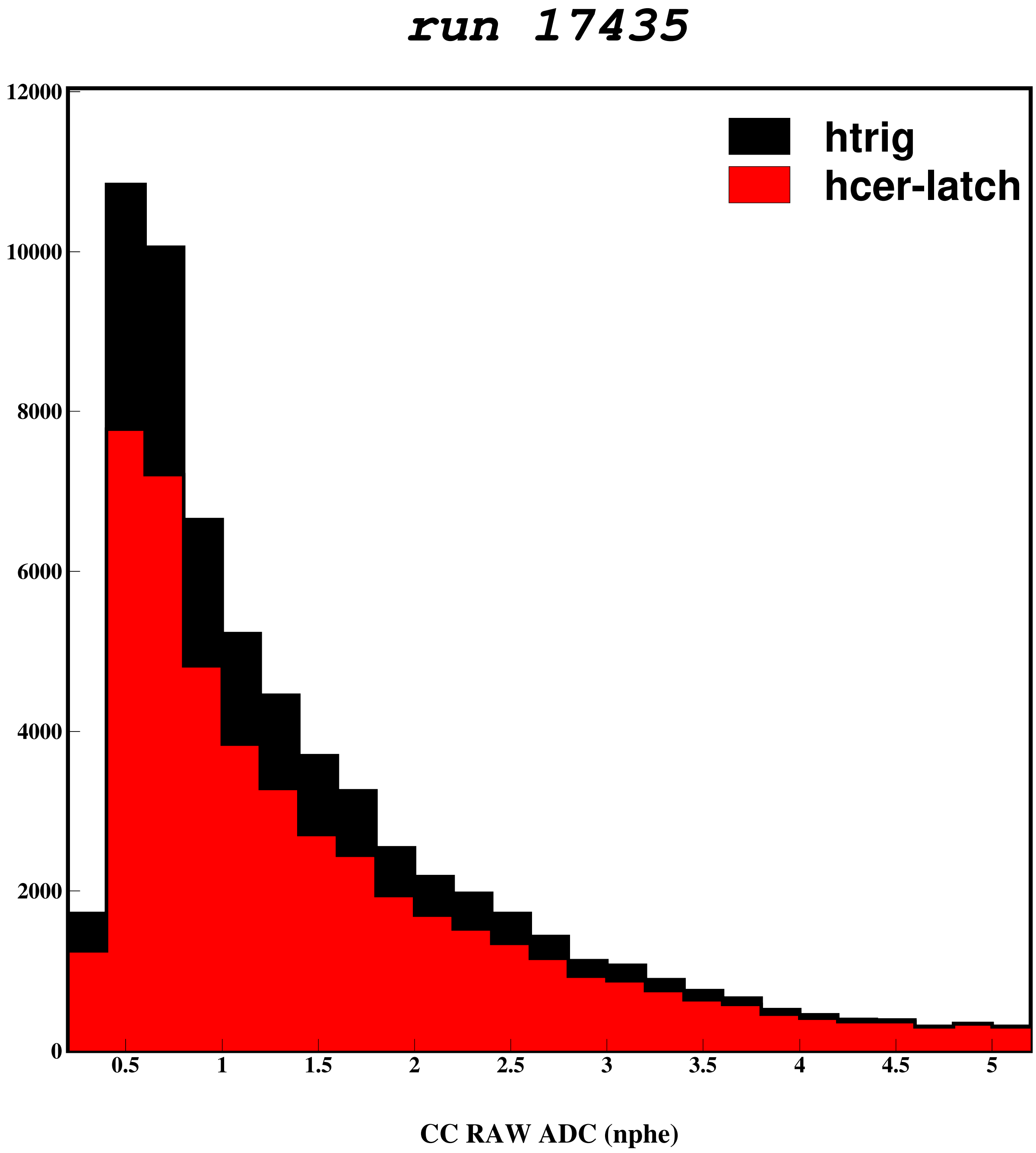
run 17299
htrig
hcer-latch
600
500
400
300
200
100
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

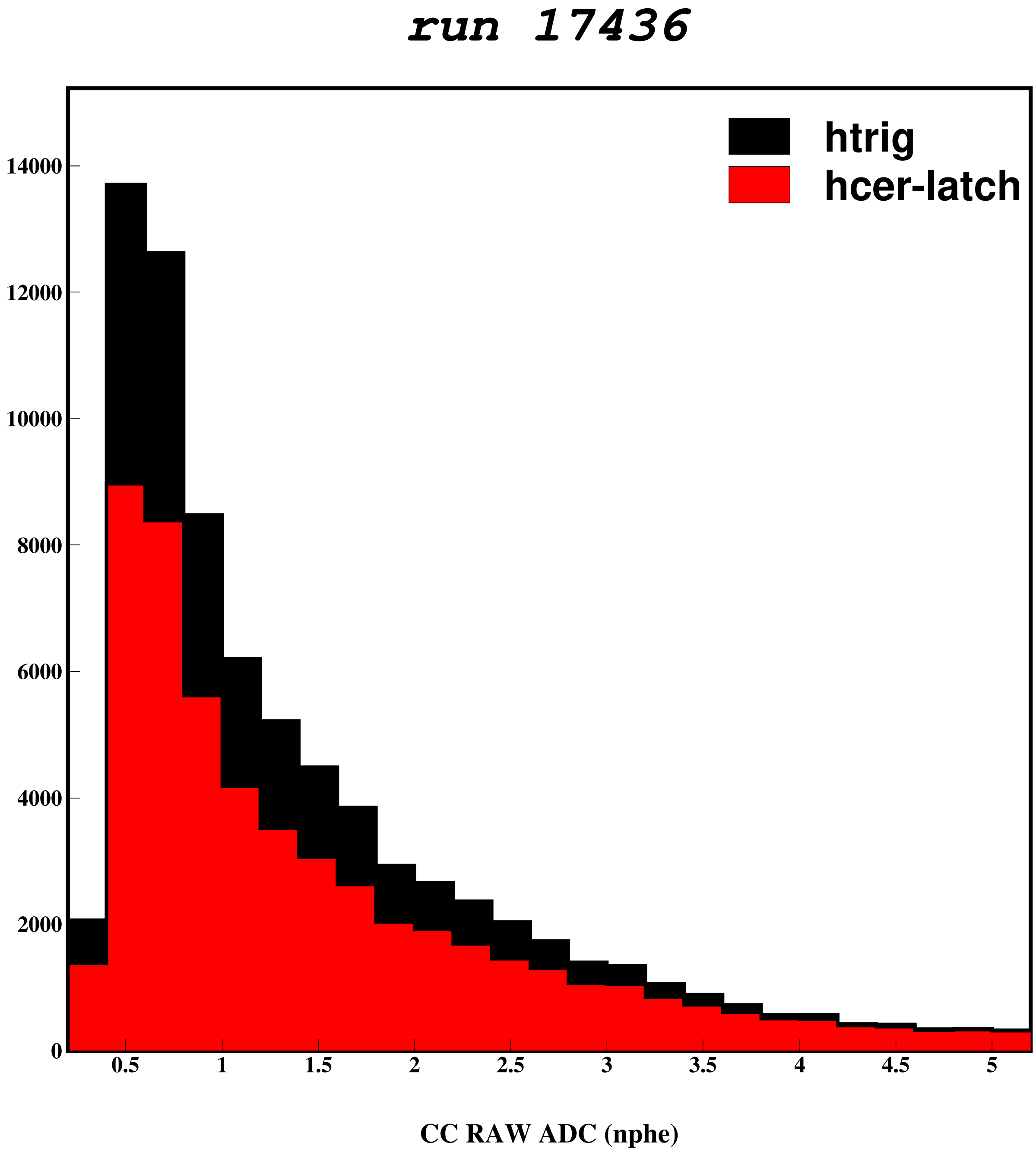

run 17300
htrig
hcer-latch
0
500
1000
1500
2000
2500
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

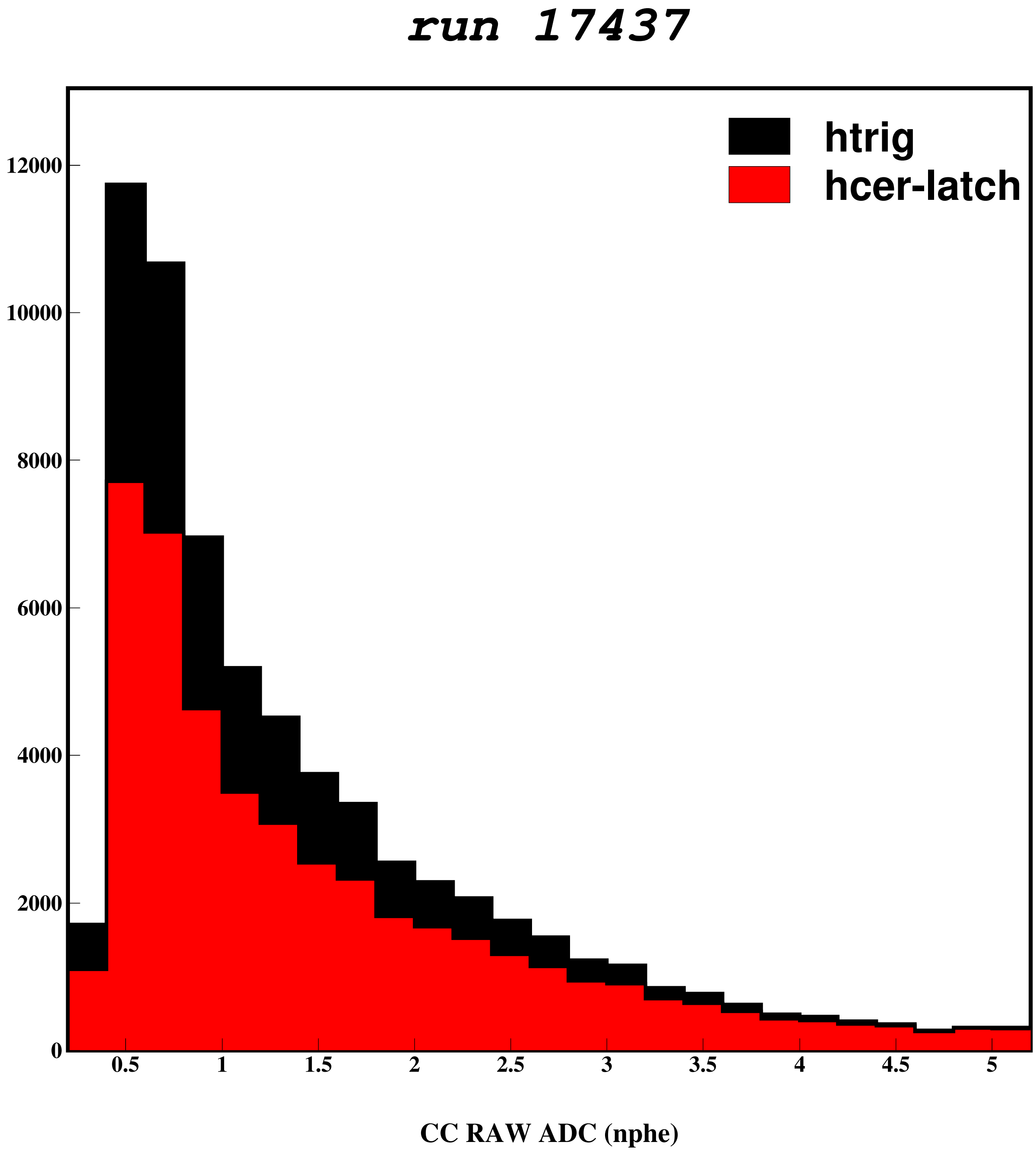

run 17312
400
350
300
250
200
150
100
50
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
htrig
hcer-latch
CC RAW ADC (nphe)



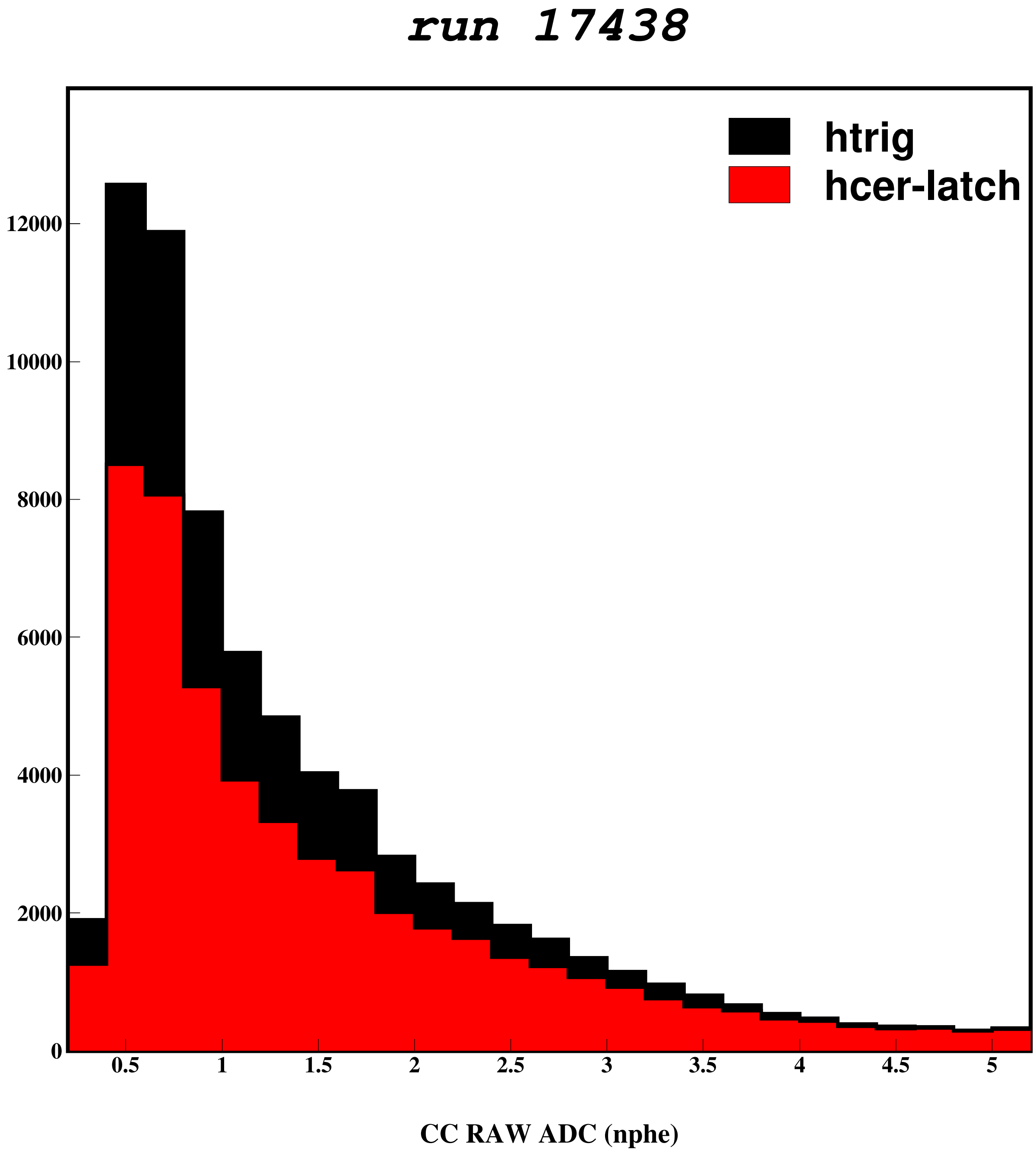
run 17434
htrig
hcer-latch
12000
10000
8000
6000
4000
2000
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

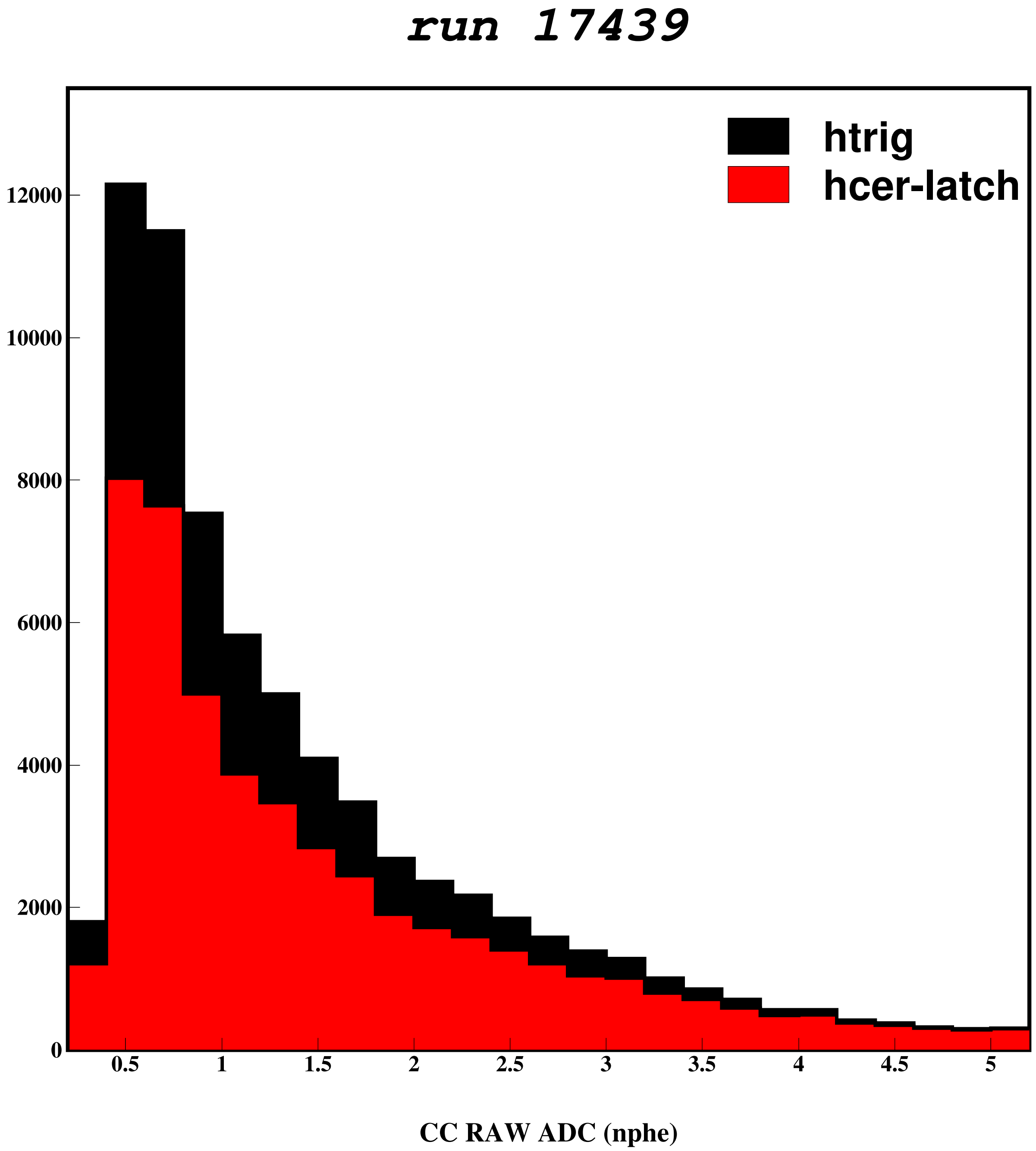

run 17435
htrig
hcer-latch
0
2000
4000
6000
8000
10000
12000
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

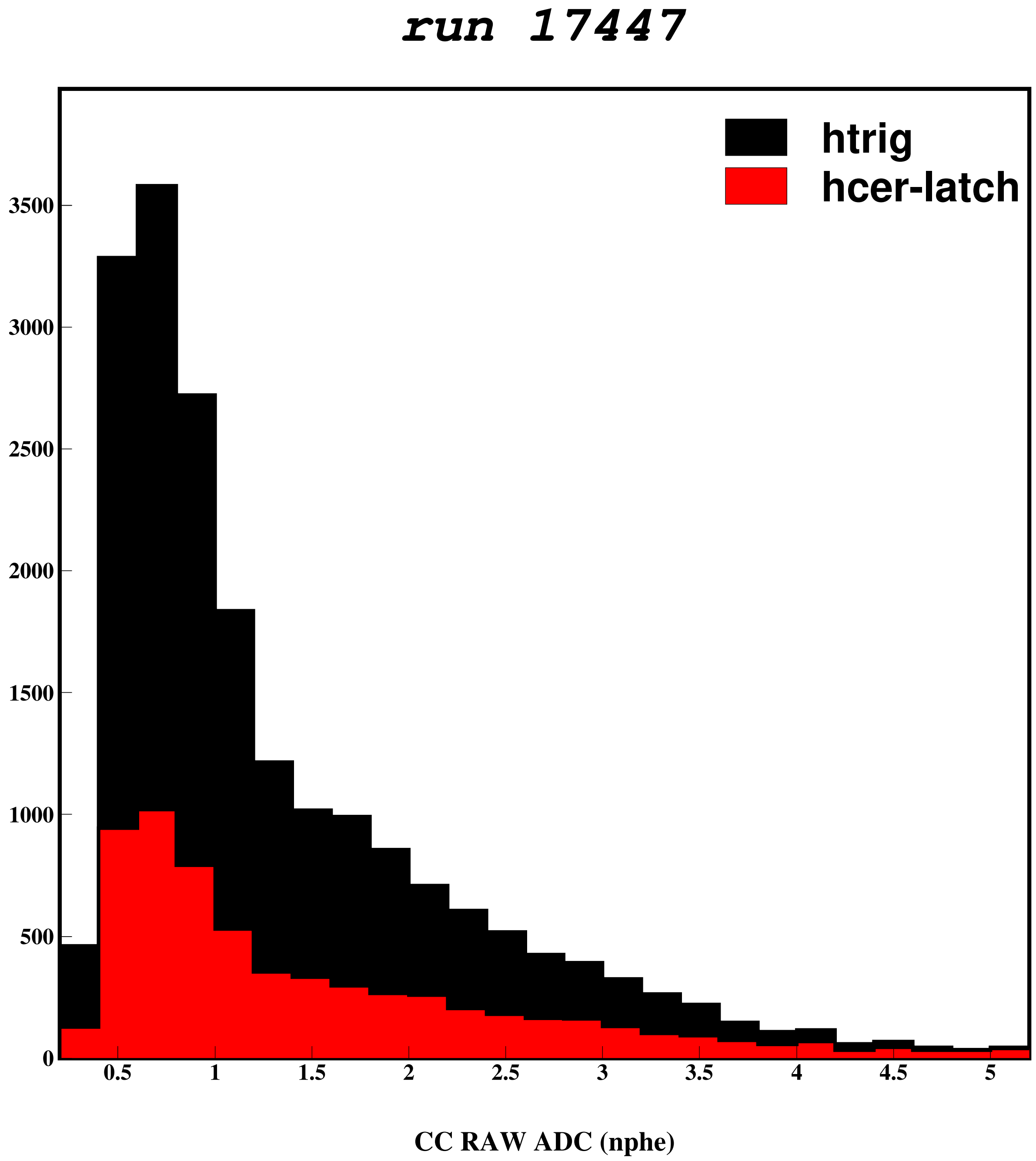

run 17436
htrig
hcer-latch
14000
12000
10000
8000
6000
4000
2000
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

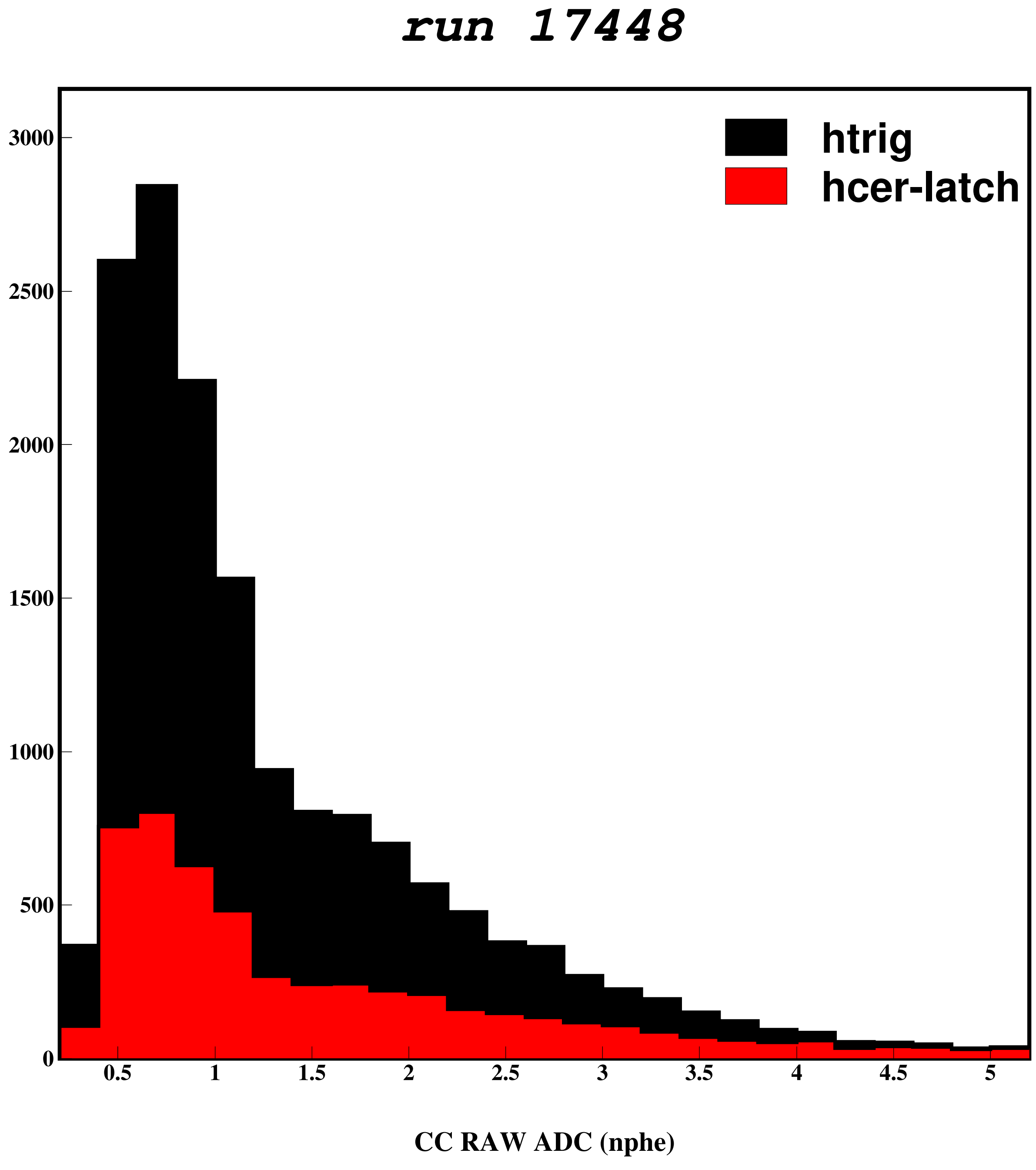


run 17437
htrig
hcer-latch
12000
10000
8000
6000
4000
2000
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

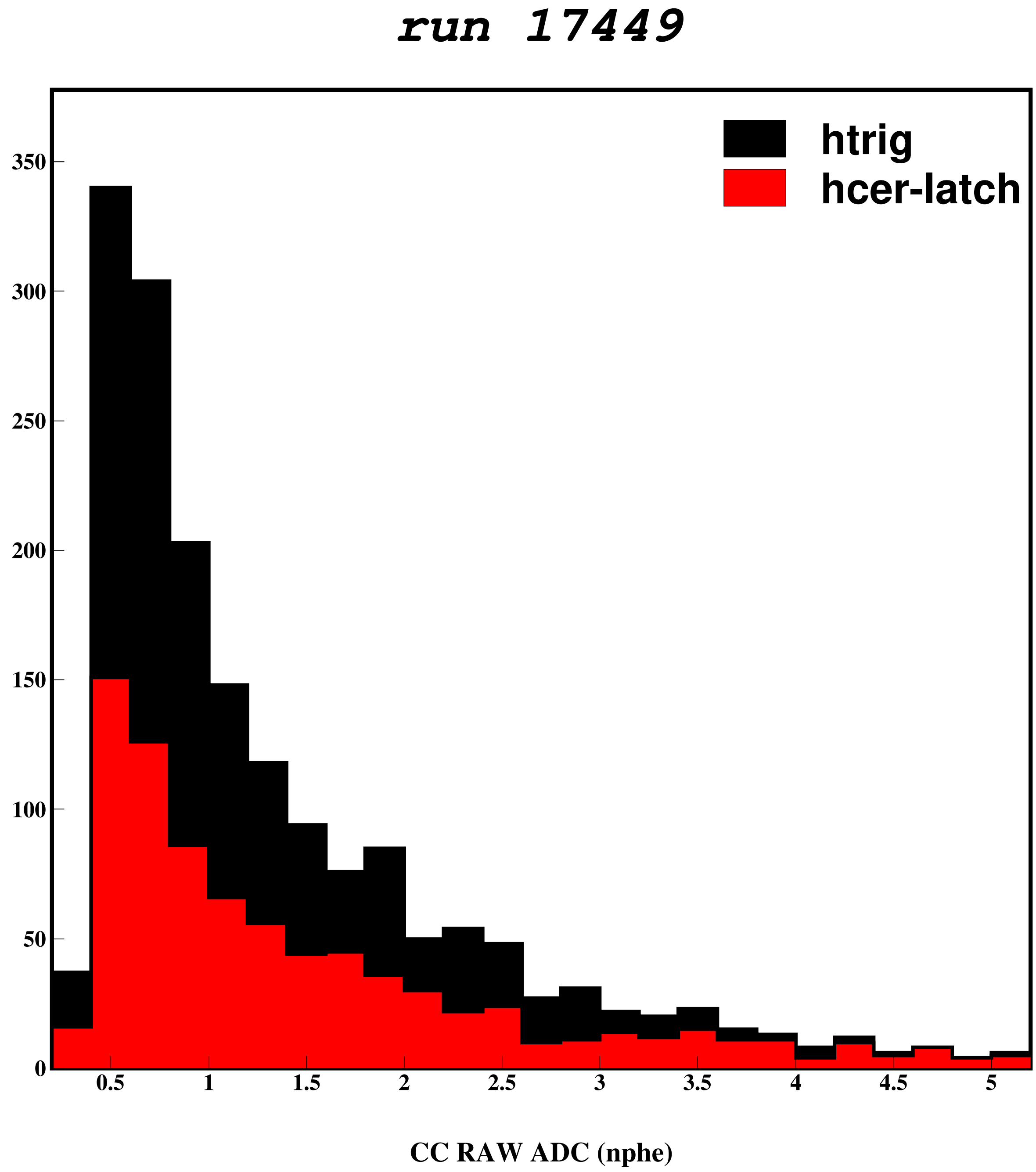

run 17438
htrig
hcer-latch
12000
10000
8000
6000
4000
2000
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

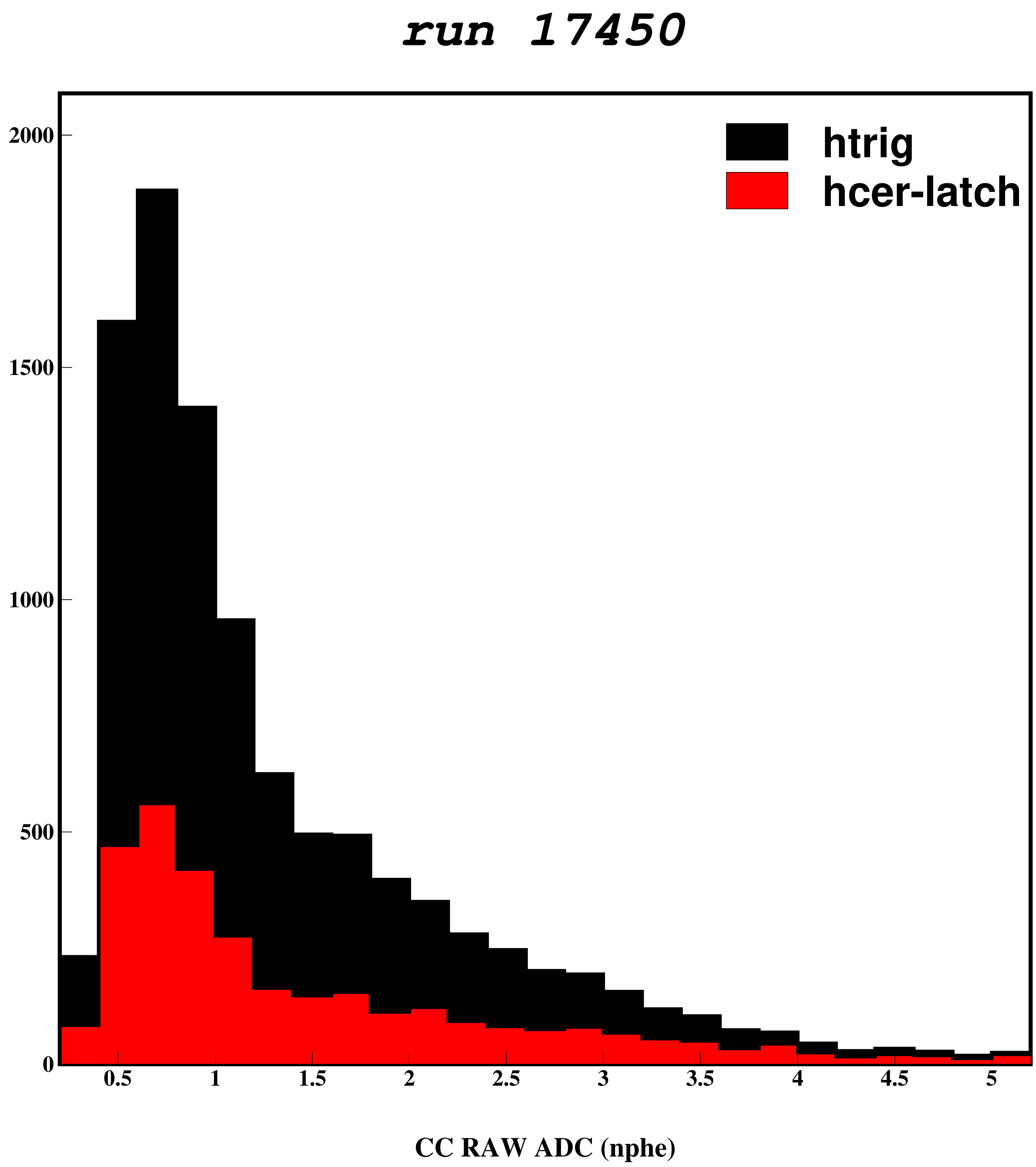

run 17439
htrig
hcer-latch
12000
10000
8000
6000
4000
2000
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

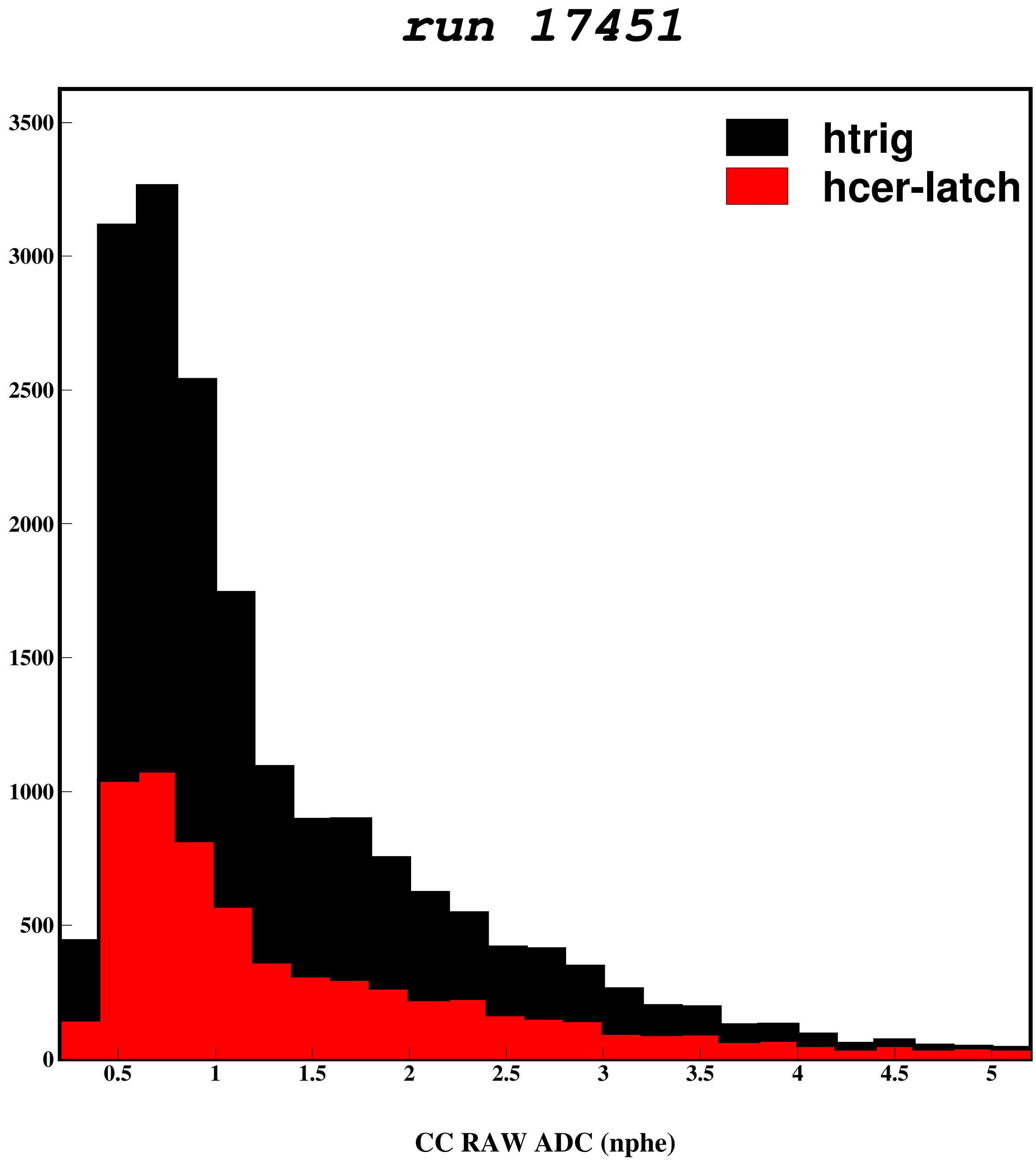

run 17447
htrig
hcer-latch
3500
3000
2500
2000
1500
1000
500
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

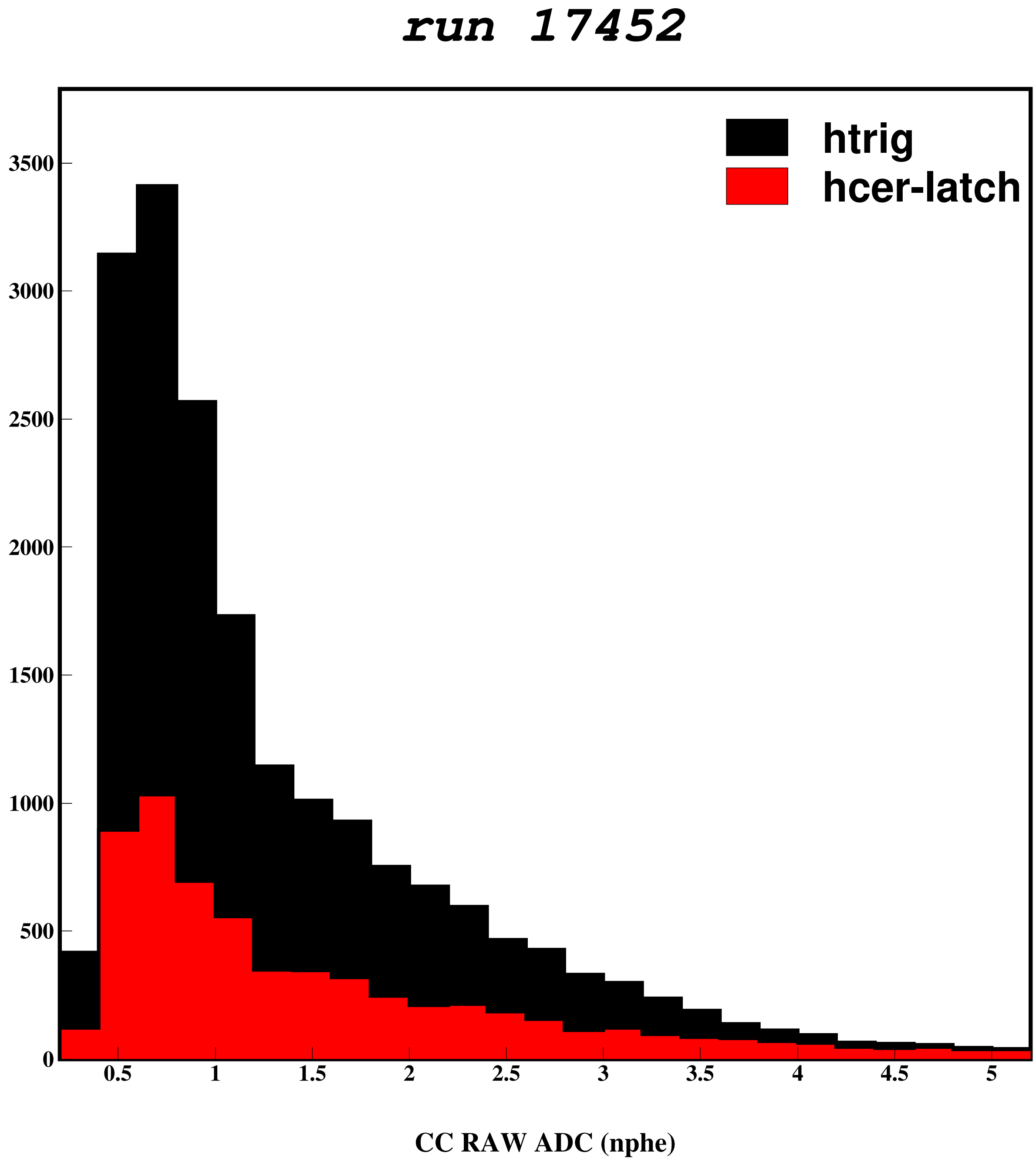


run 17448
htrig
hcer-latch
3000
2500
2000
1500
1000
500
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

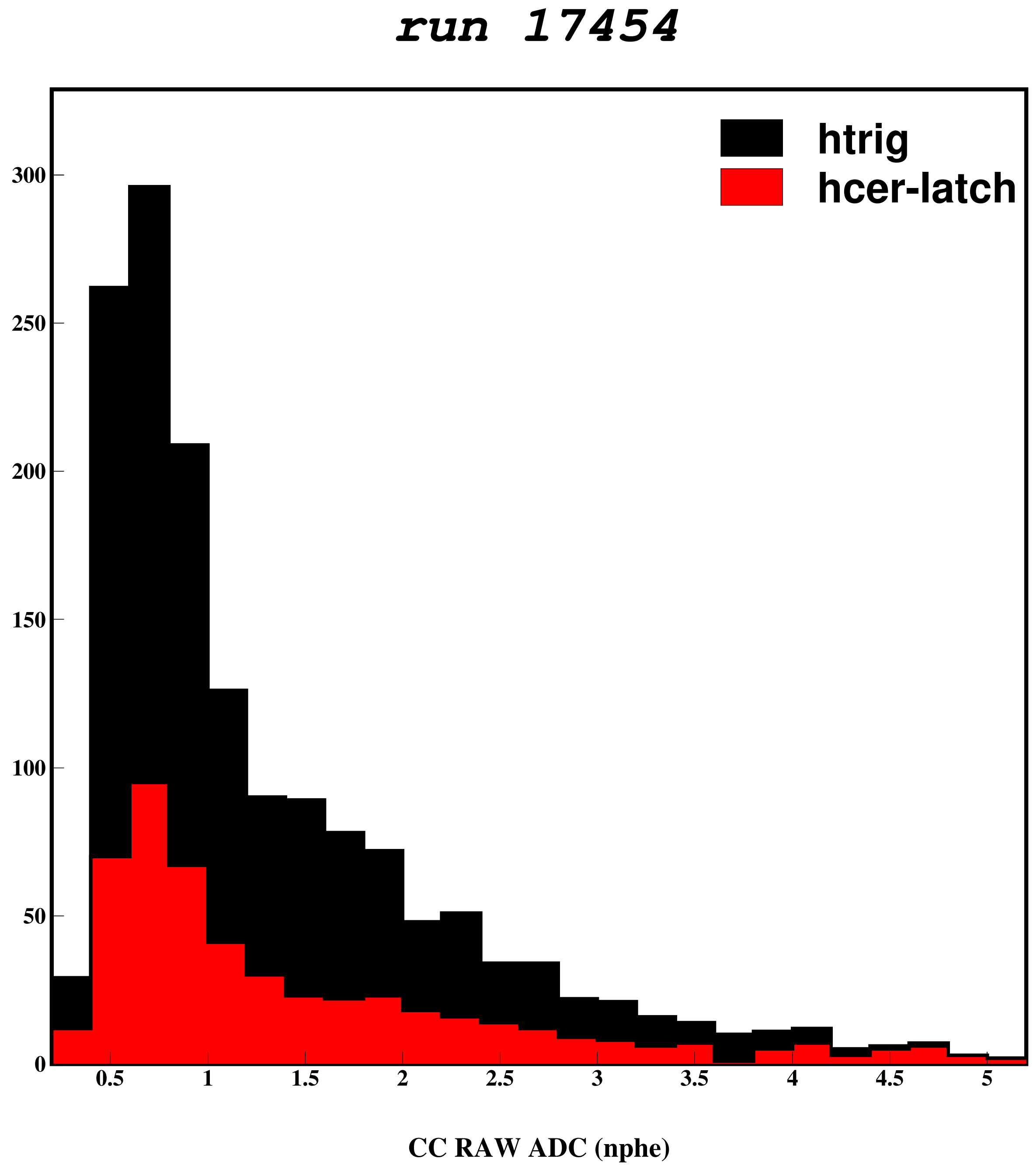

run 17449
htrig
hcer-latch
0
50
100
150
200
250
300
350
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)


run 17450
htrig
hcer-latch
2000
1500
1000
500
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

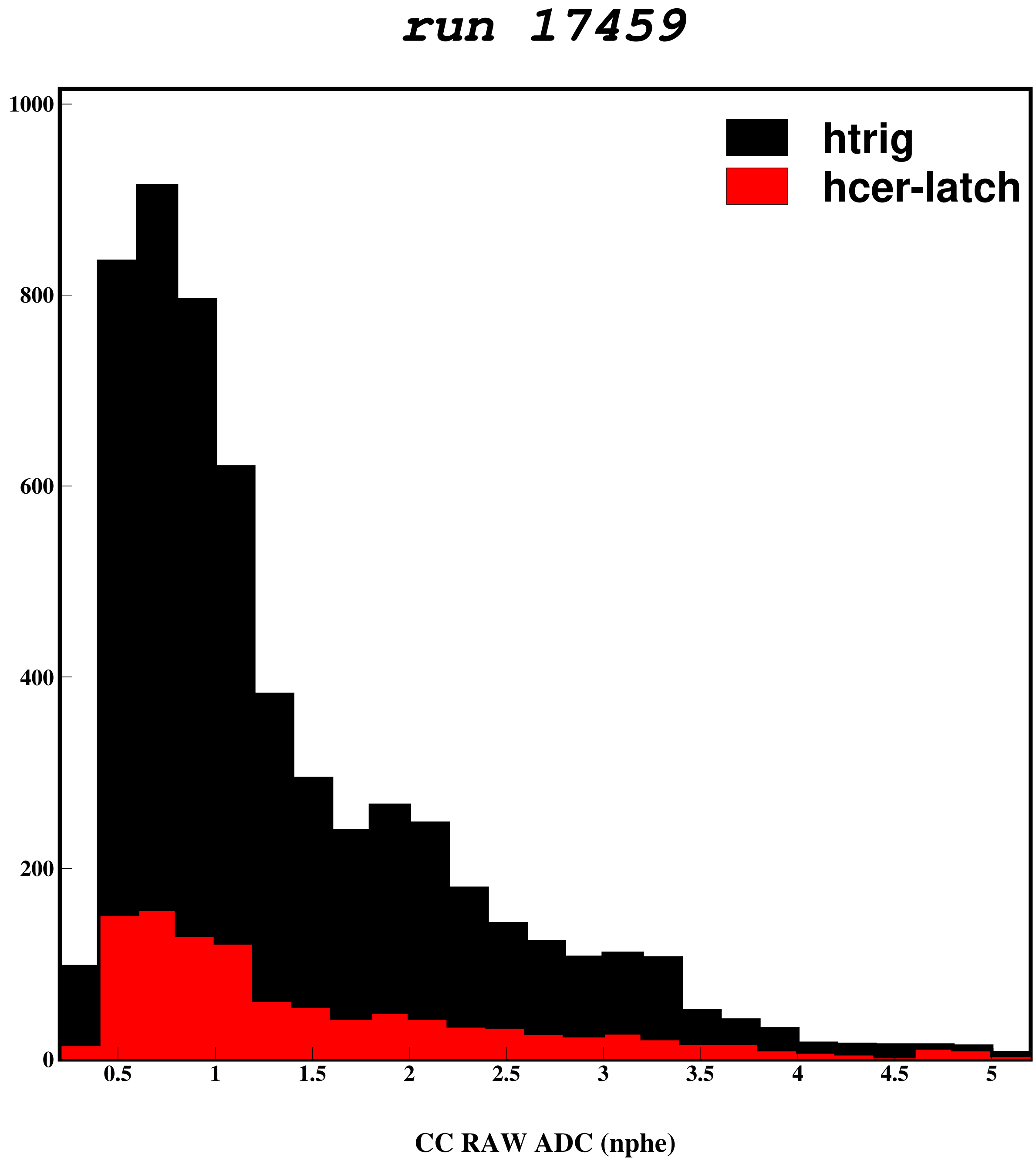

run 17451
htrig
hcer-latch
3500
3000
2500
2000
1500
1000
500
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

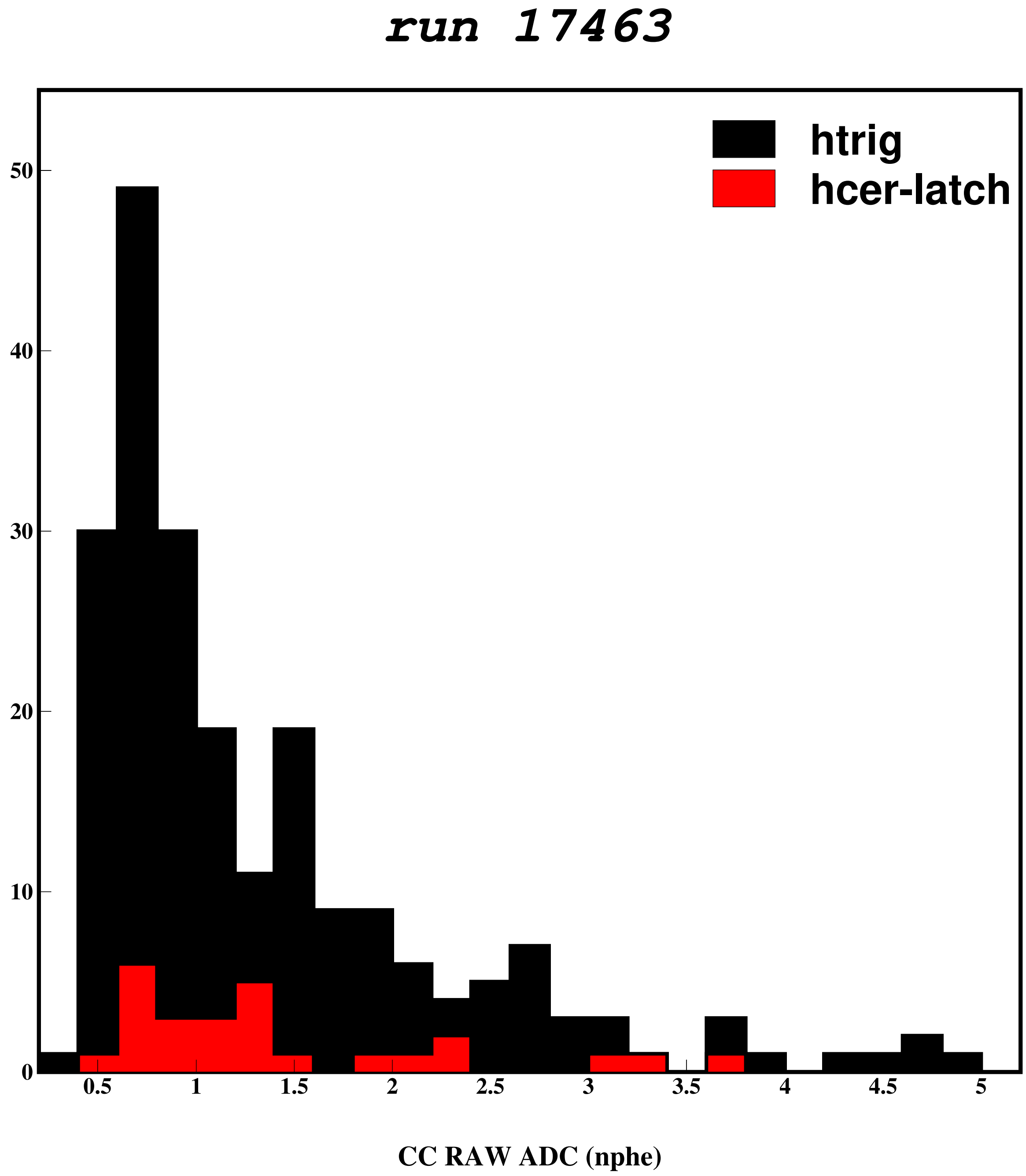


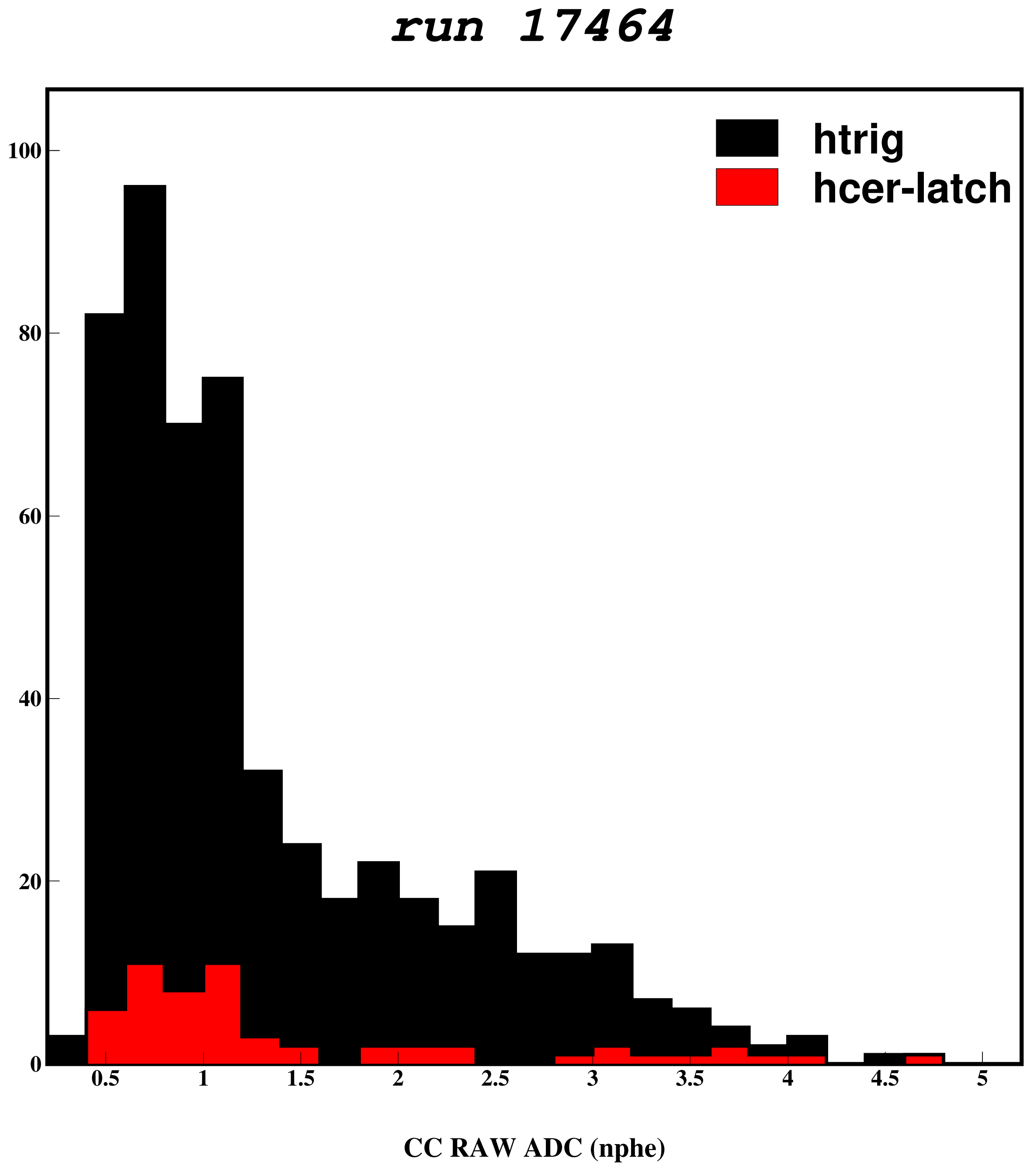
run 17452
htrig
hcer-latch
3500
3000
2500
2000
1500
1000
500
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

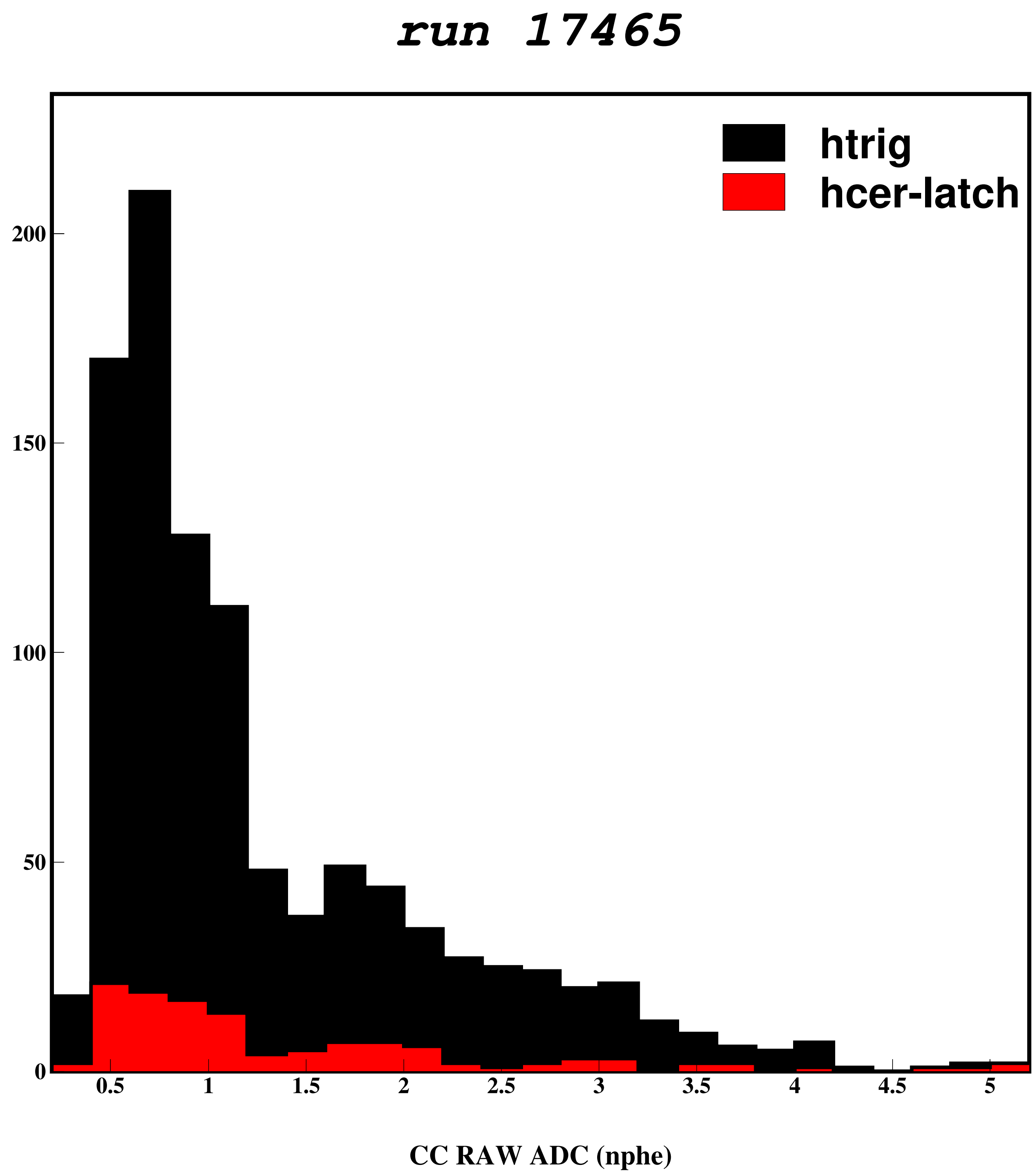


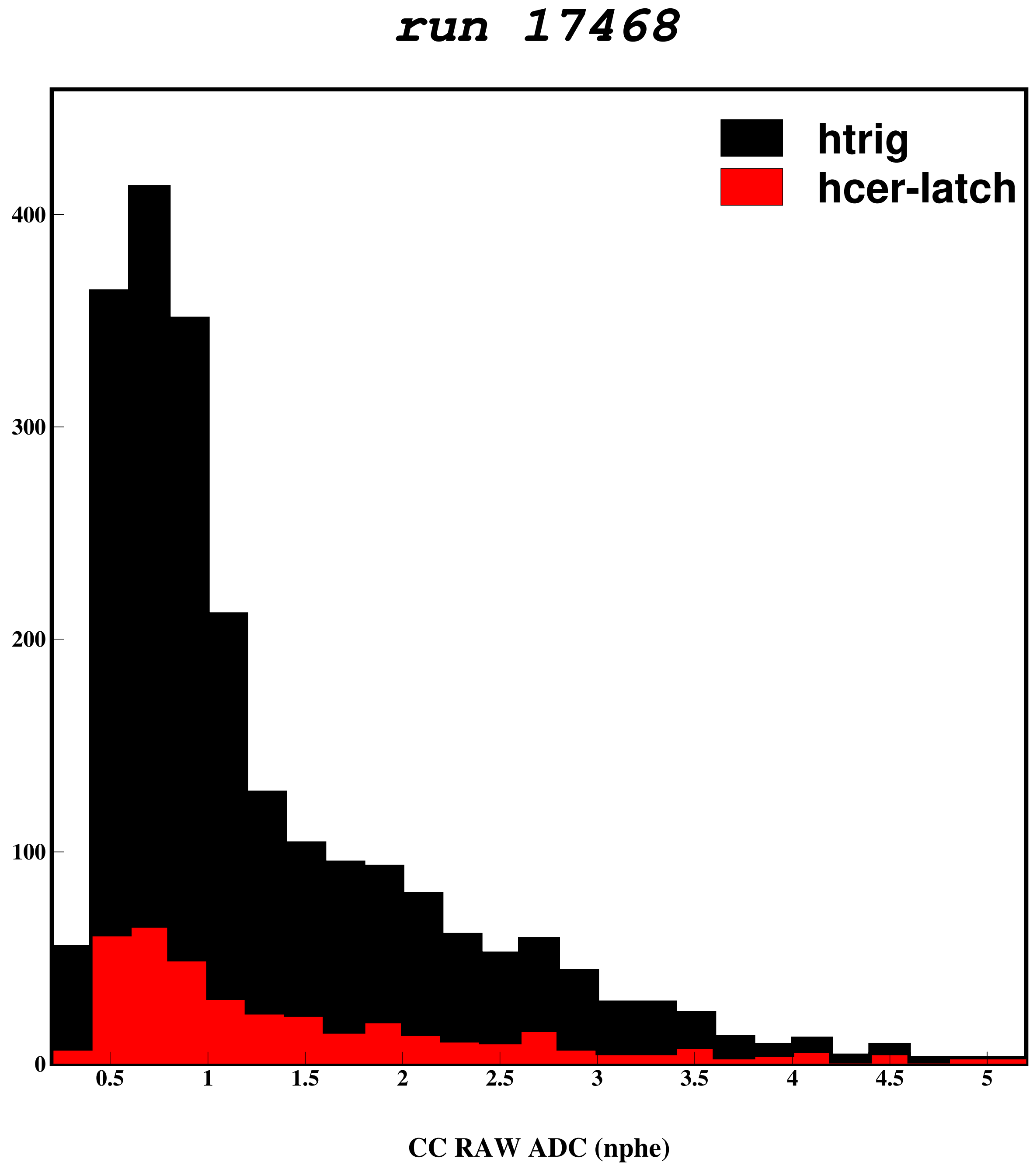
run 17454
htrig
hcer-latch
0
50
100
150
200
250
300
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)


run 17459
htrig
hcer-latch
1000
800
600
400
200
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

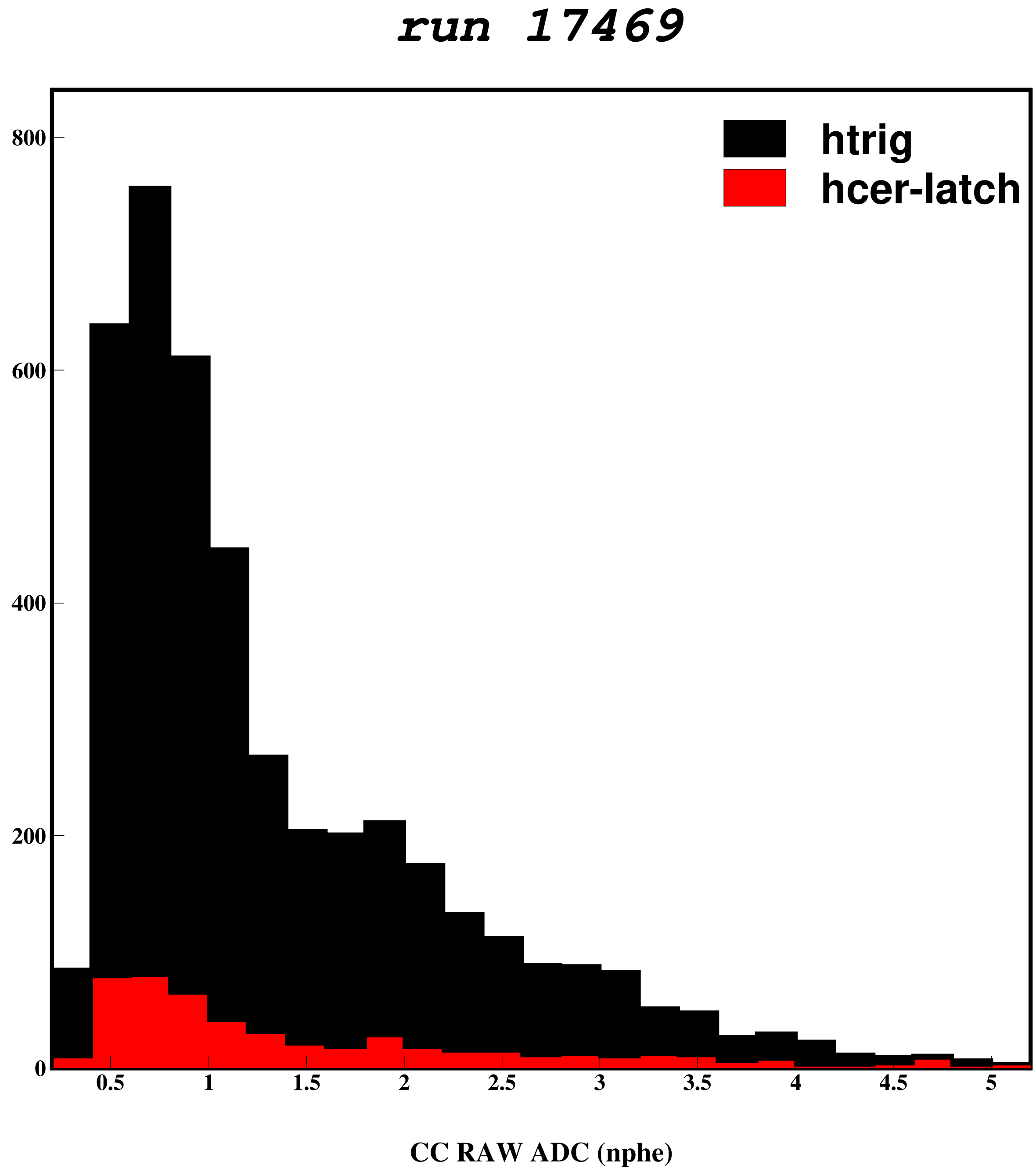

run 17463
htrig
hcer-latch
0
10
20
30
40
50
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

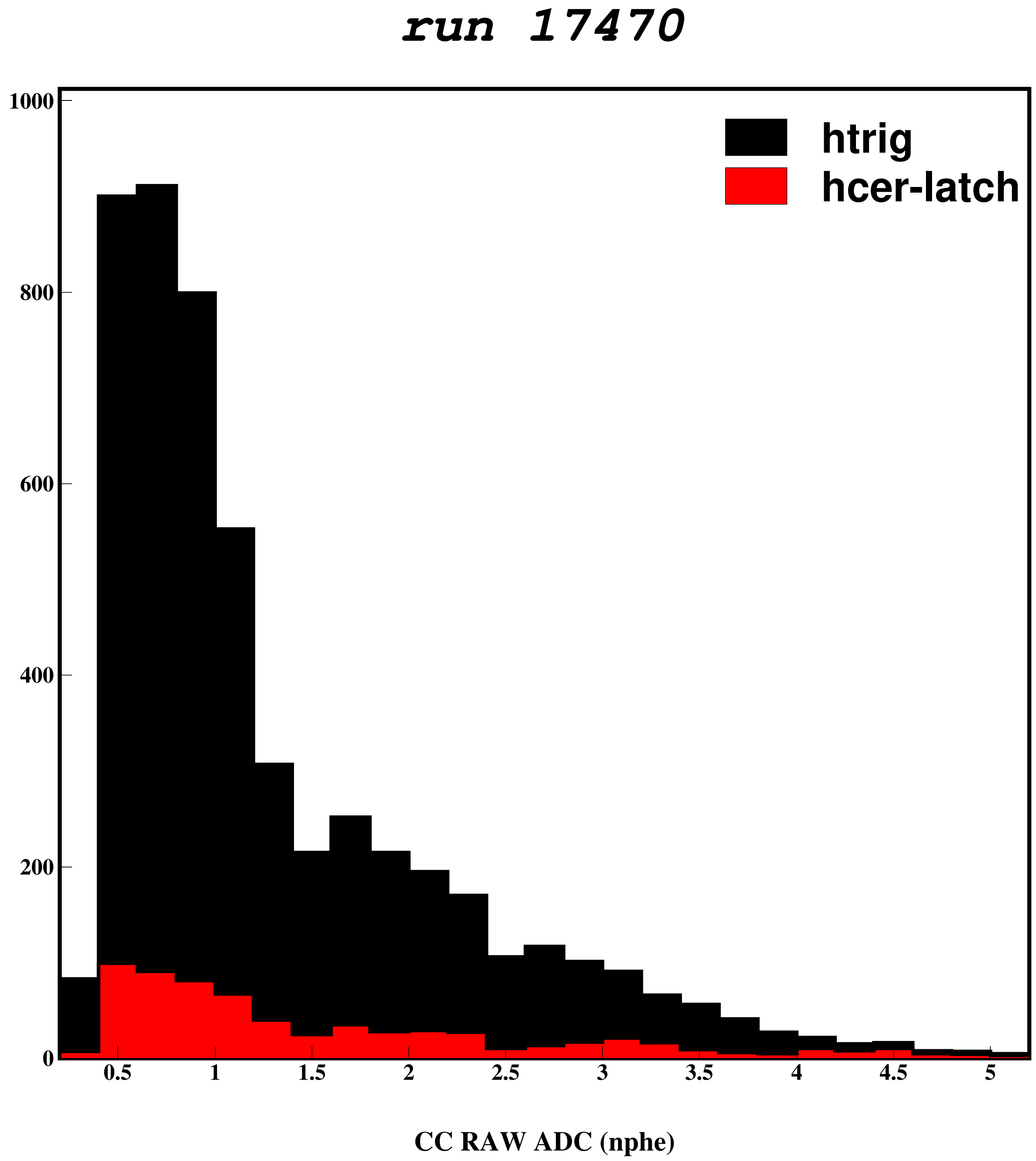

run 17464
htrig
hcer-latch
0
20
40
60
80
100
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

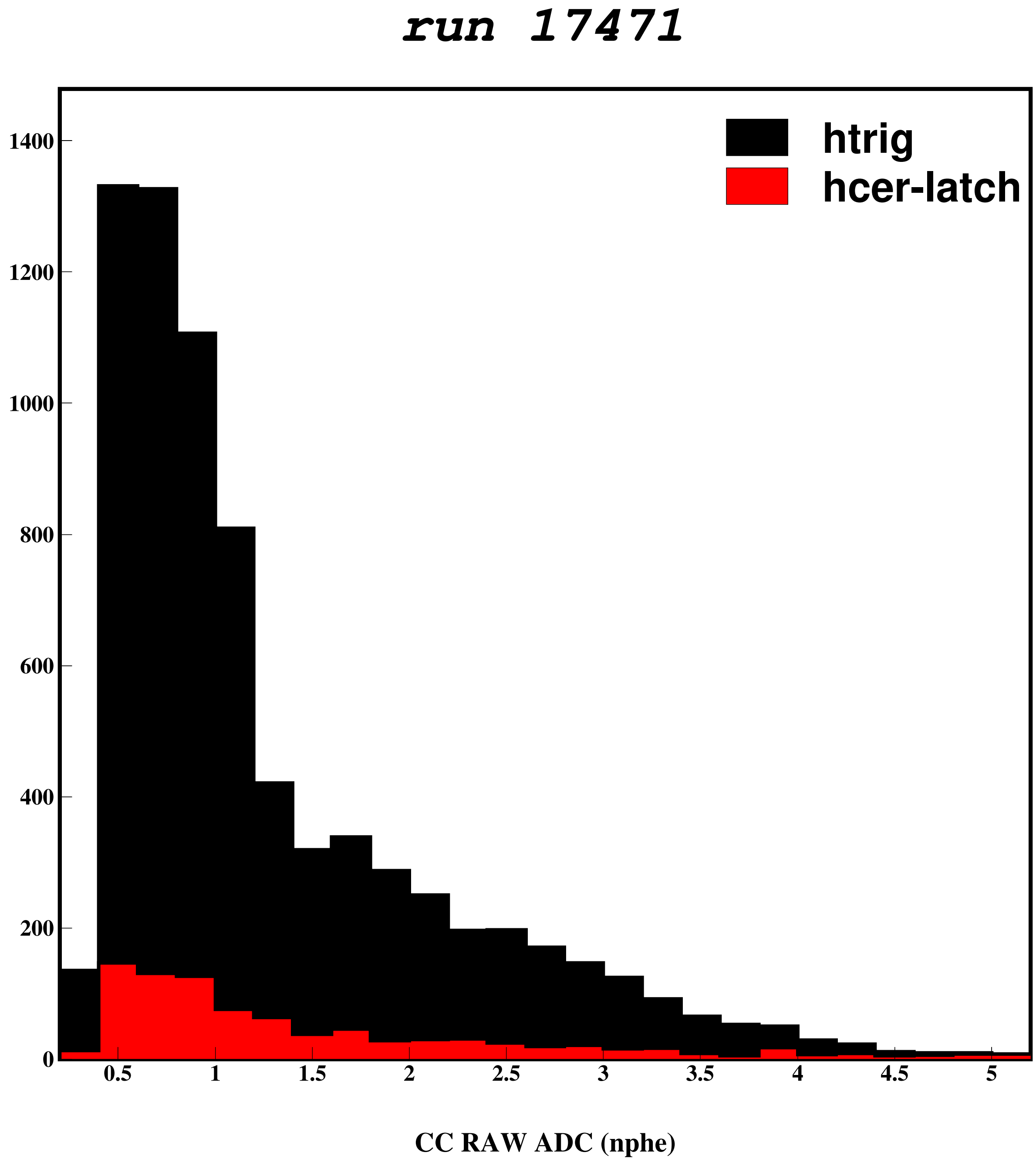

run 17465
htrig
hcer-latch
200
150
100
50
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)



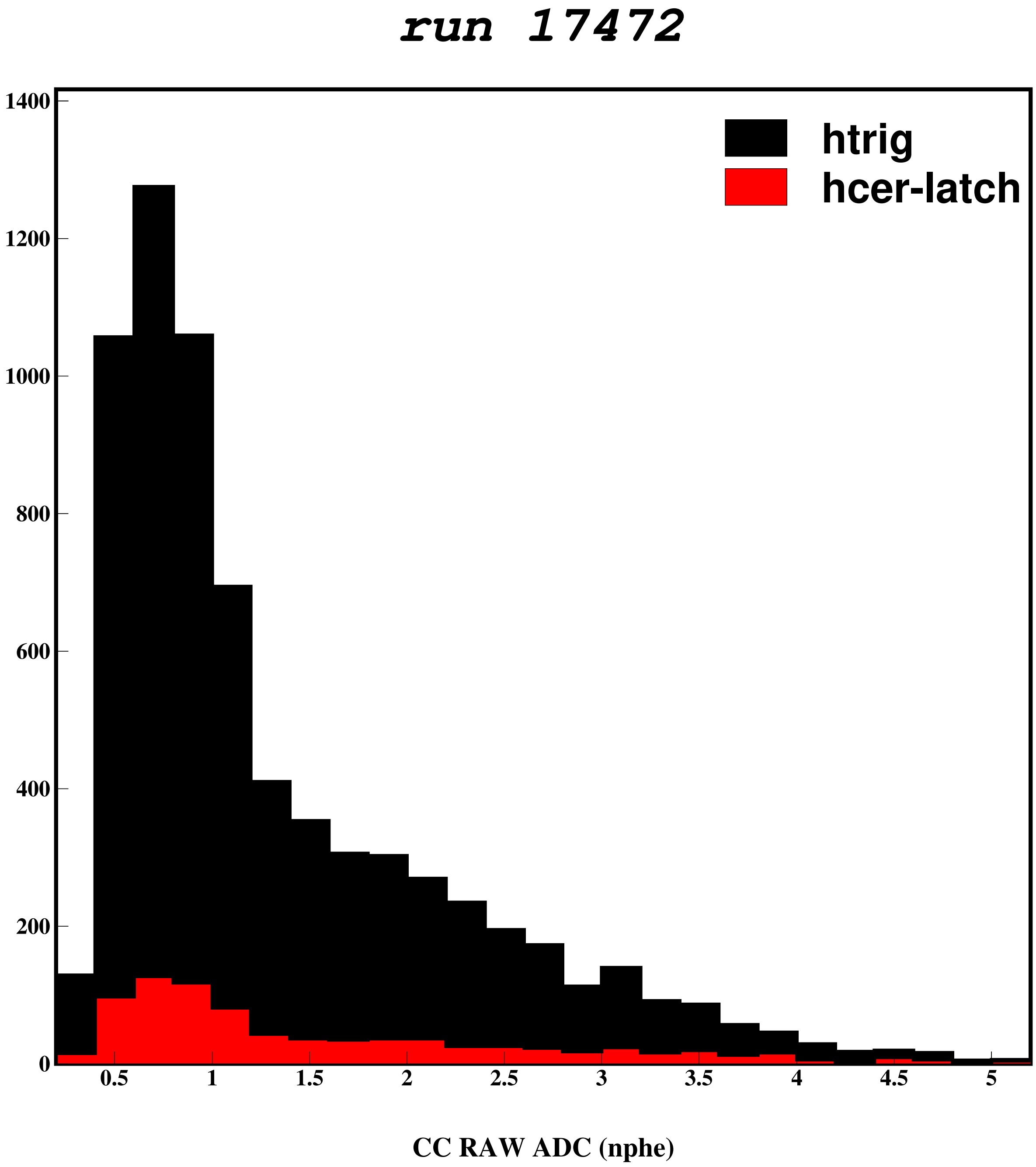
run 17468
htrig
hcer-latch
400
300
200
100
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)

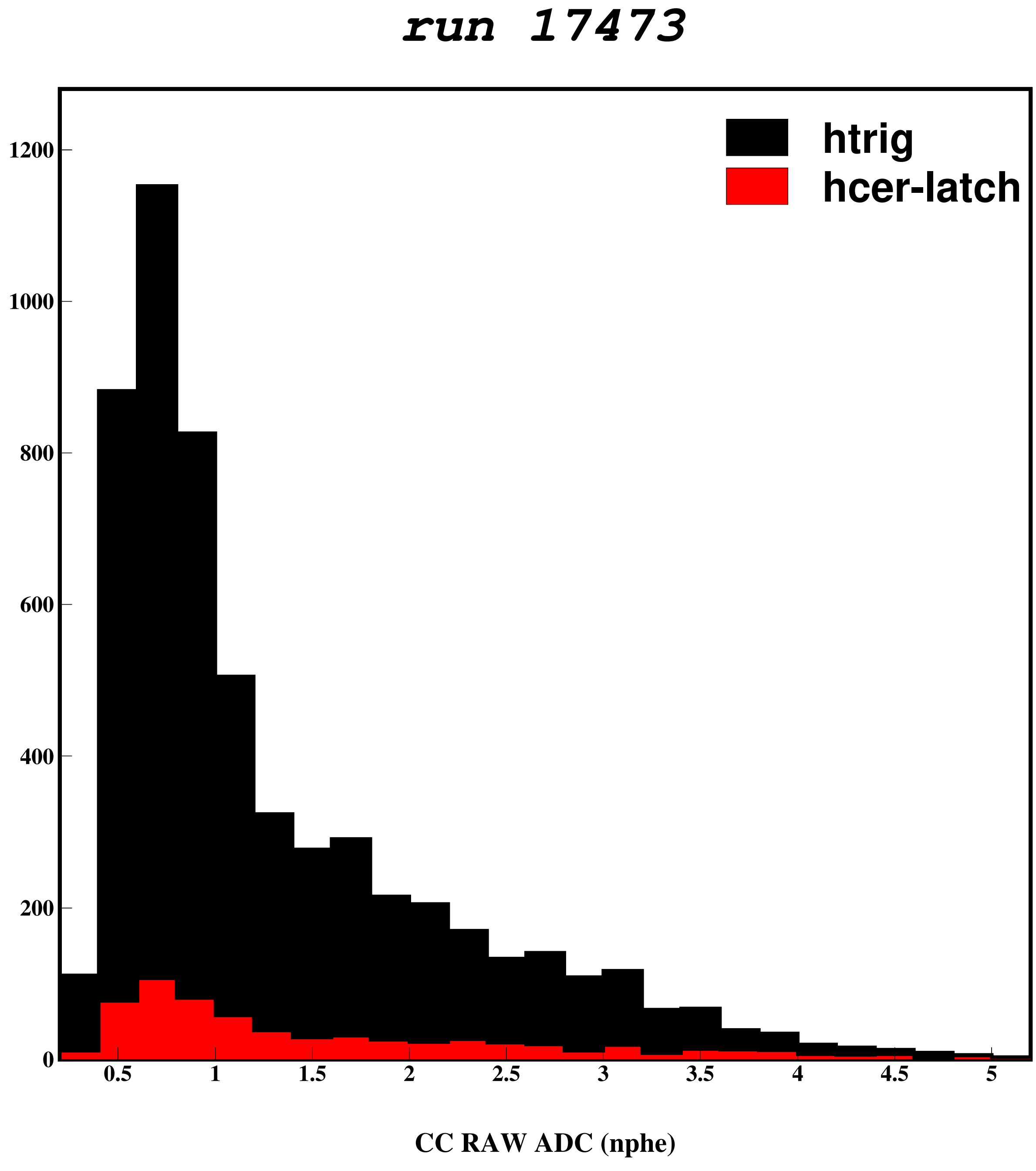

run 17469
htrig
hcer-latch
800
600
400
200
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)


run 17470
htrig
hcer-latch
1000
800
600
400
200
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)



run 17471
htrig
hcer-latch
1400
1200
1000
800
600
400
200
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)


run 17472
1400
1200
1000
800
600
400
200
0
htrig
hcer-latch
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)


run 17473
htrig
hcer-latch
1200
1000
800
600
400
200
0
0.5
1
1.5
2
2.5
3
3.5
4
4.5
5
CC RAW ADC (nphe)