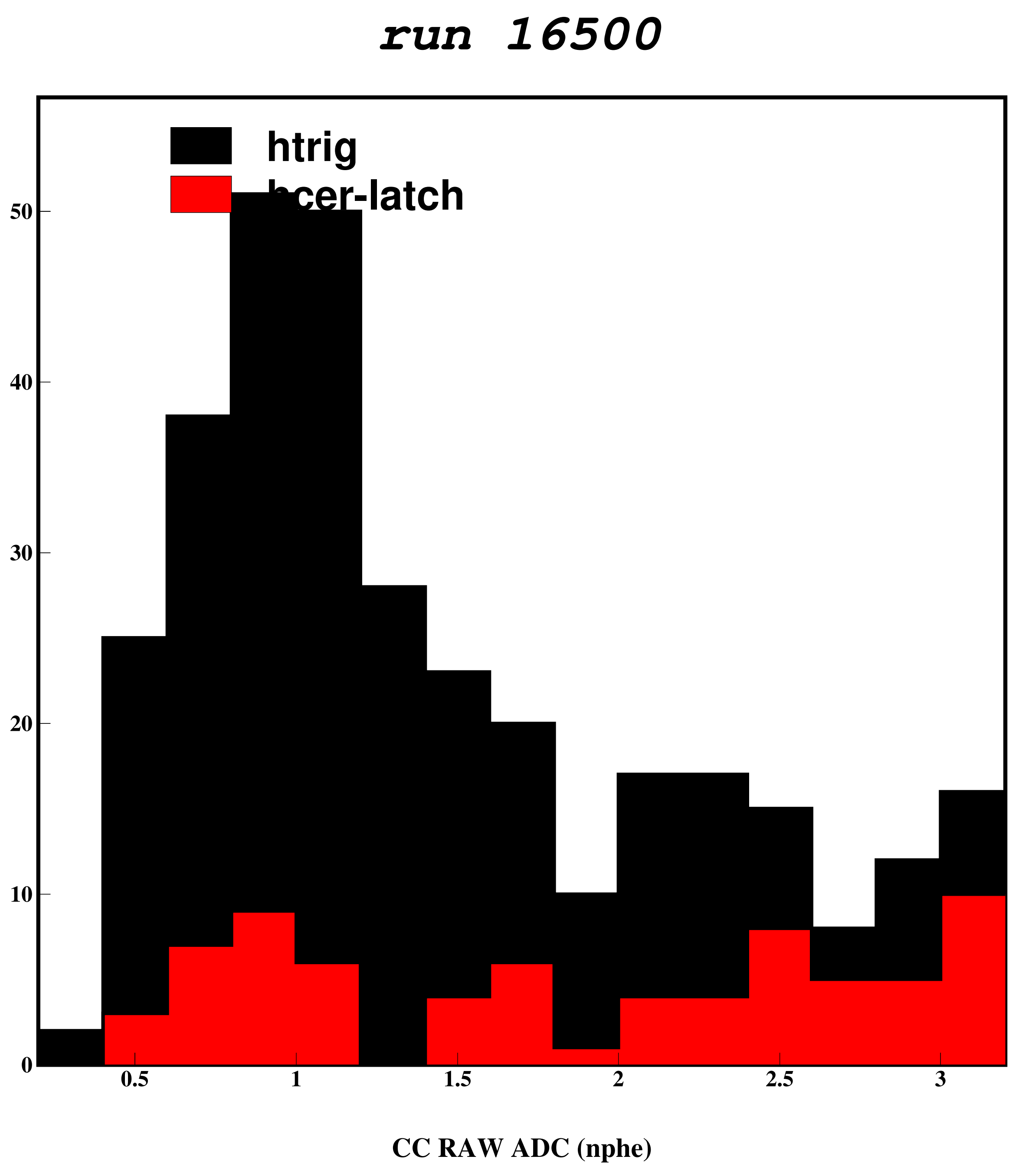
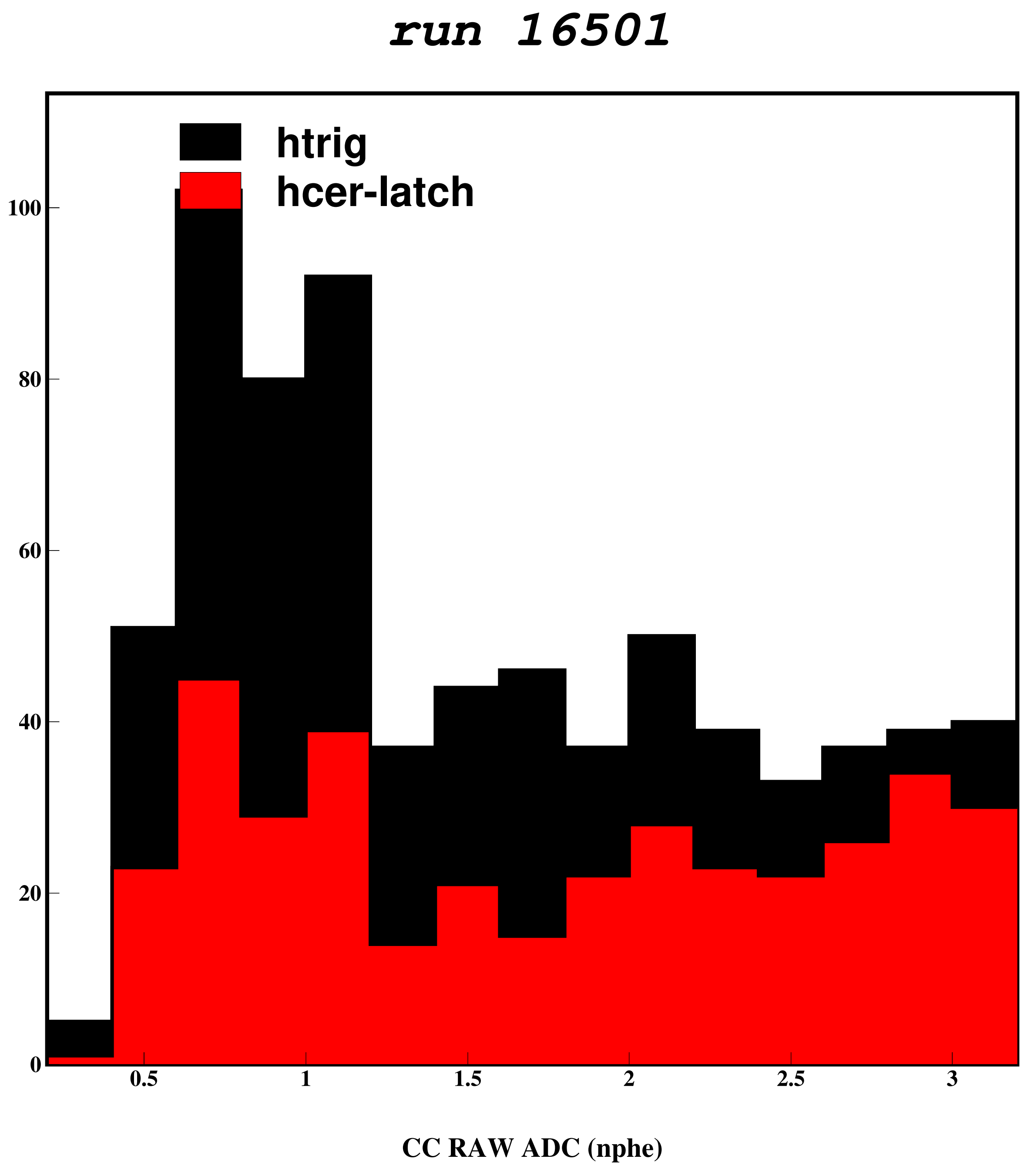
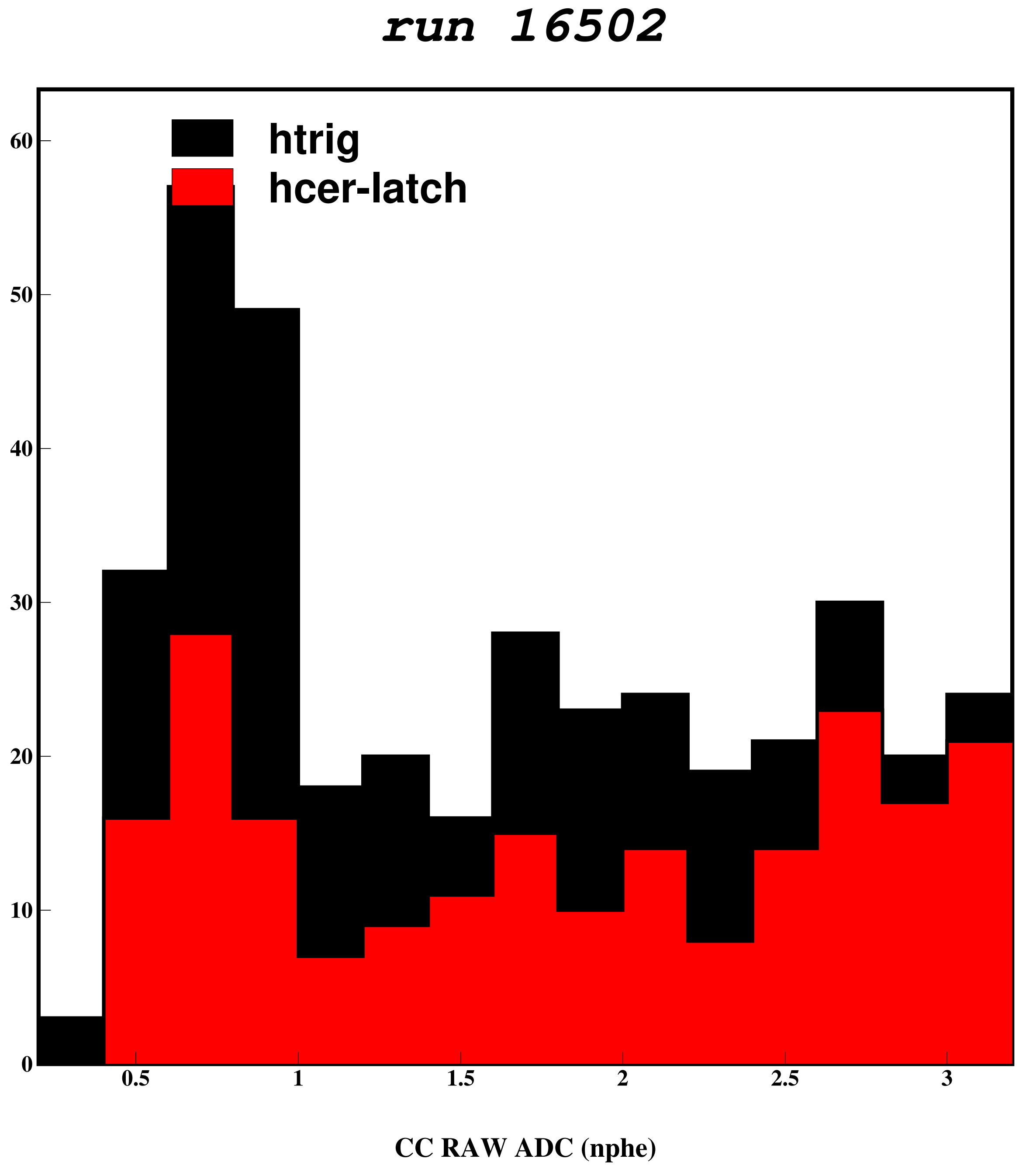
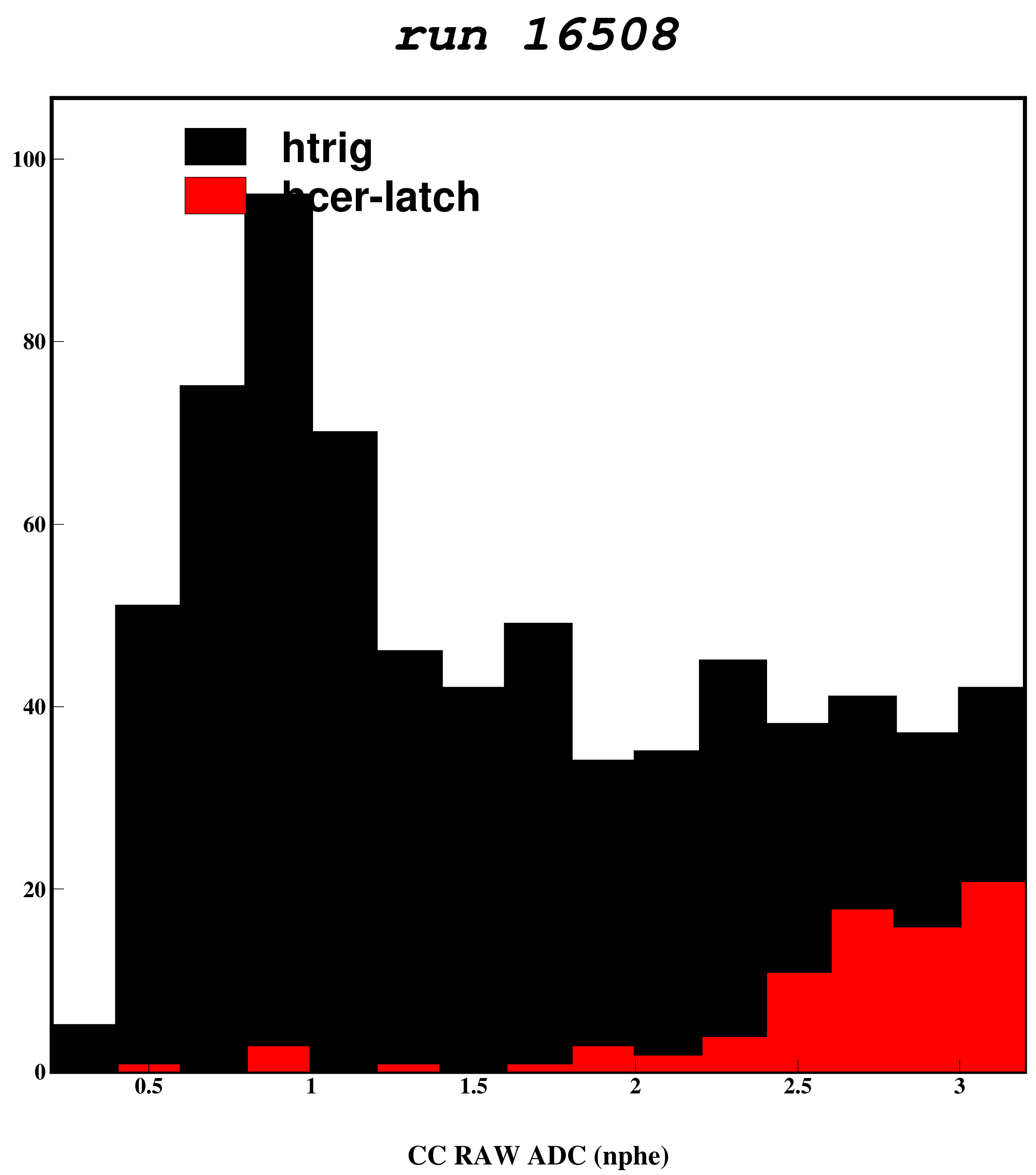


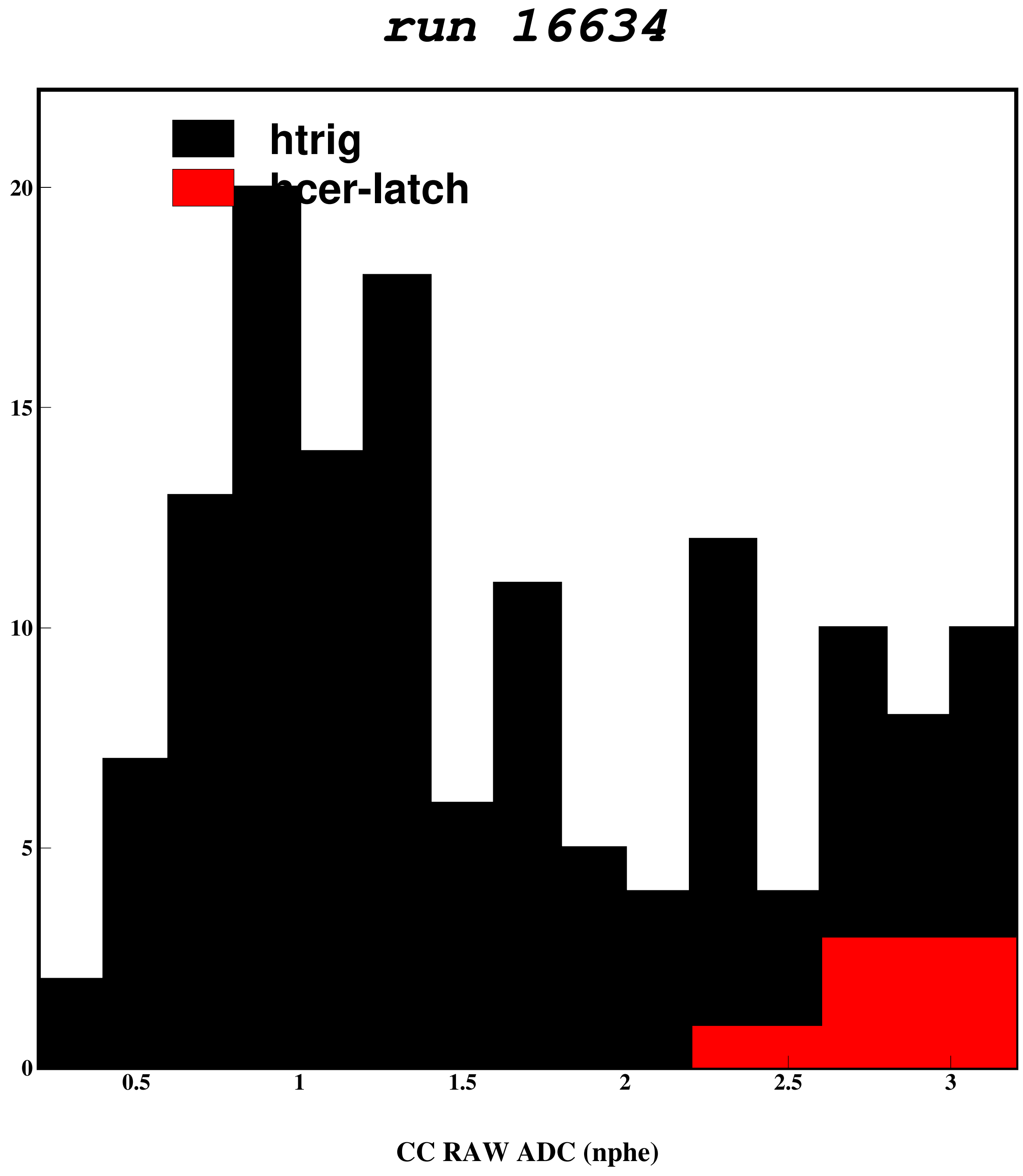
run 16500
htrig
hcer-latch
0
10
20
30
40
50
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

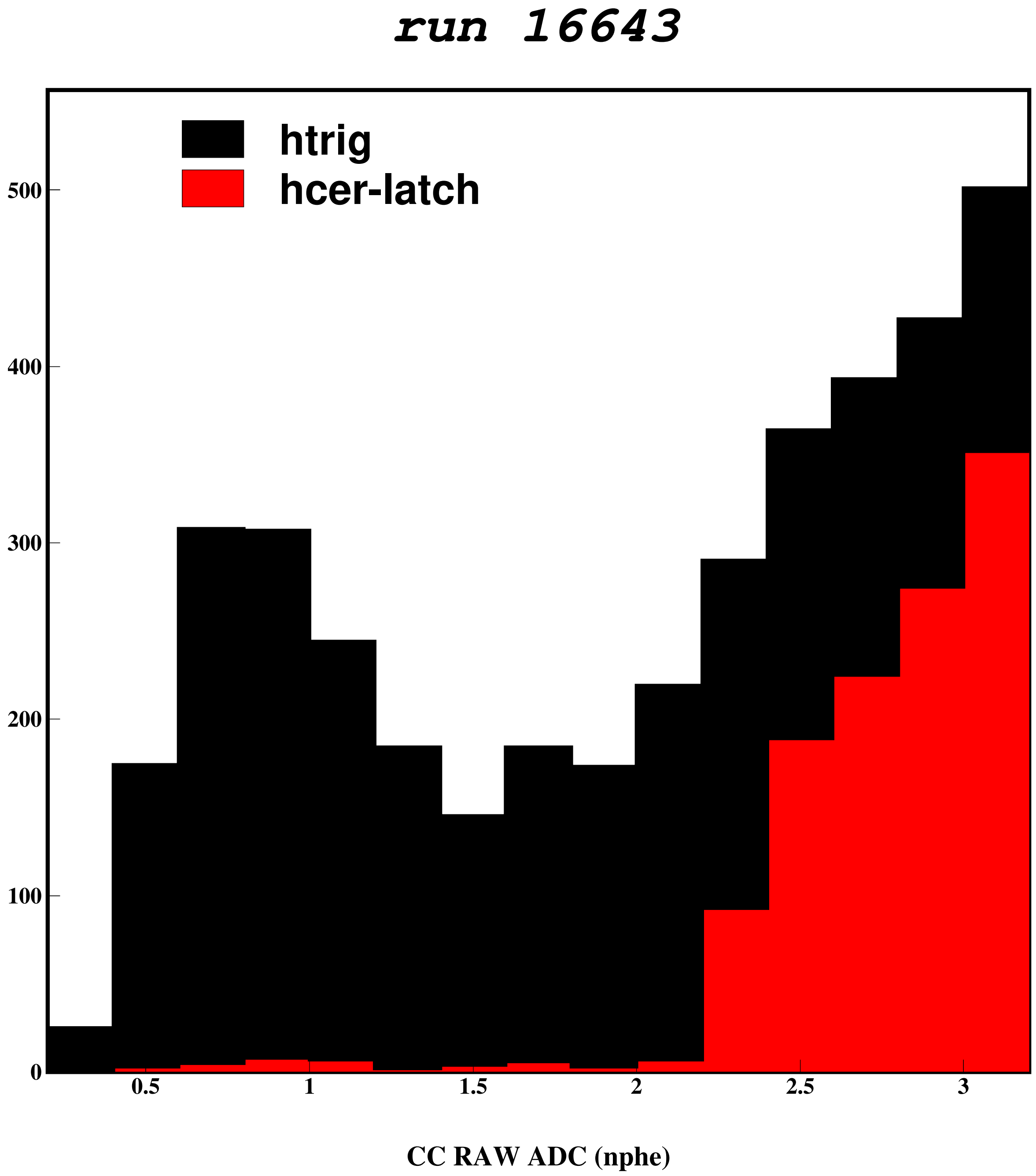

run 16501
htrig
hcer-latch
100
80
60
40
20
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)


run 16502
htrig
hcer-latch
0
10
20
30
40
50
60
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

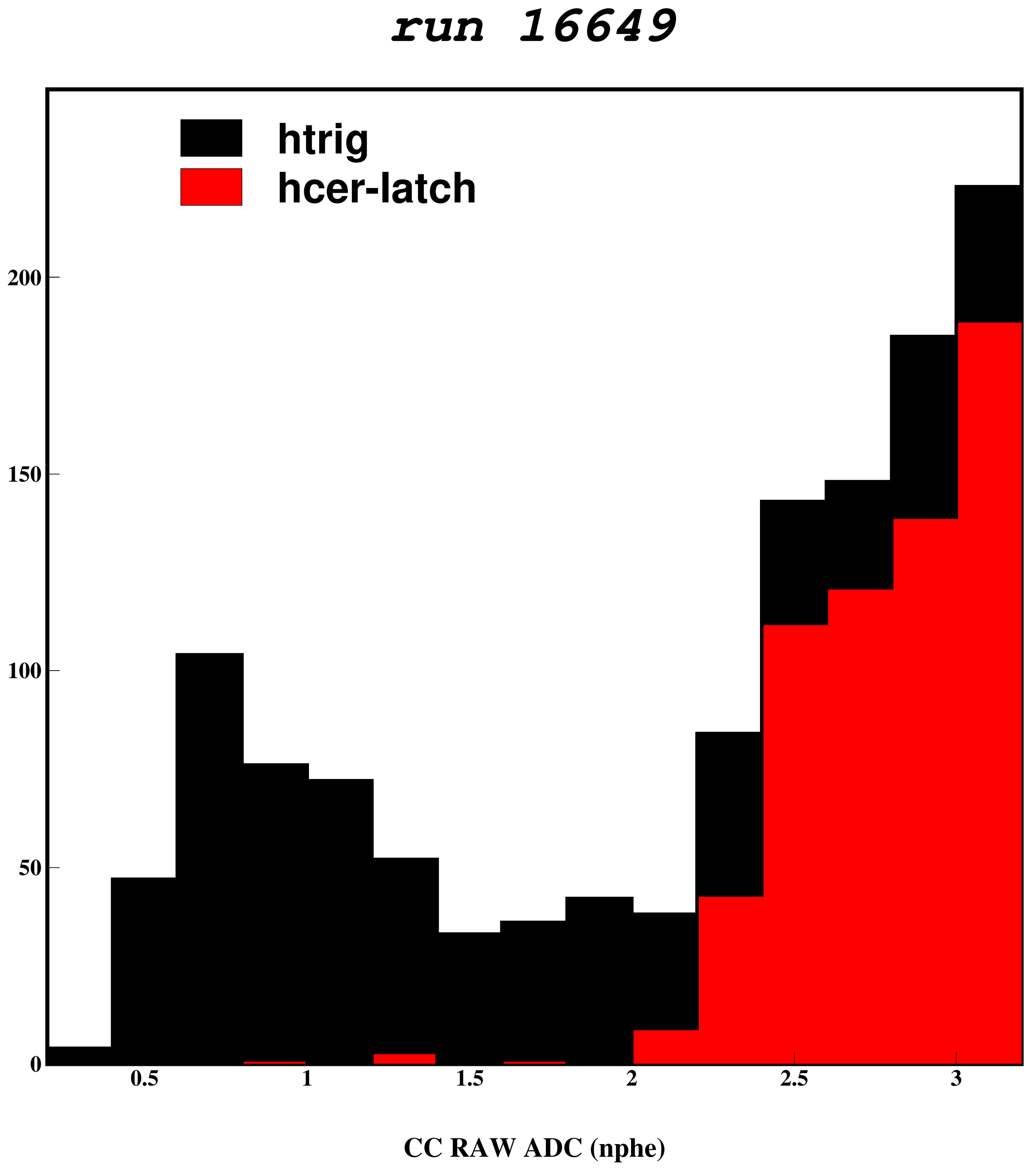


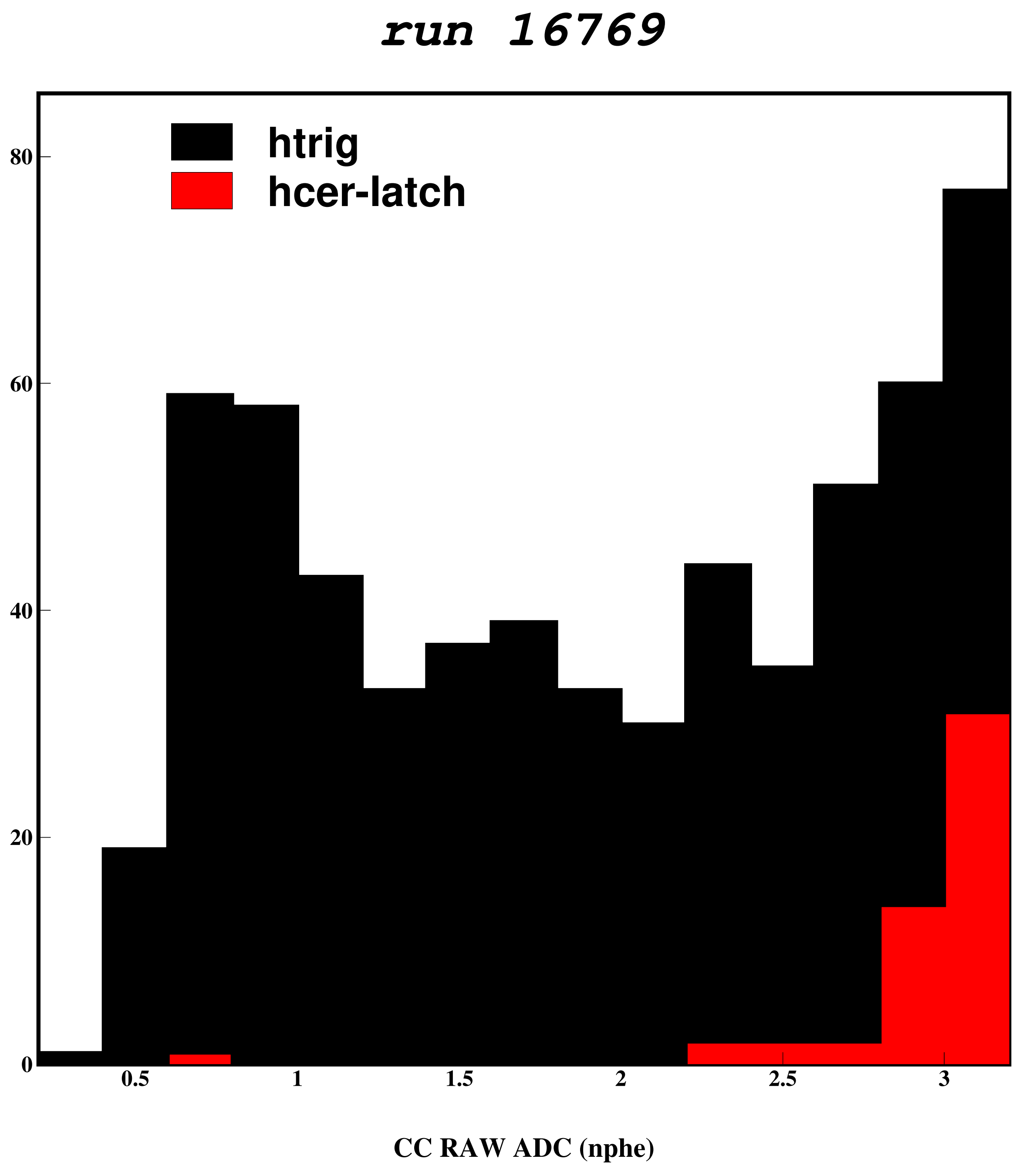
run 16508
htrig
hcer-latch
100
80
60
40
20
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

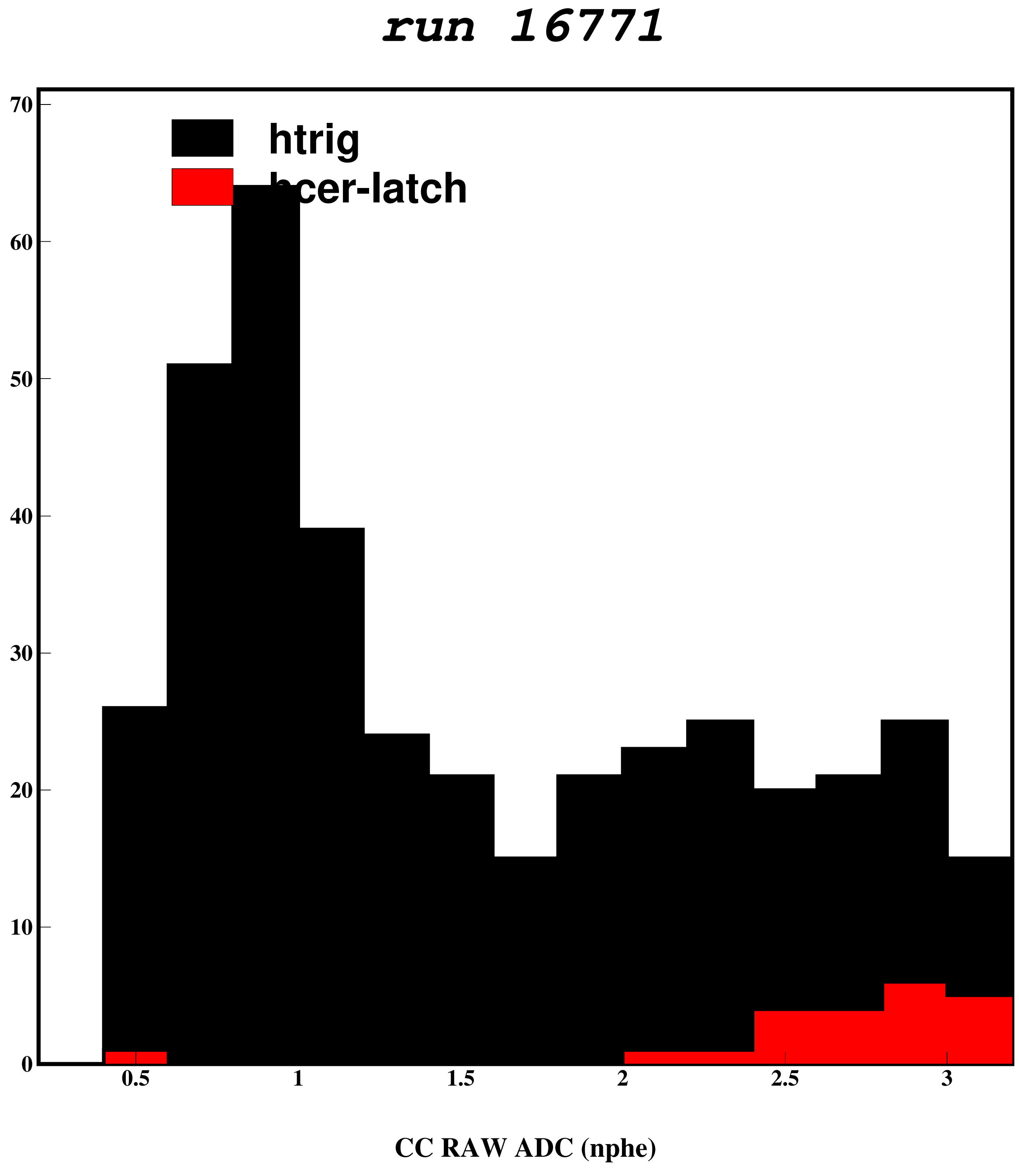

run 16634
htrig
hcer-latch
20
15
10
5
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

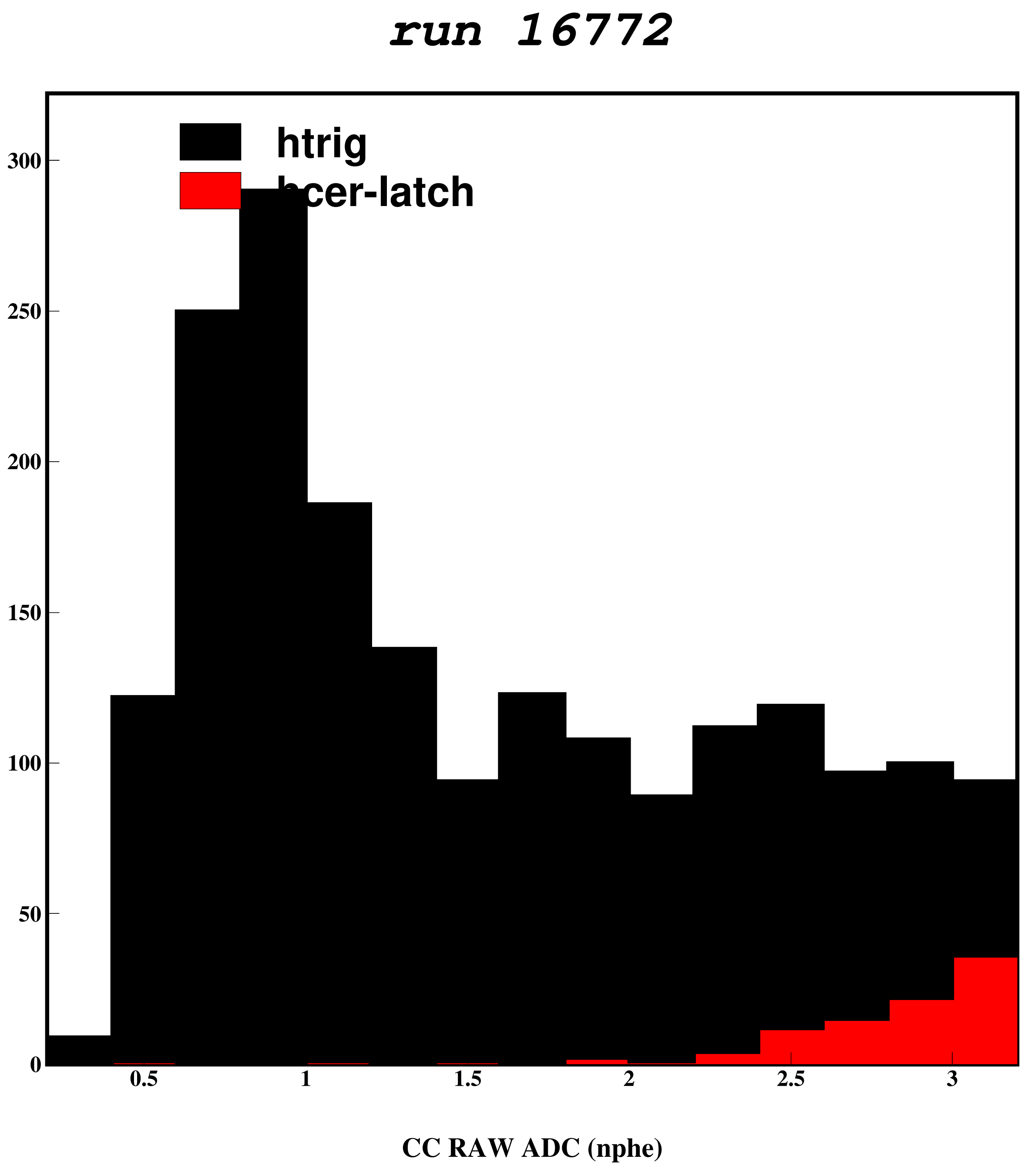


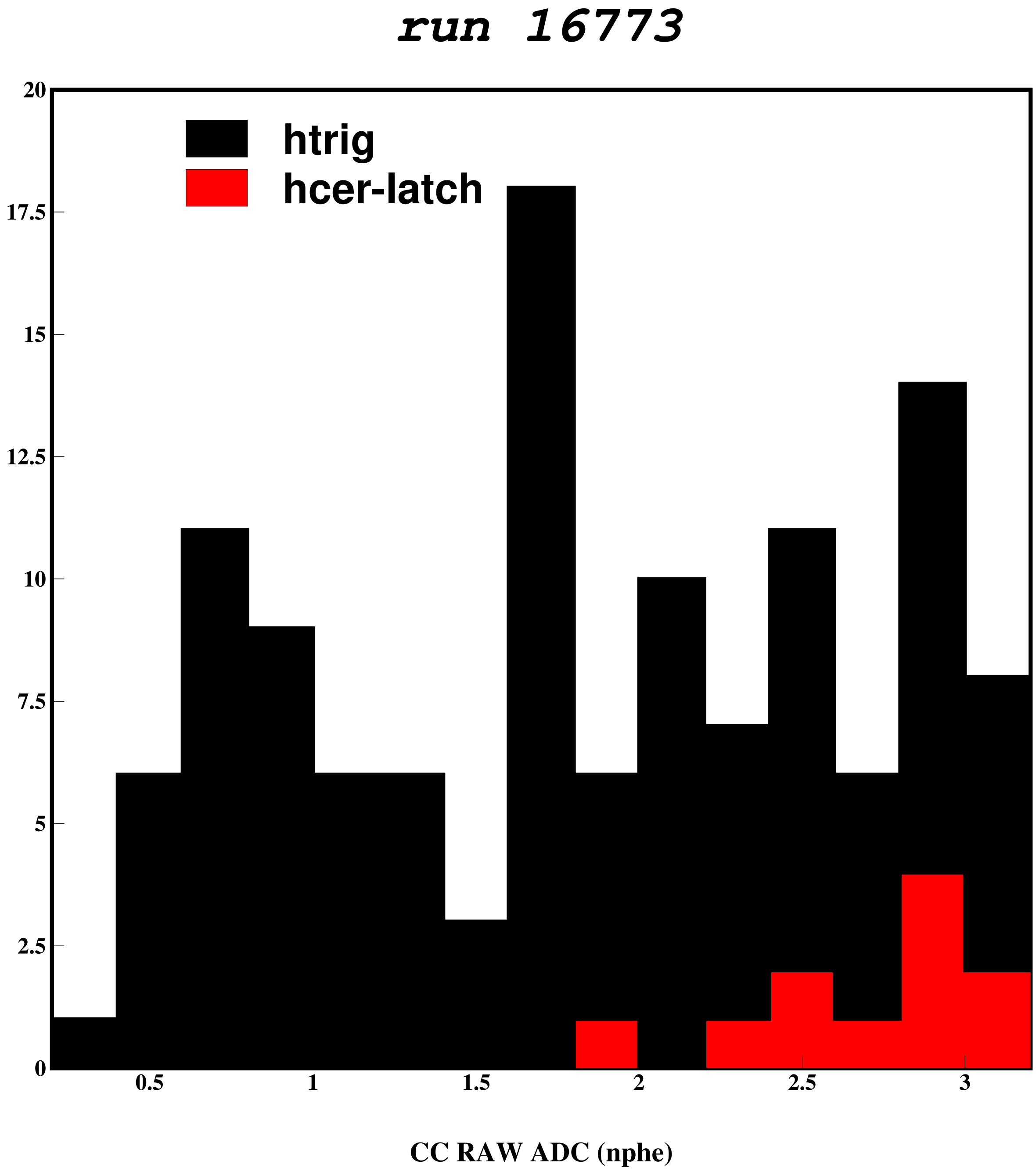
run 16643
htrig
hcer-latch
500
400
300
200
100
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)


run 16649
htrig
hcer-latch
0
50
100
150
200
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

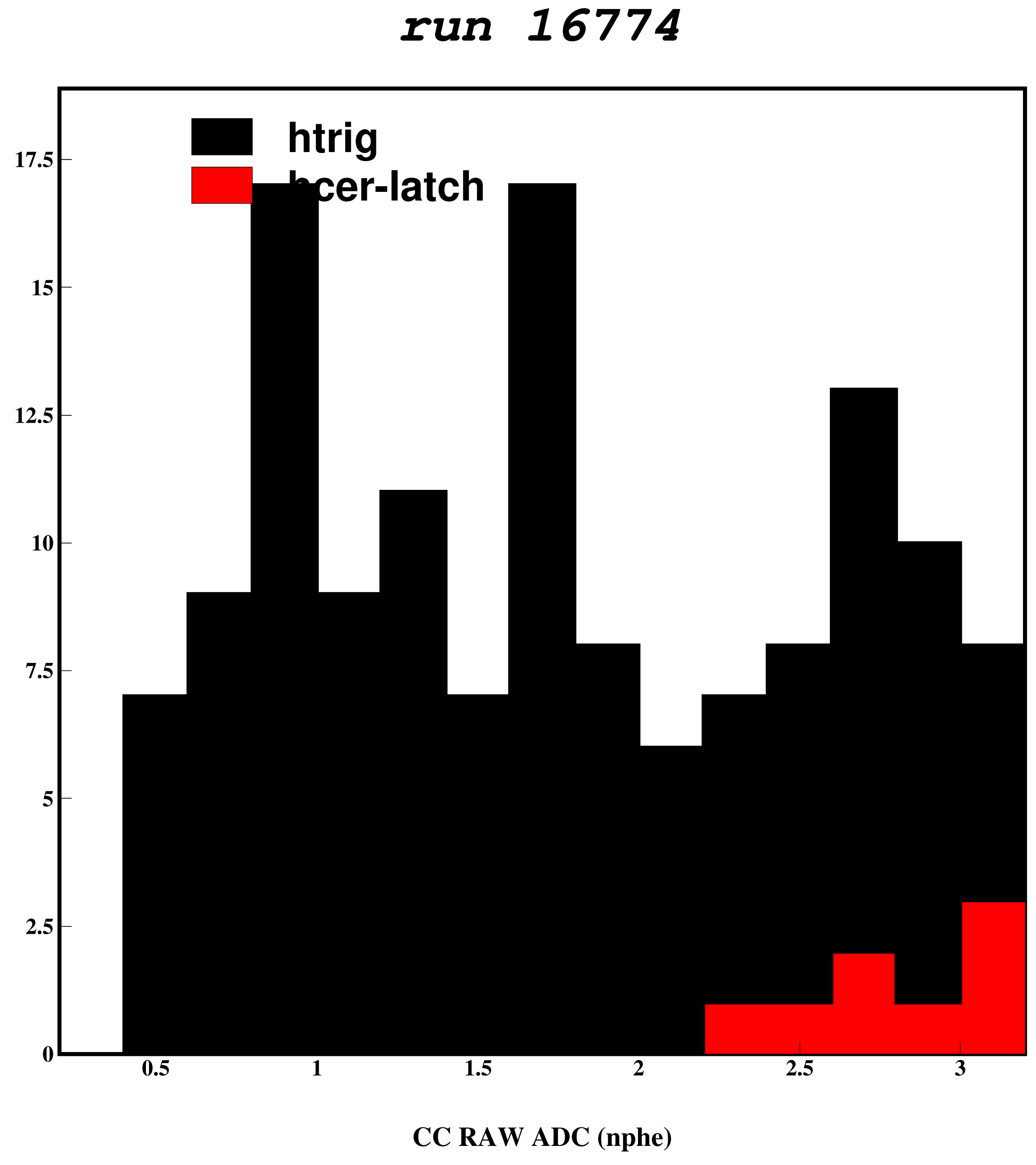

run 16769
htrig
hcer-latch
0
20
40
60
80
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

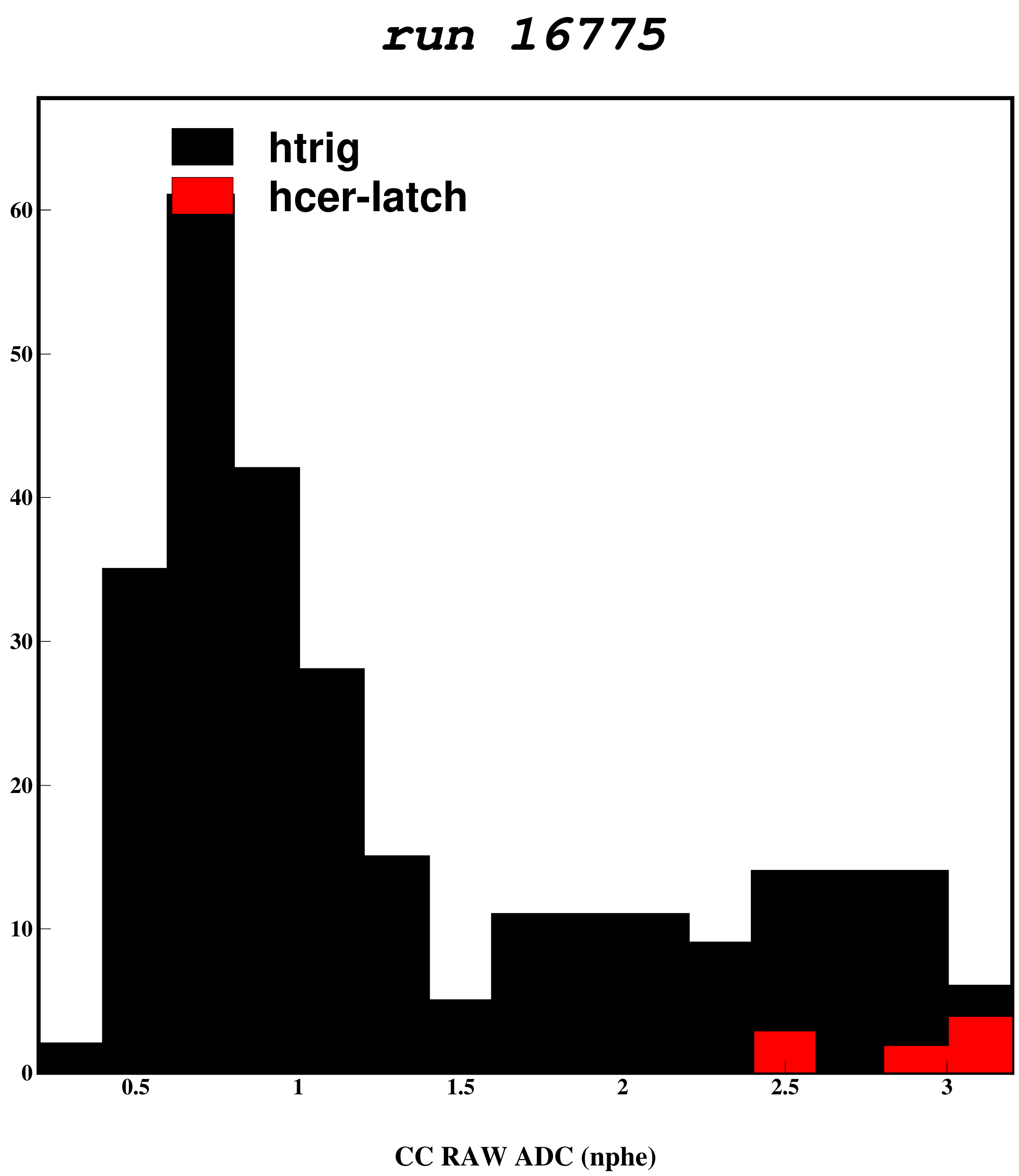

run 16771
htrig
hcer-latch
70
60
50
40
30
20
10
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

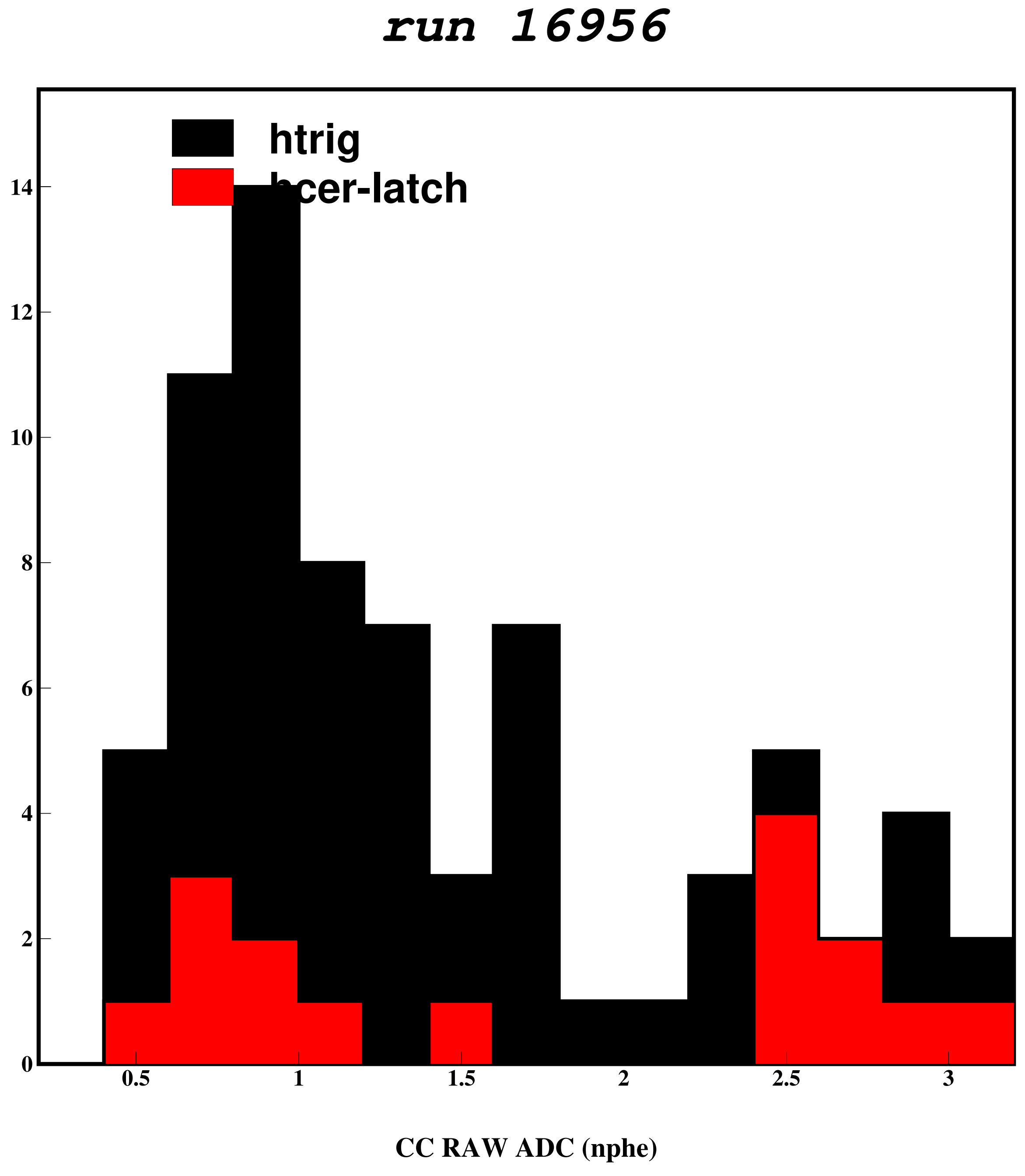

run 16772
htrig
hcer-latch
300
250
200
150
100
50
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

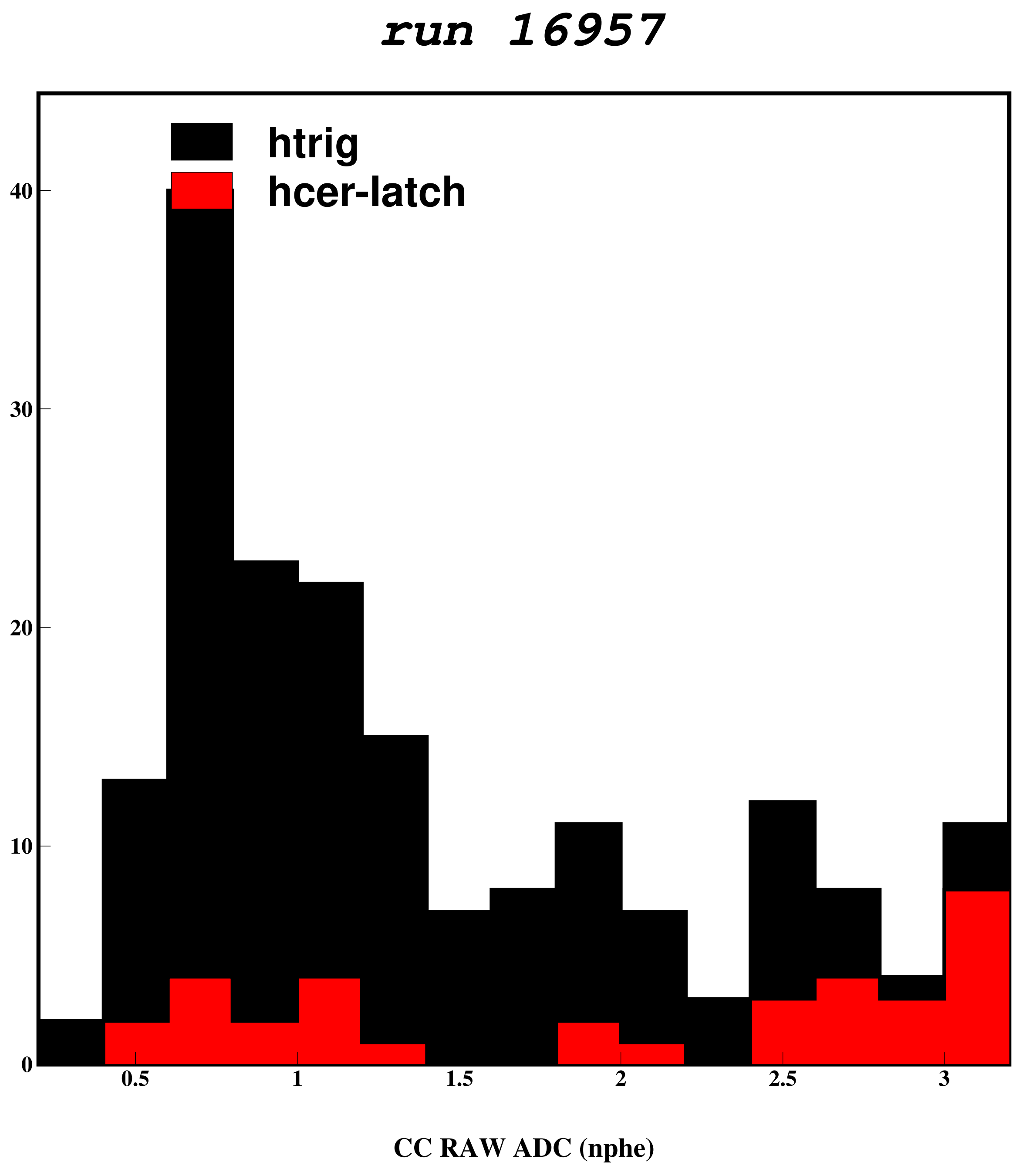


run 16773
20
17.5
15
12.5
10
7.5
5
2.5
0
htrig
hcer-latch
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

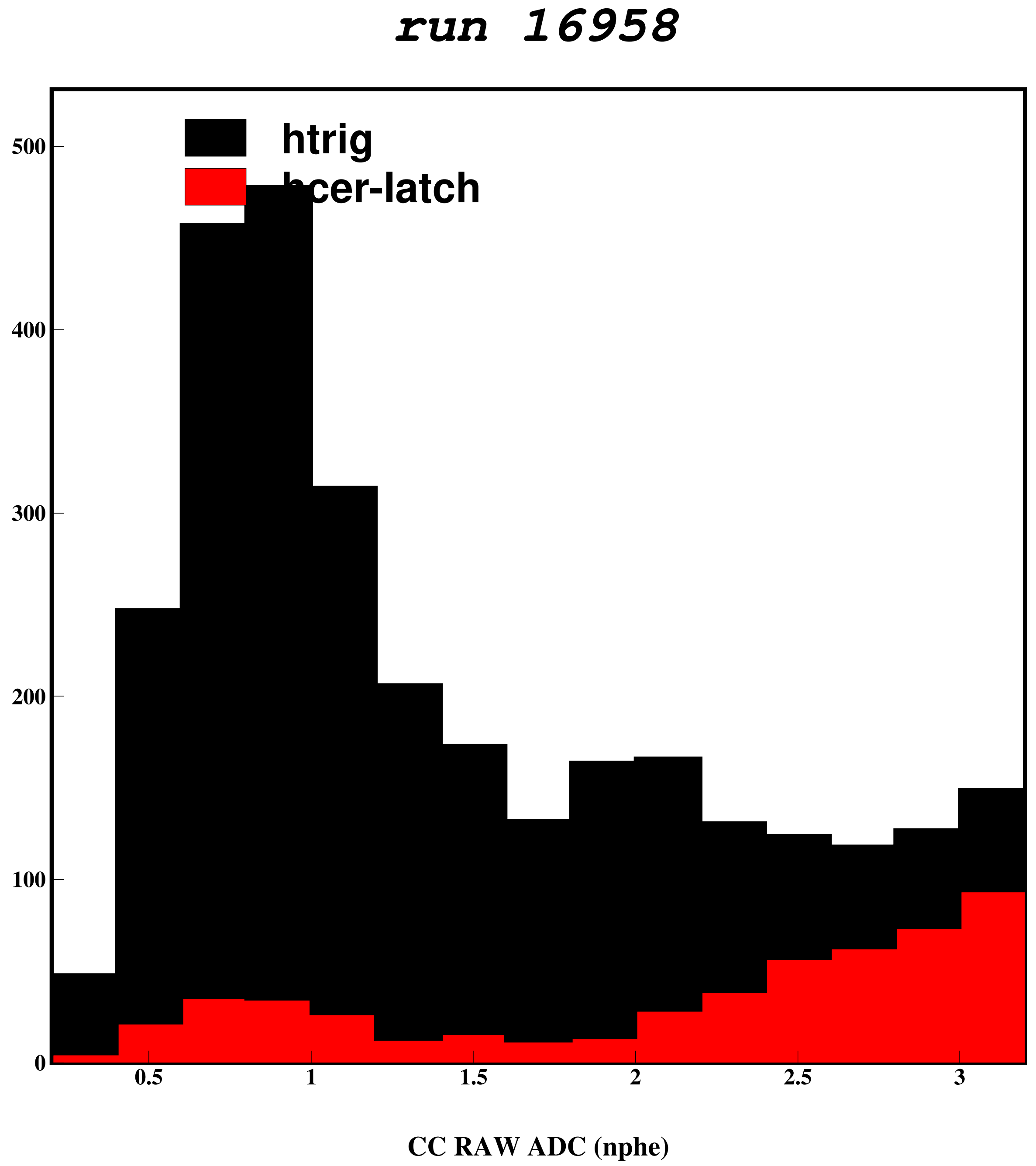

run 16774
htrig
hcer-latch
17.5
15
12.5
10
7.5
5
2.5
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

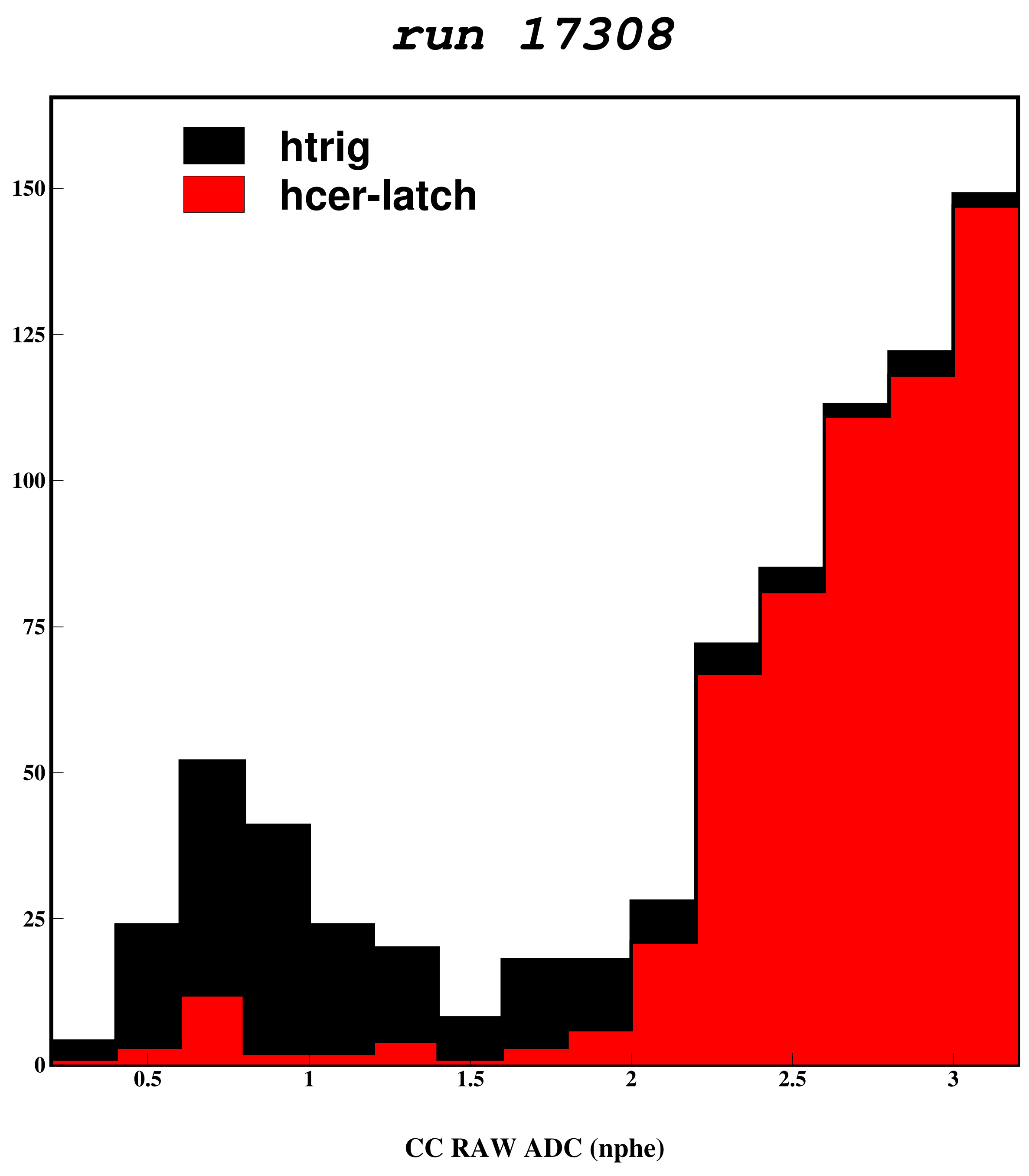

run 16775
htrig
hcer-latch
0
10
20
30
40
50
60
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)


run 16956
htrig
hcer-latch
14
12
10
8
6
4
2
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

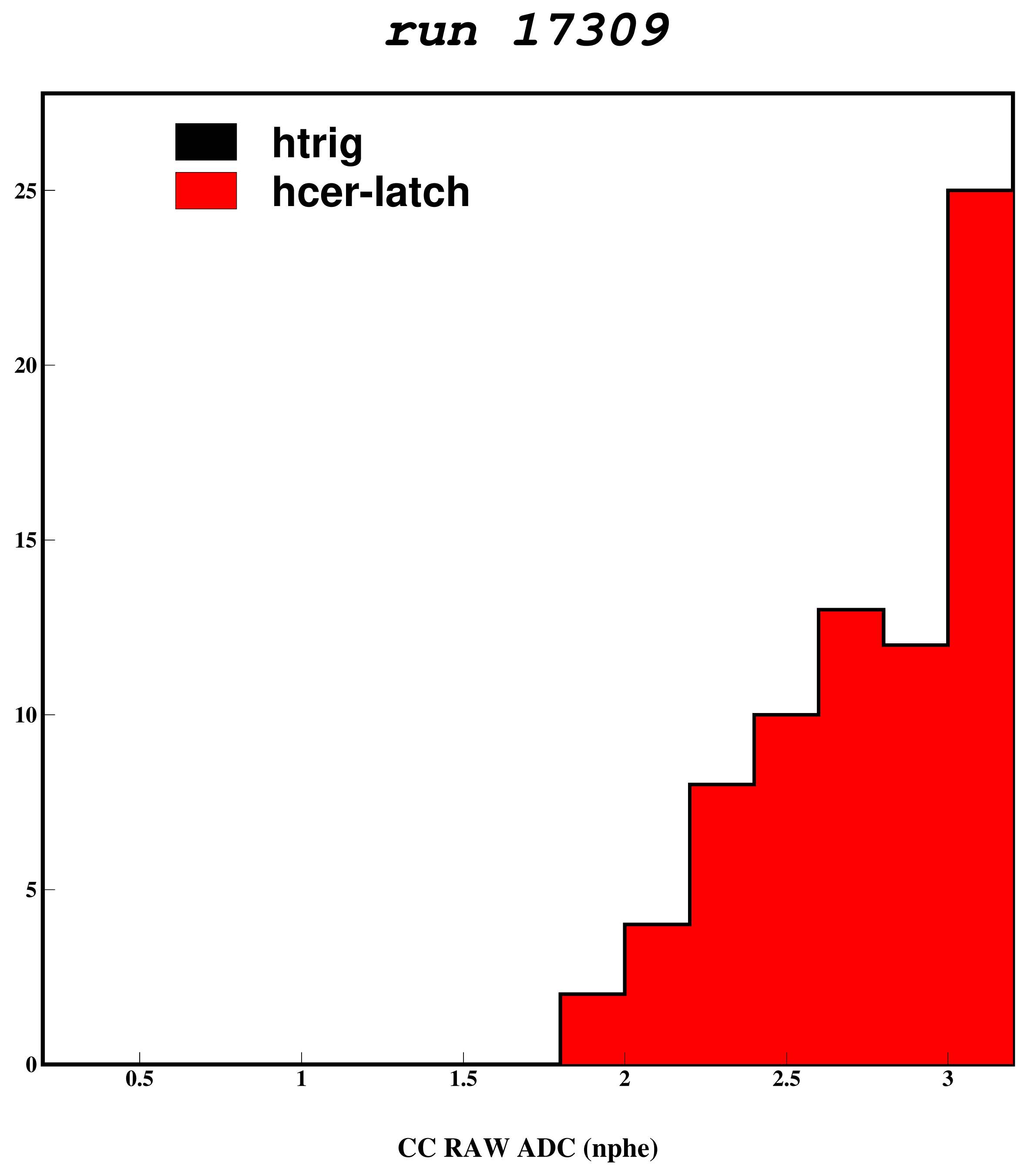


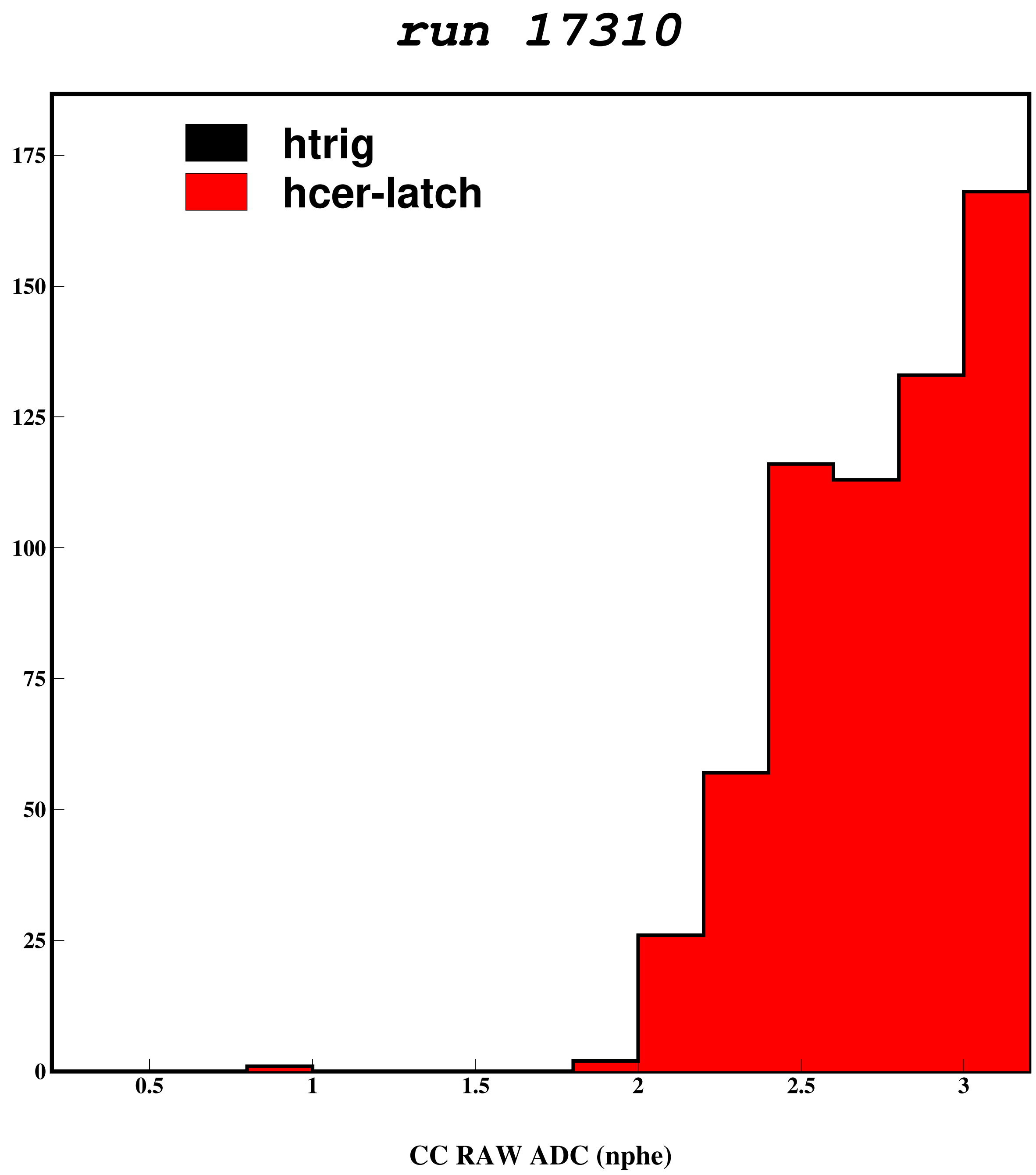
run 16957
htrig
hcer-latch
0
10
20
30
40
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)


run 16958
htrig
hcer-latch
500
400
300
200
100
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

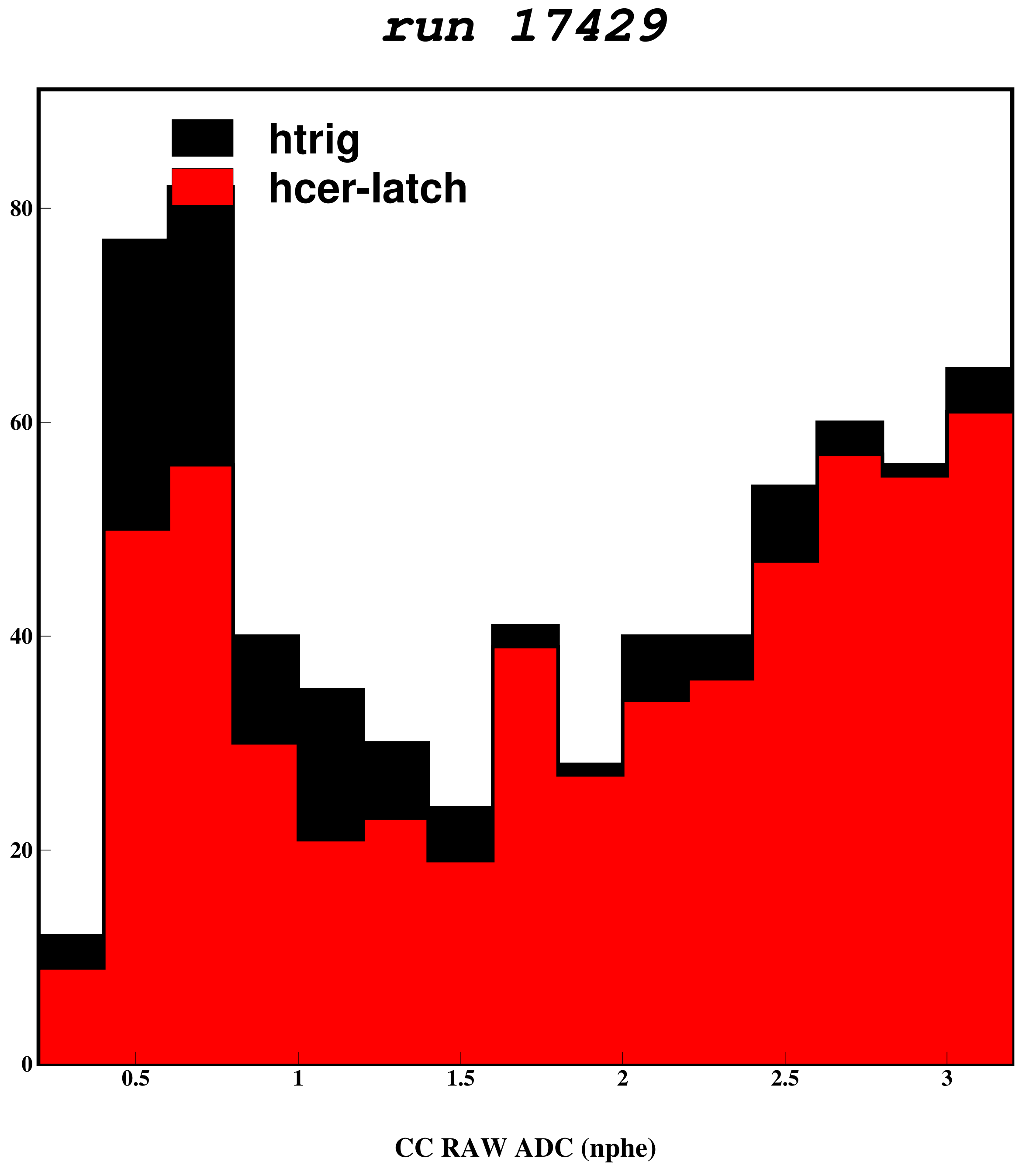


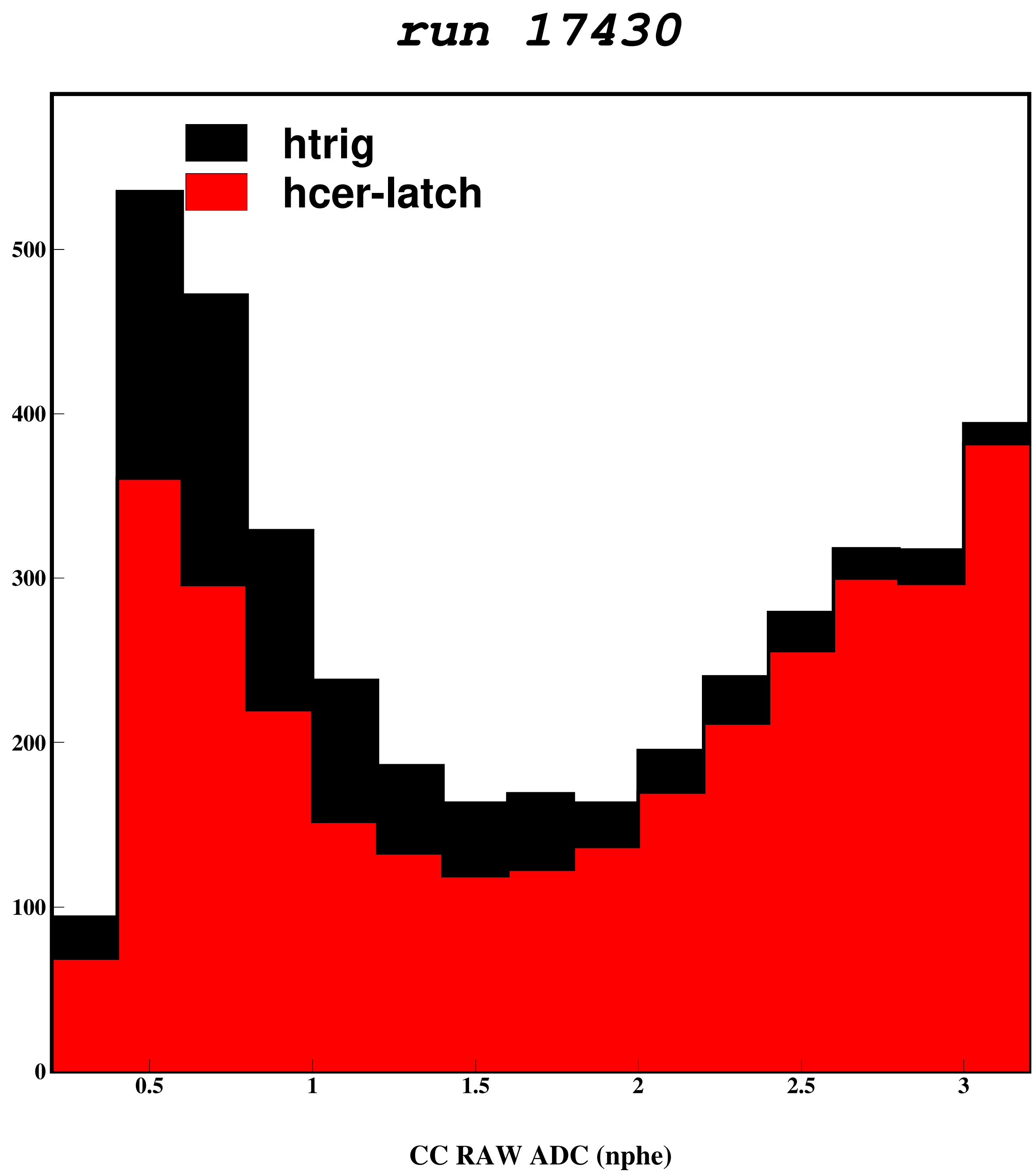
run 17308
htrig
hcer-latch
150
125
100
75
50
25
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

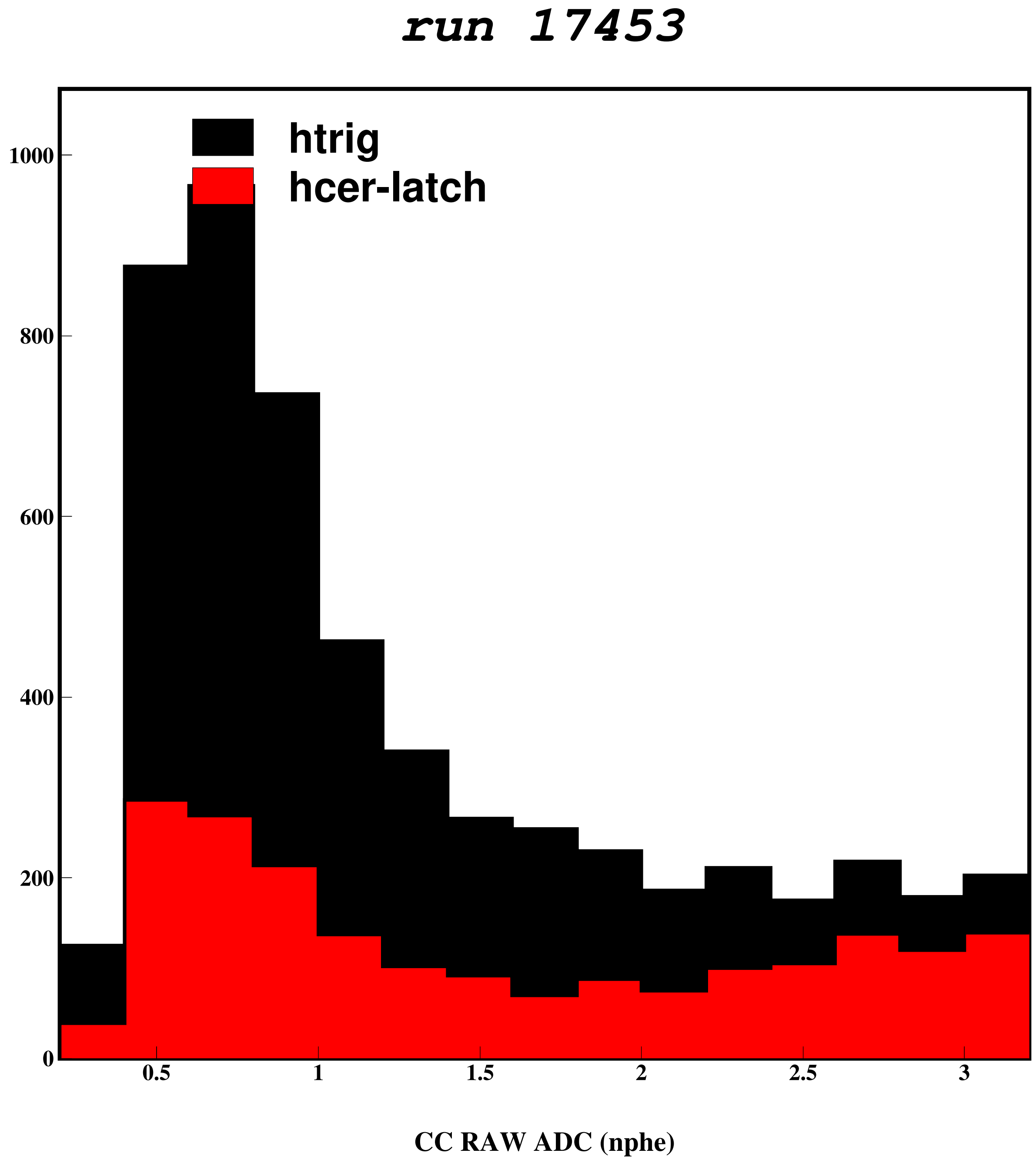

run 17309
htrig
hcer-latch
0
5
10
15
20
25
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

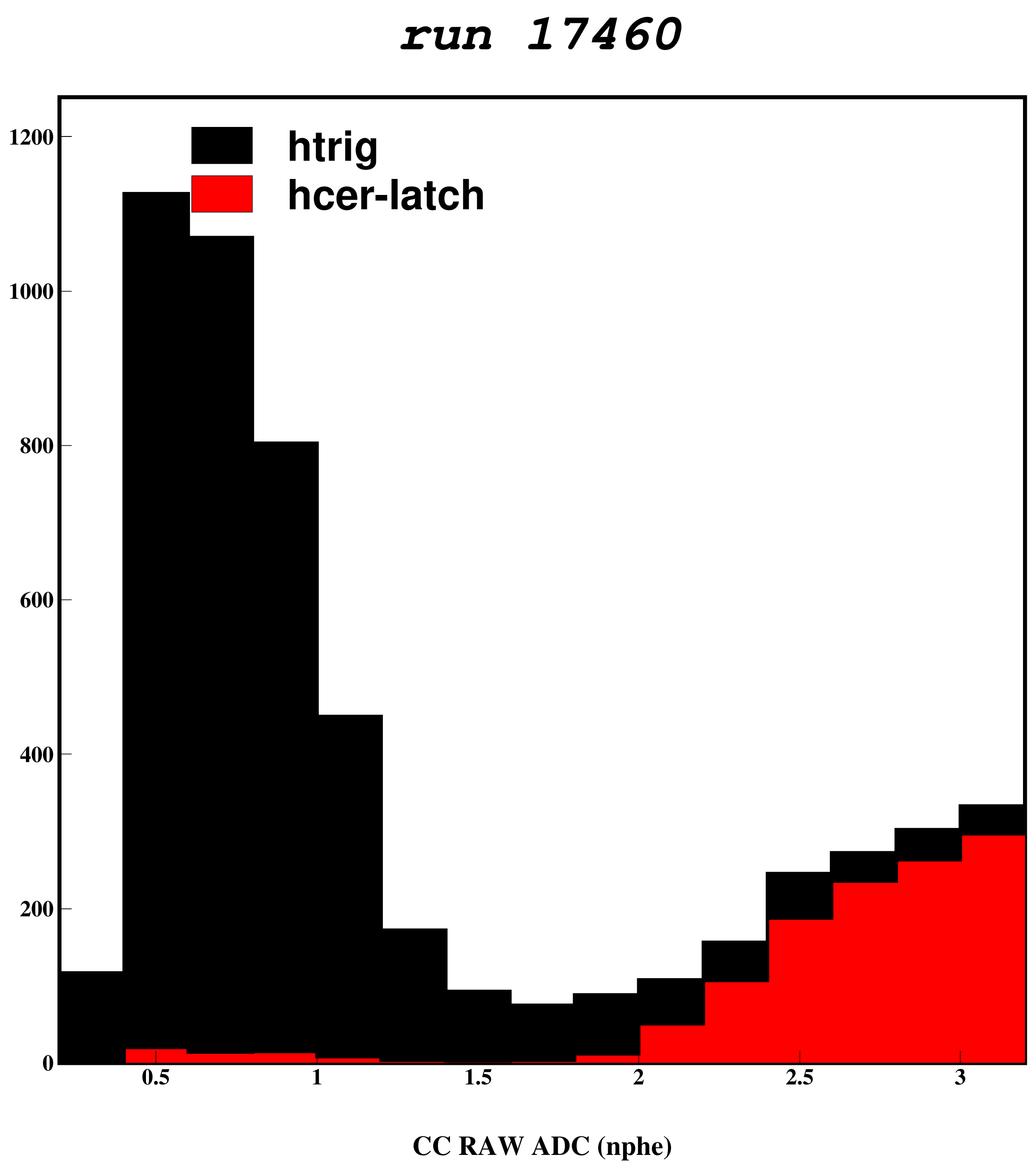


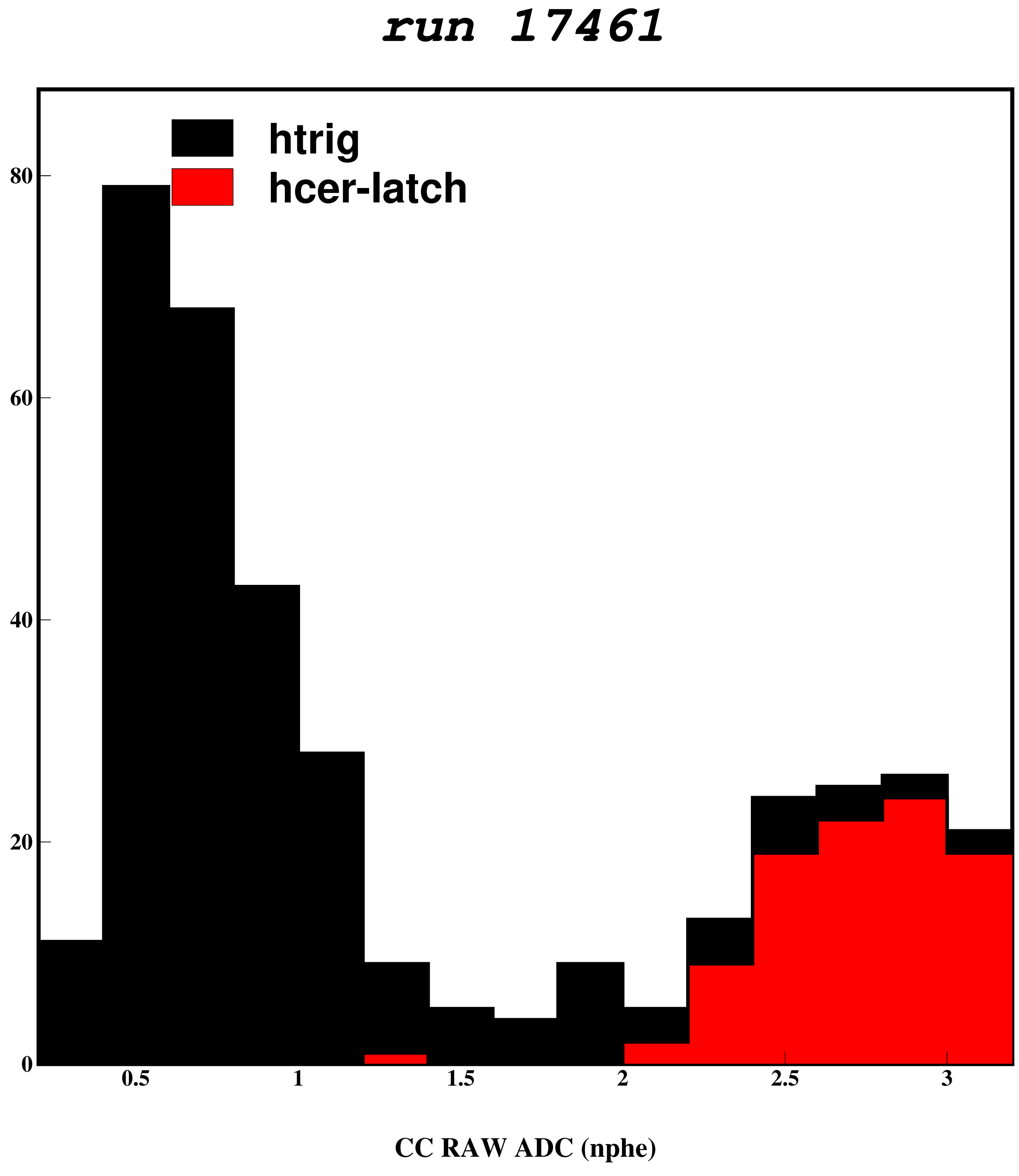
run 17310
htrig
hcer-latch
175
150
125
100
75
50
25
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

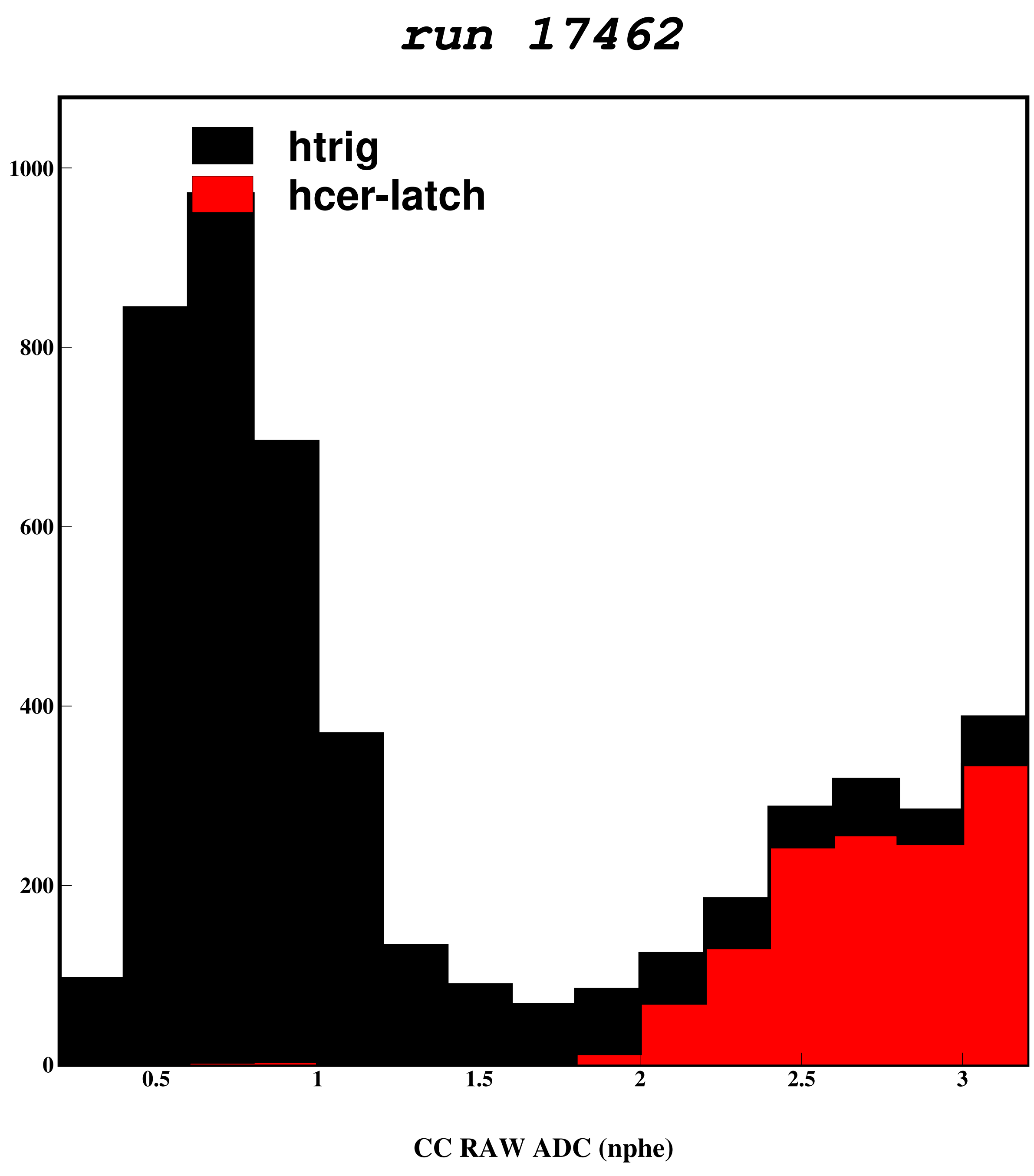

run 17429
htrig
hcer-latch
80
60
40
20
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

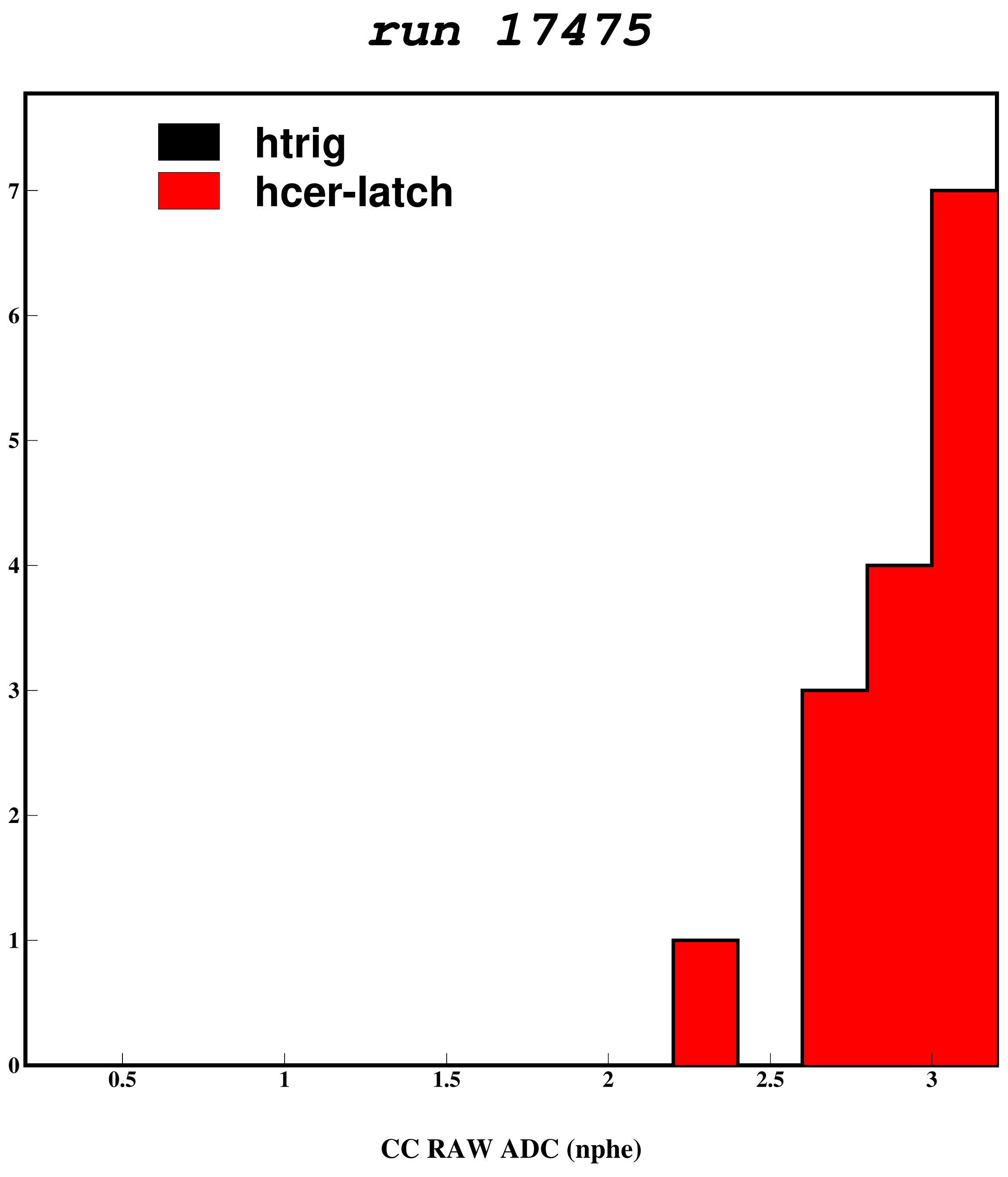

run 17430
htrig
hcer-latch
500
400
300
200
100
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

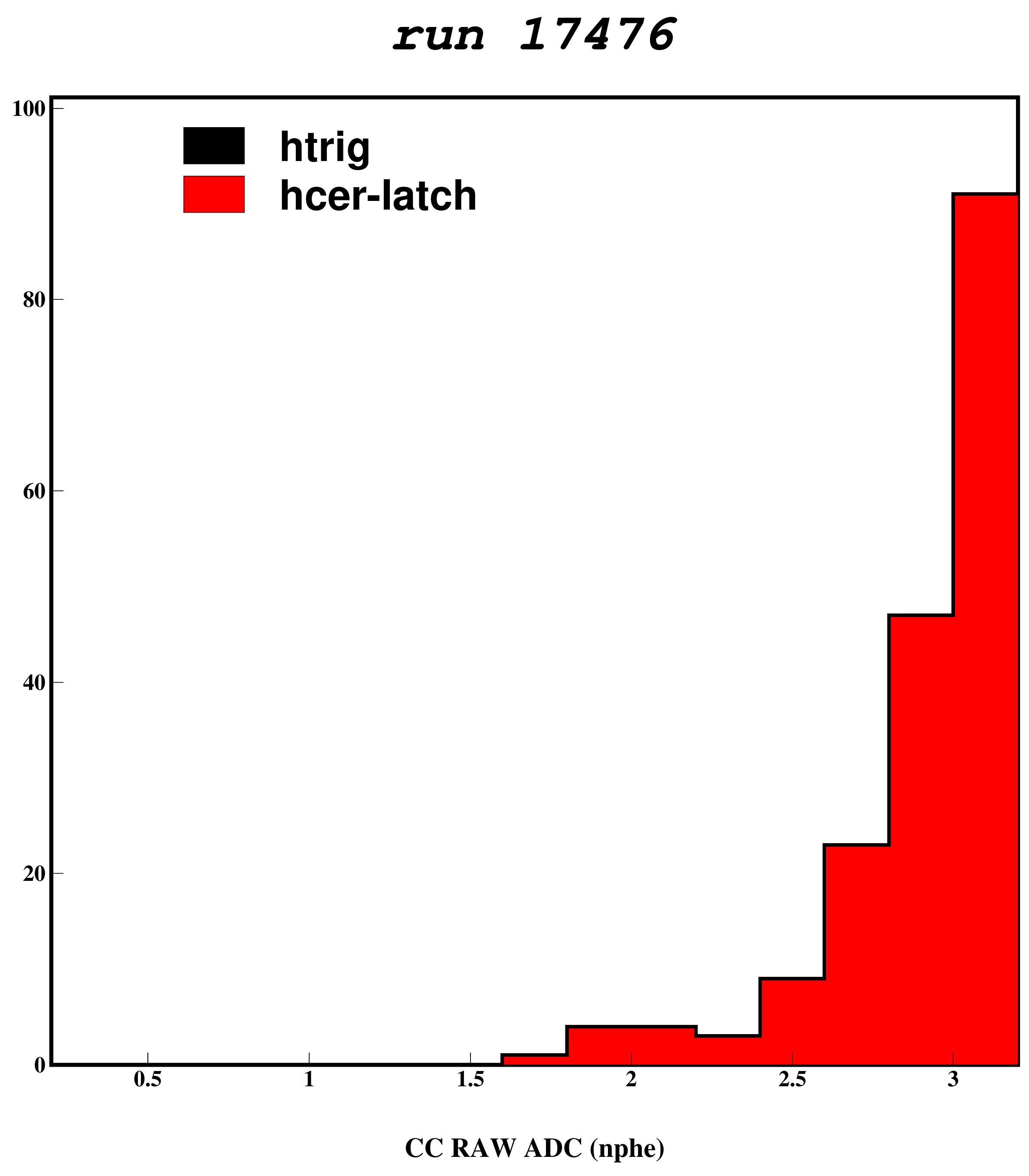

run 17453
htrig
hcer-latch
1000
800
600
400
200
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)


run 17460
htrig
hcer-latch
0
200
400
600
800
1000
1200
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

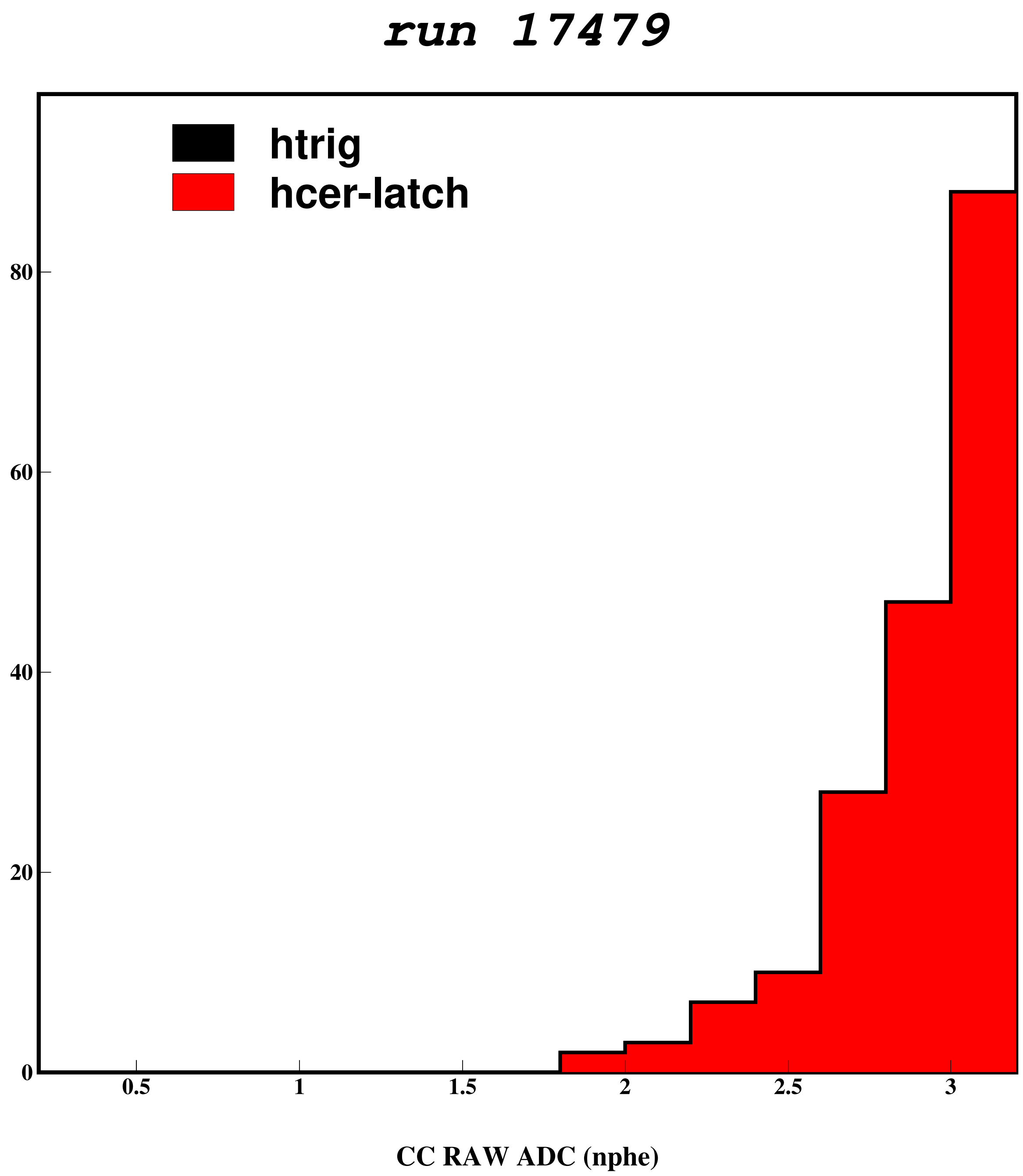

run 17461
htrig
hcer-latch
80
60
40
20
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)


run 17462
htrig
hcer-latch
0
200
400
600
800
1000
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

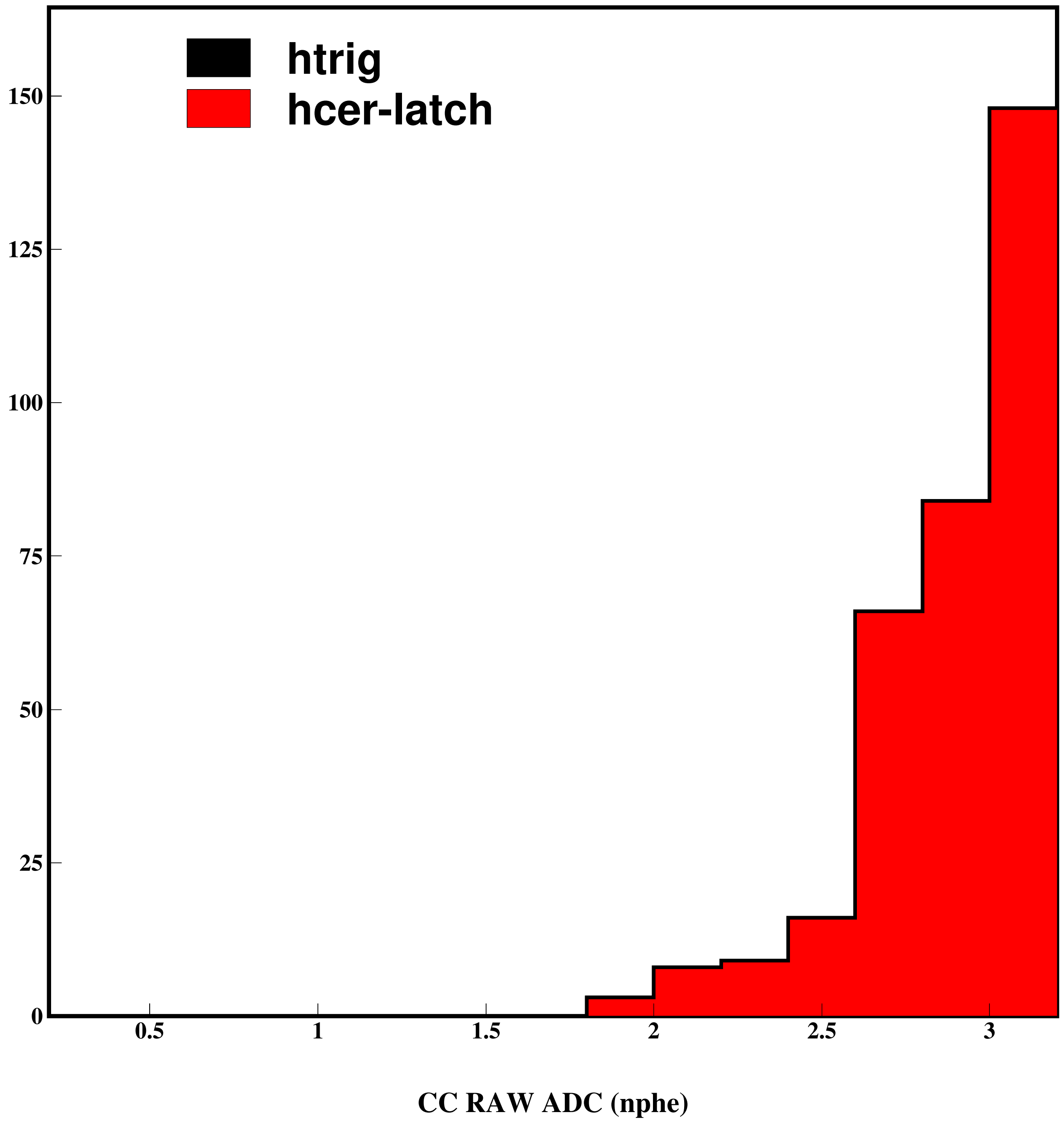

run 17475
htrig
hcer-latch
0
1
2
3
4
5
6
7
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)



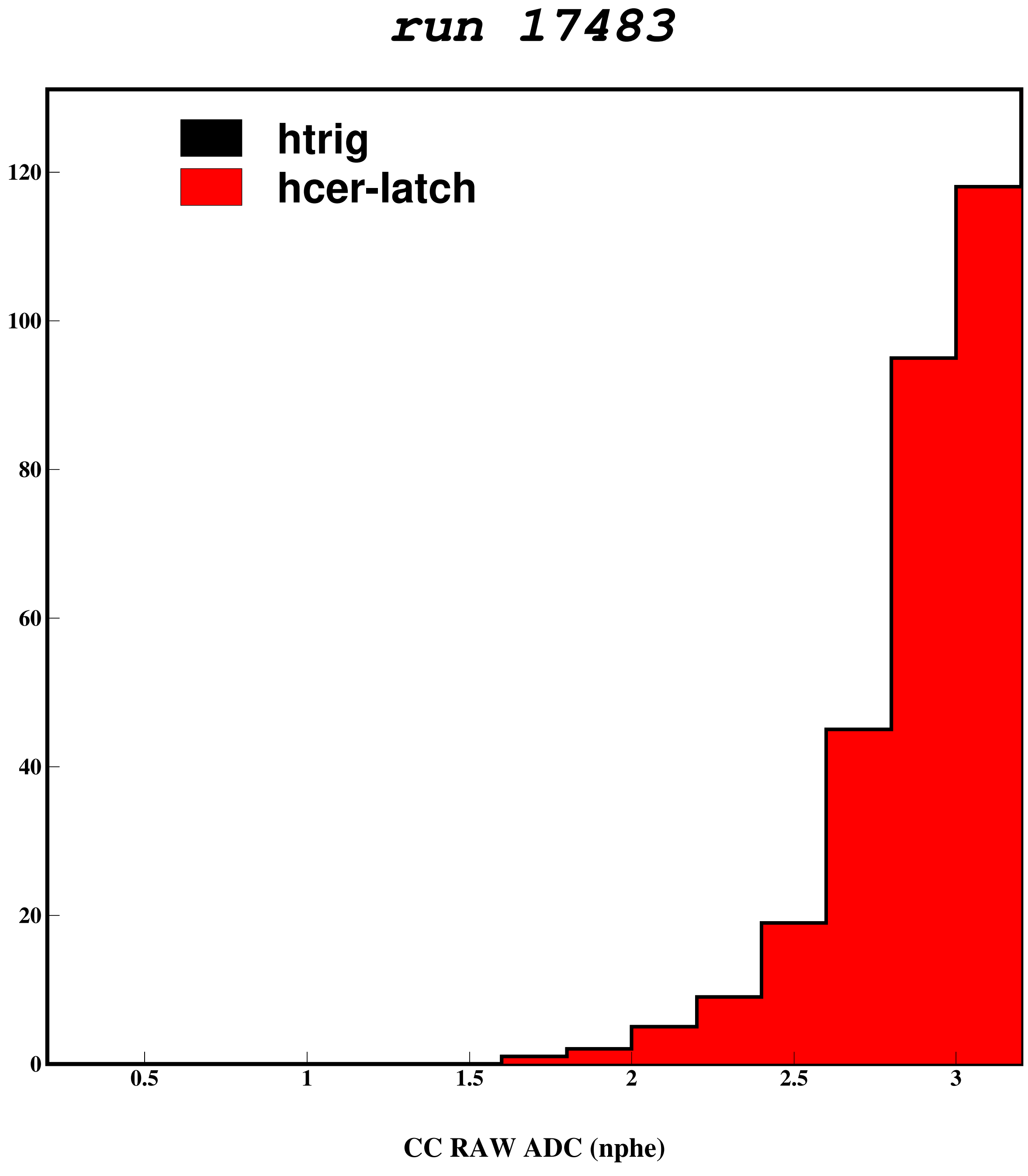
run 17476
htrig
hcer-latch
0
20
40
60
80
100
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)


run 17479
htrig
hcer-latch
80
60
40
20
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

run 17481

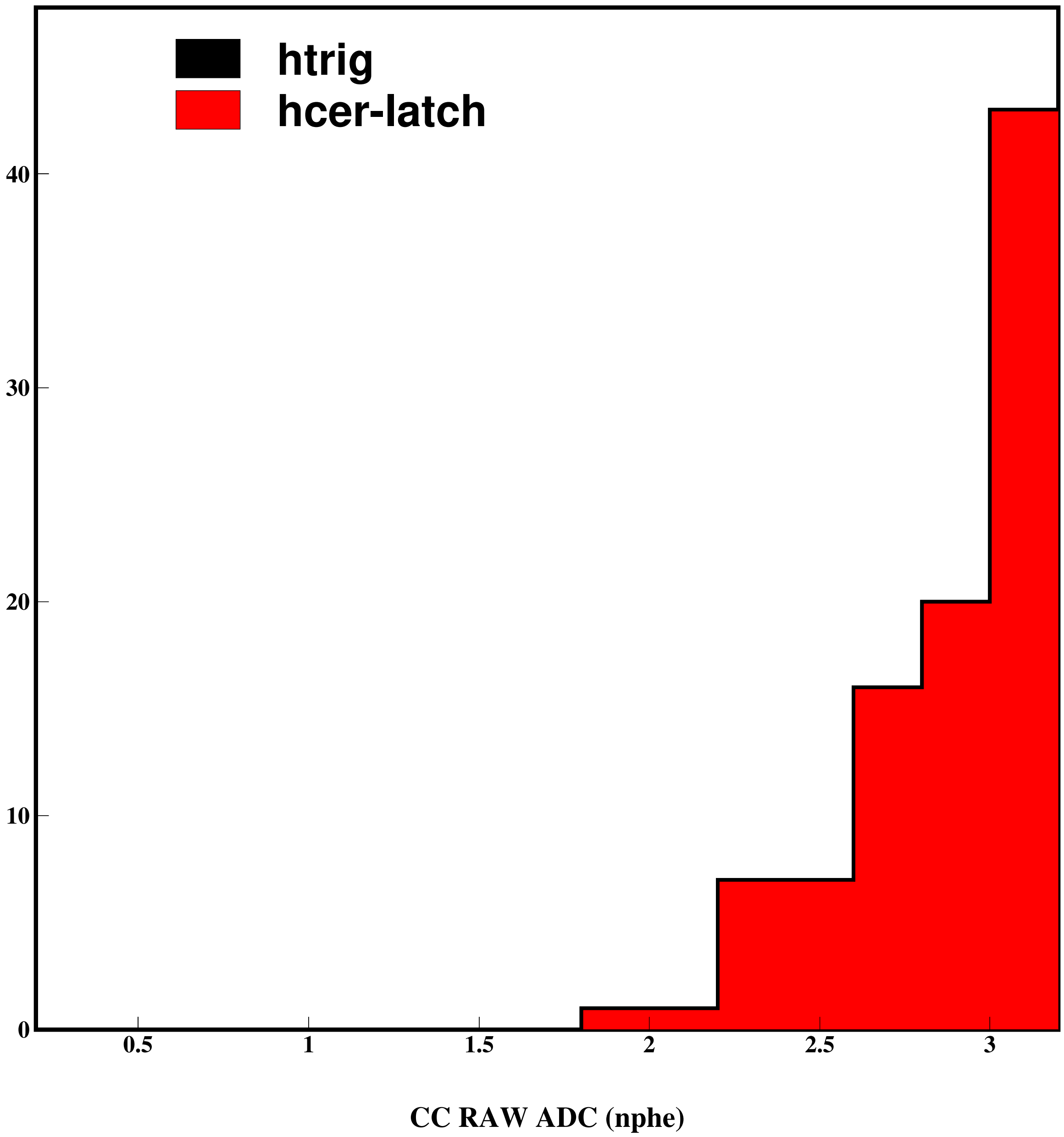


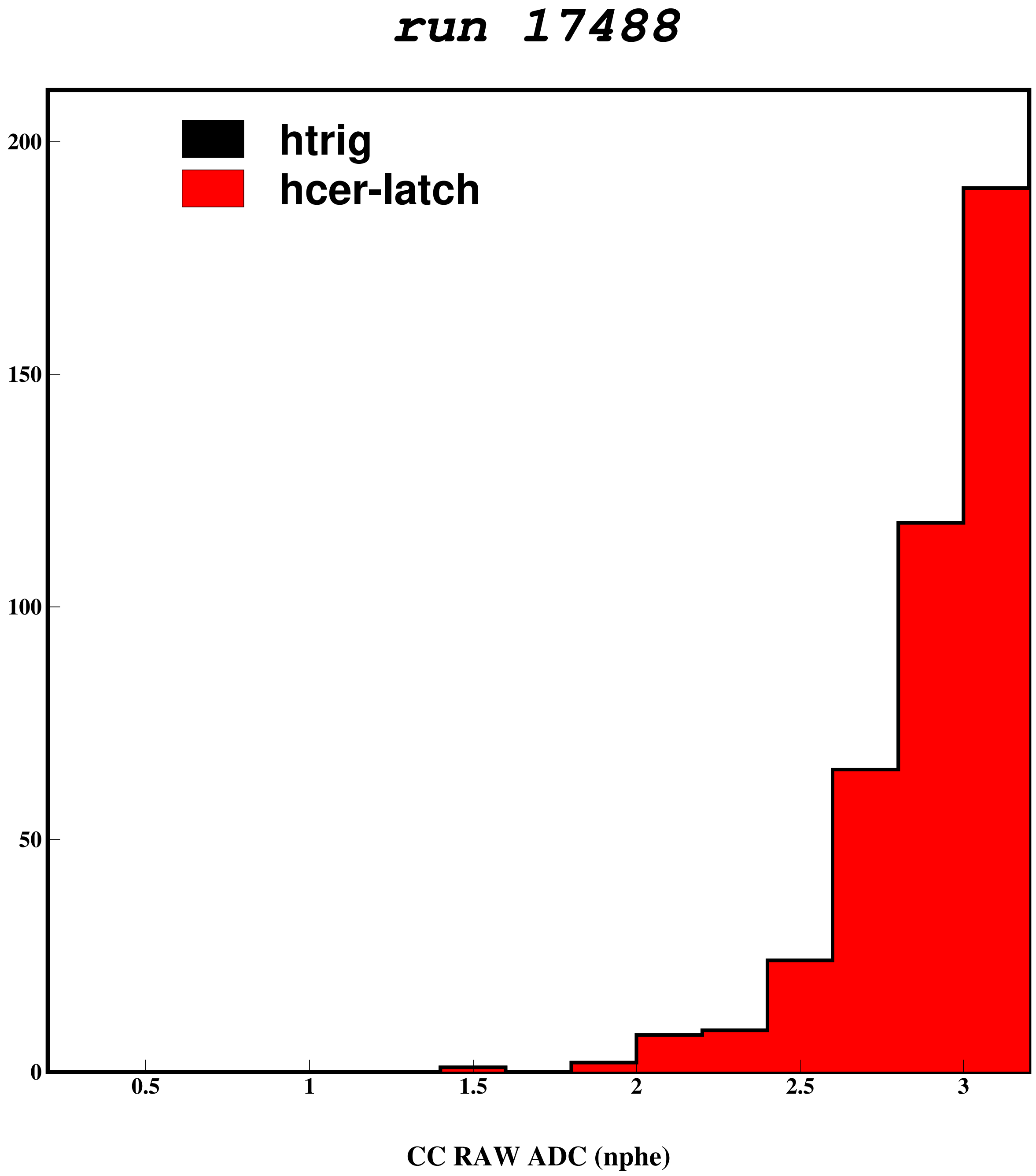
htrig
hcer-latch
150
125
100
75
50
25
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)



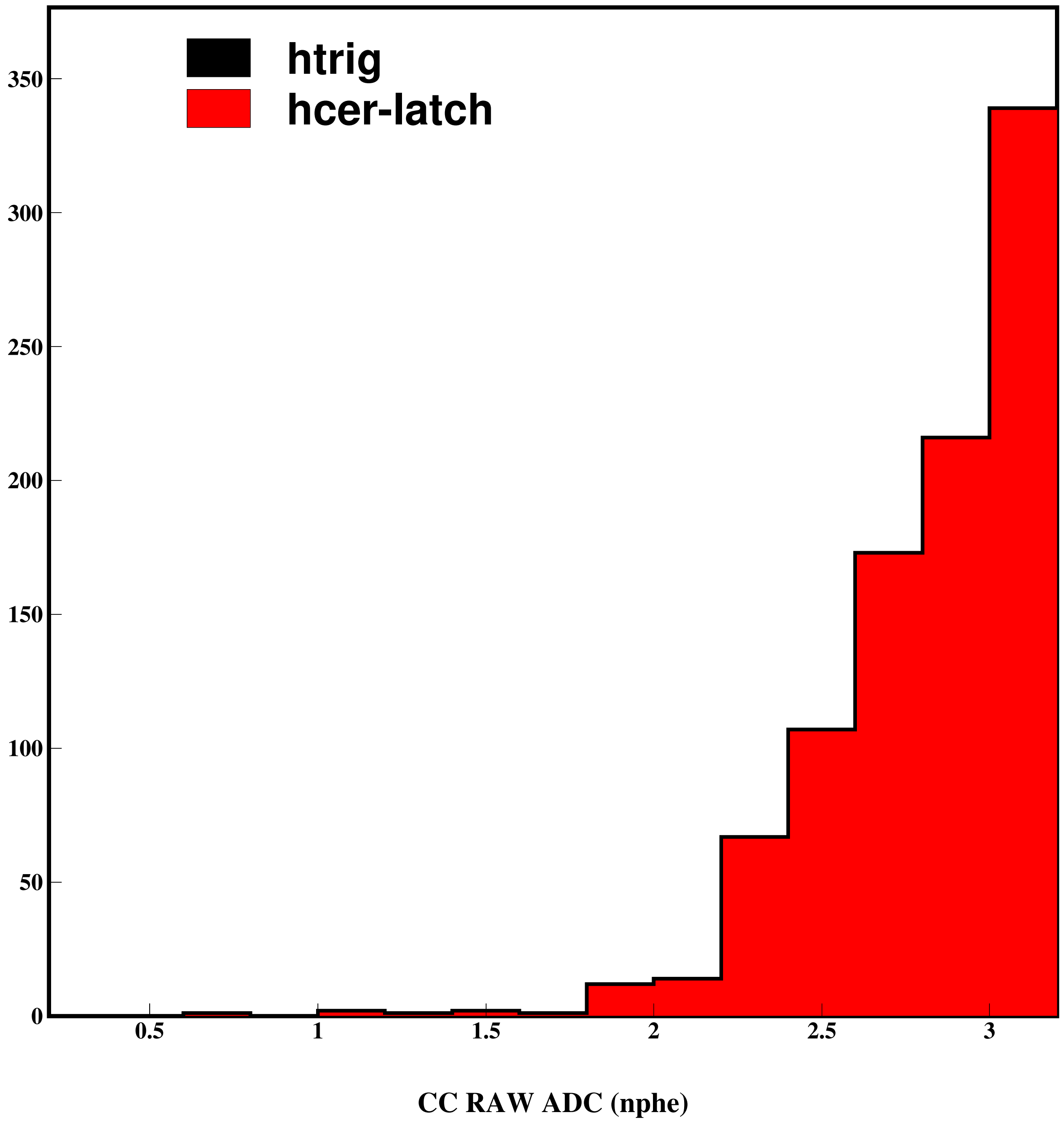
run 17483
htrig
hcer-latch
120
100
80
60
40
20
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

run 17487

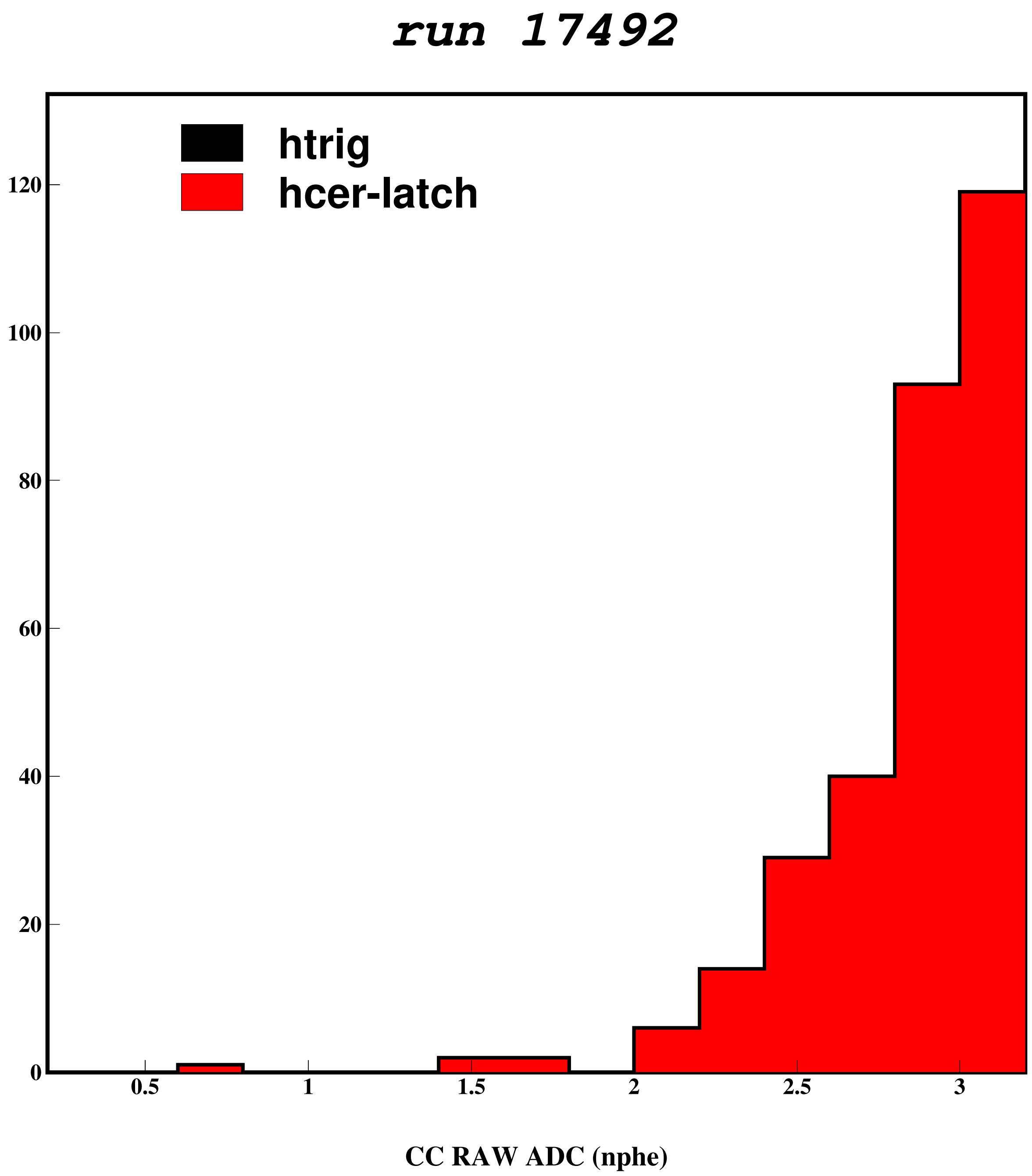



run 17488
htrig
hcer-latch
200
150
100
50
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

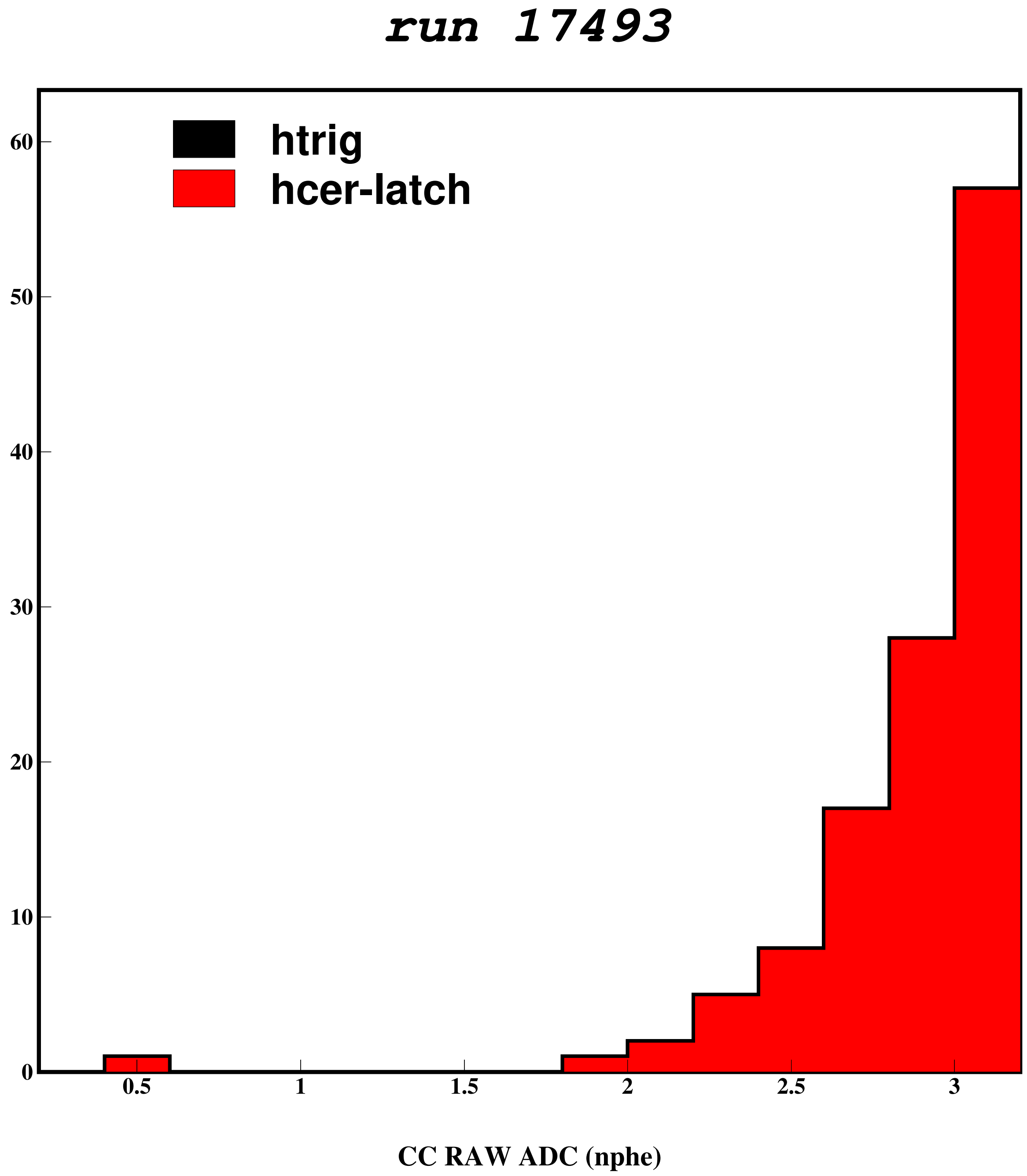
run 17491



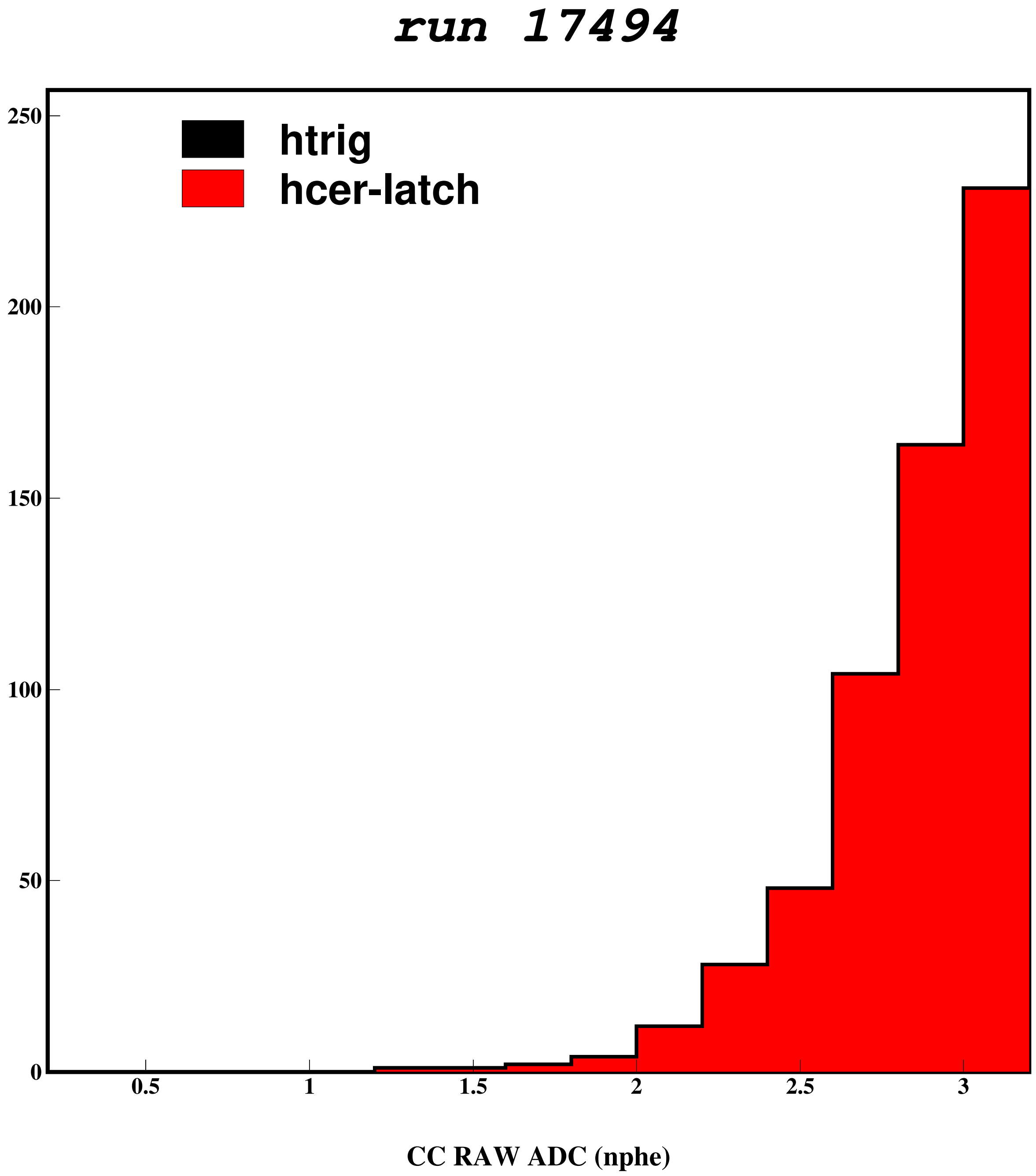
htrig
hcer-latch
350
300
250
200
150
100
50
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

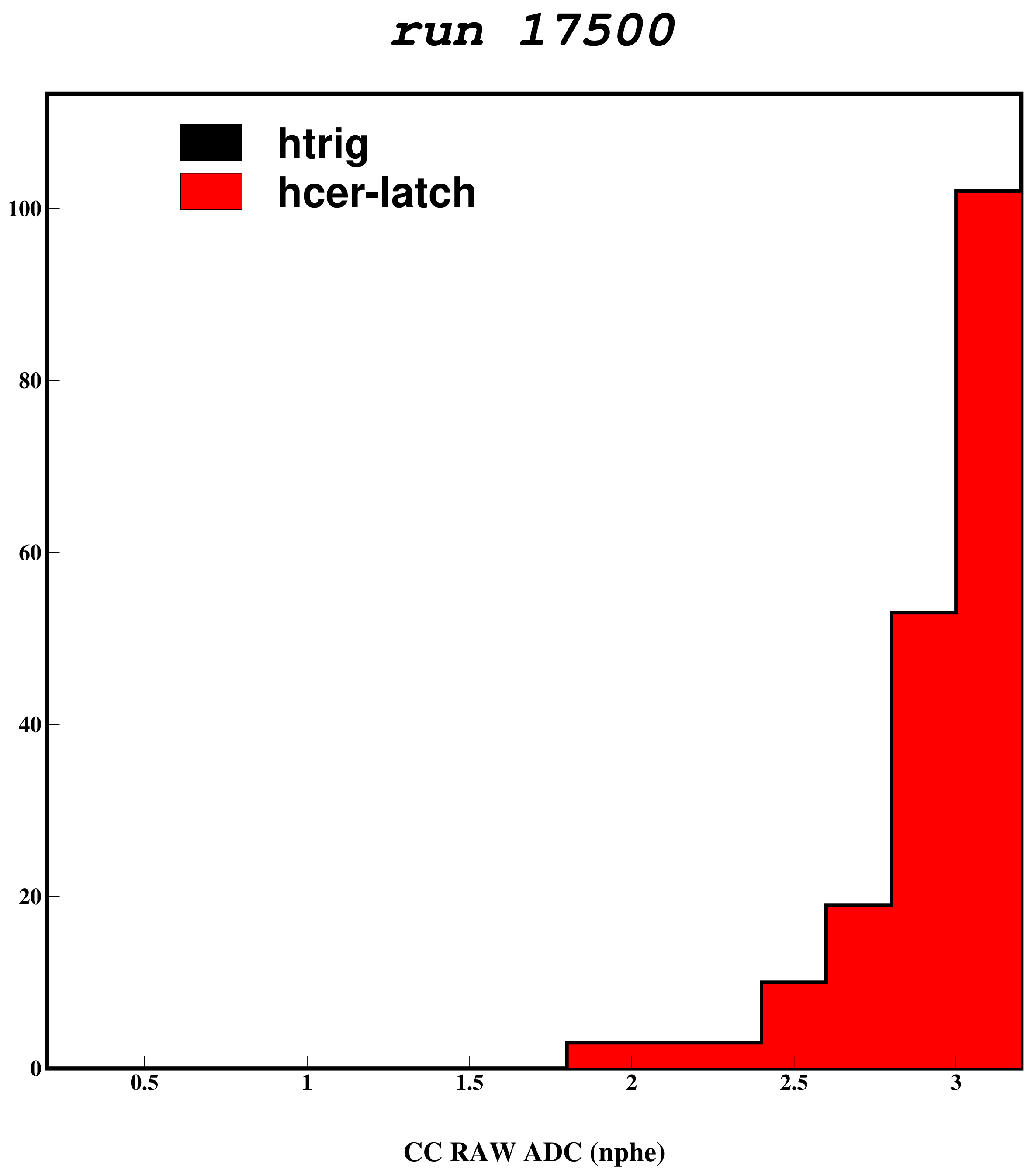


run 17492
htrig
hcer-latch
120
100
80
60
40
20
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

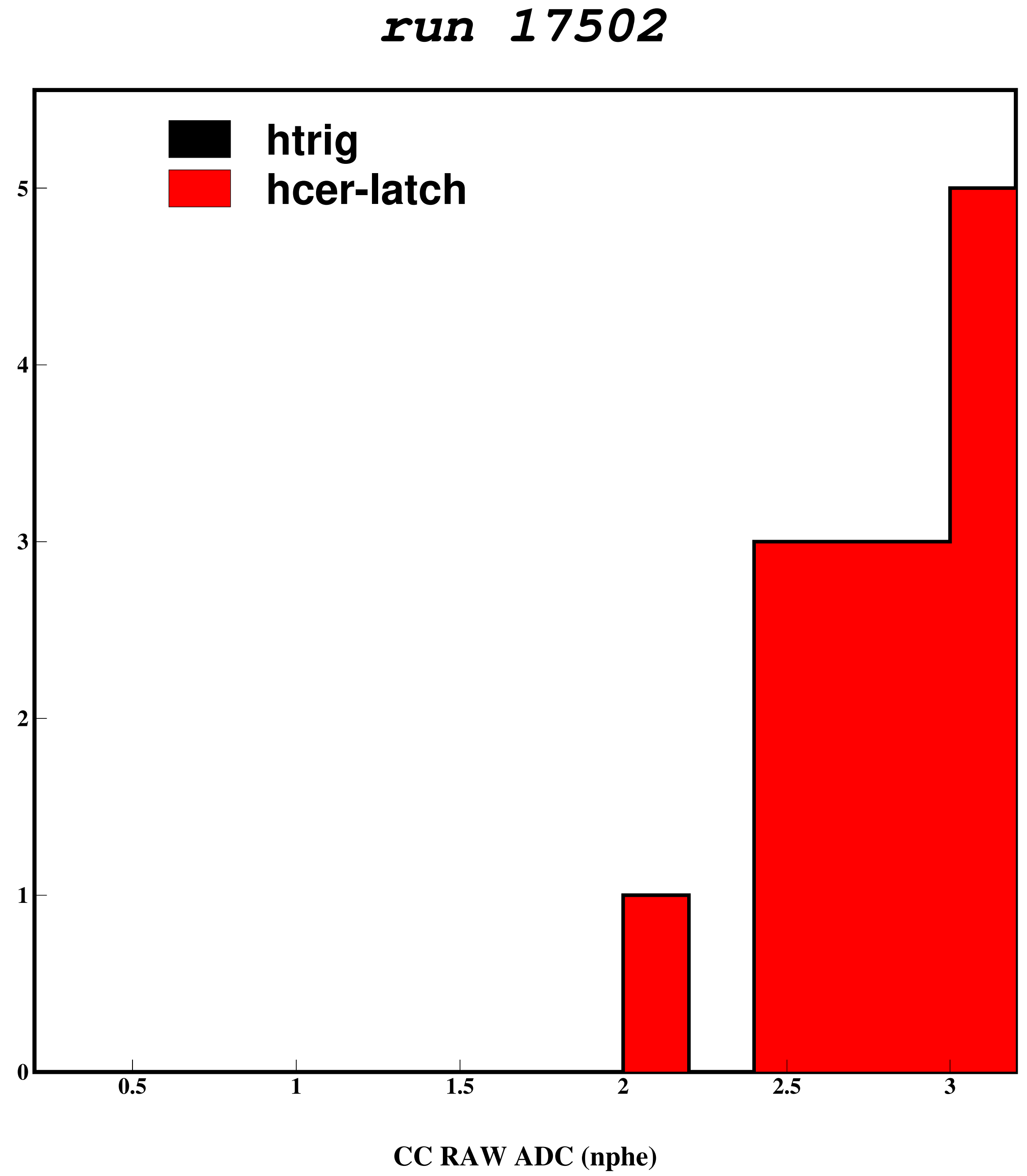


run 17493
htrig
hcer-latch
60
50
40
30
20
10
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

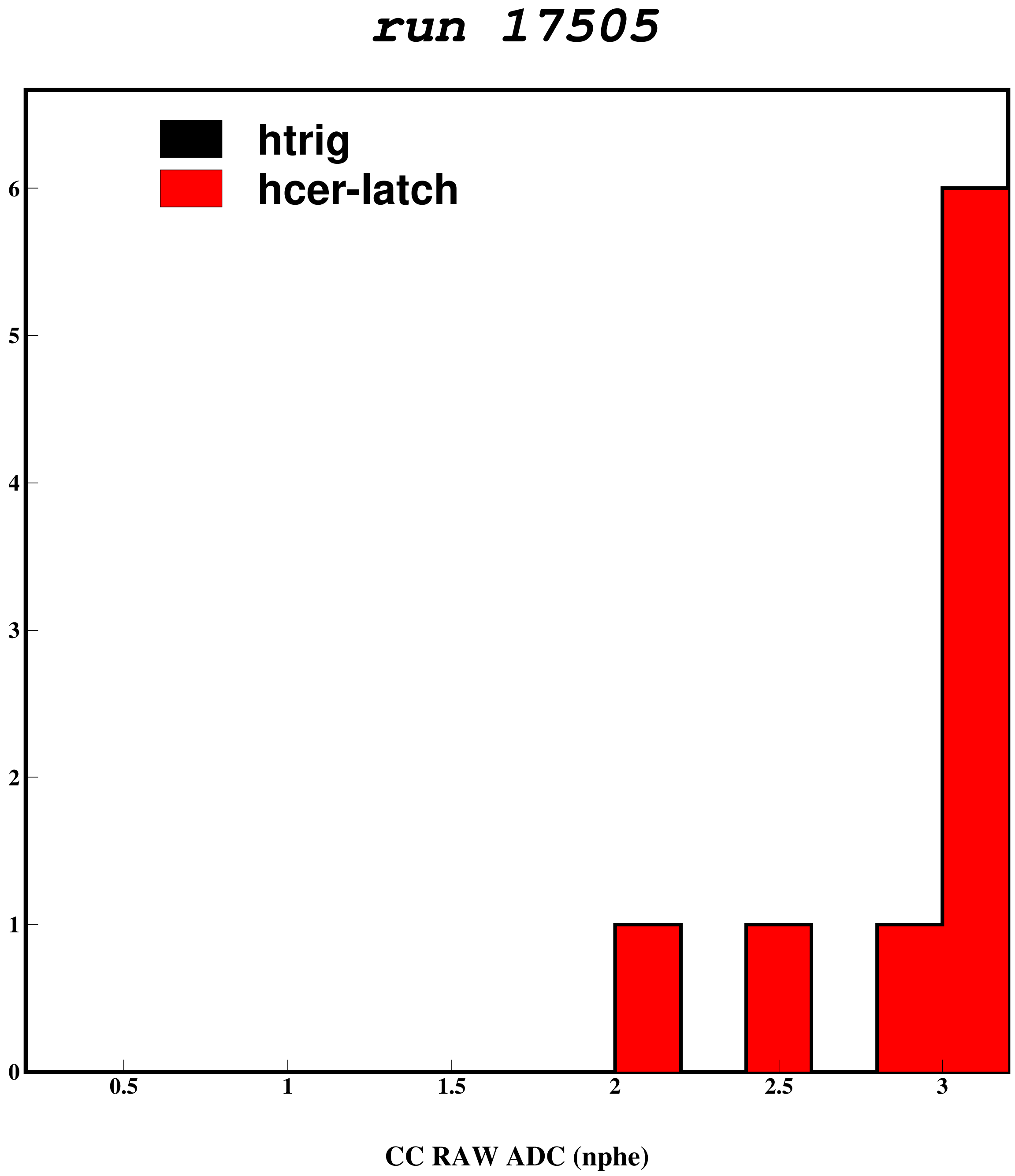

run 17494
htrig
hcer-latch
250
200
150
100
50
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)



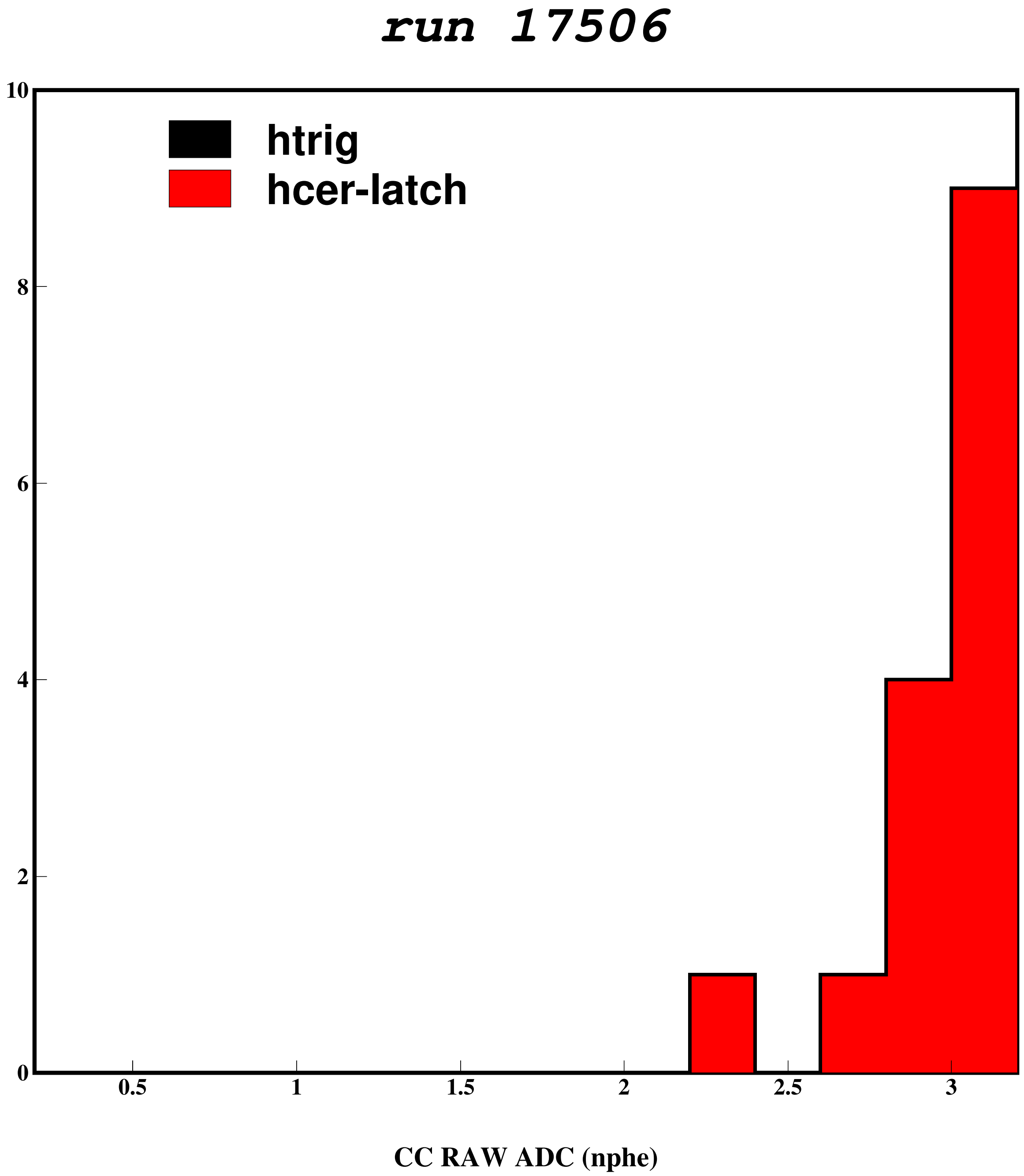
run 17500
htrig
hcer-latch
100
80
60
40
20
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)



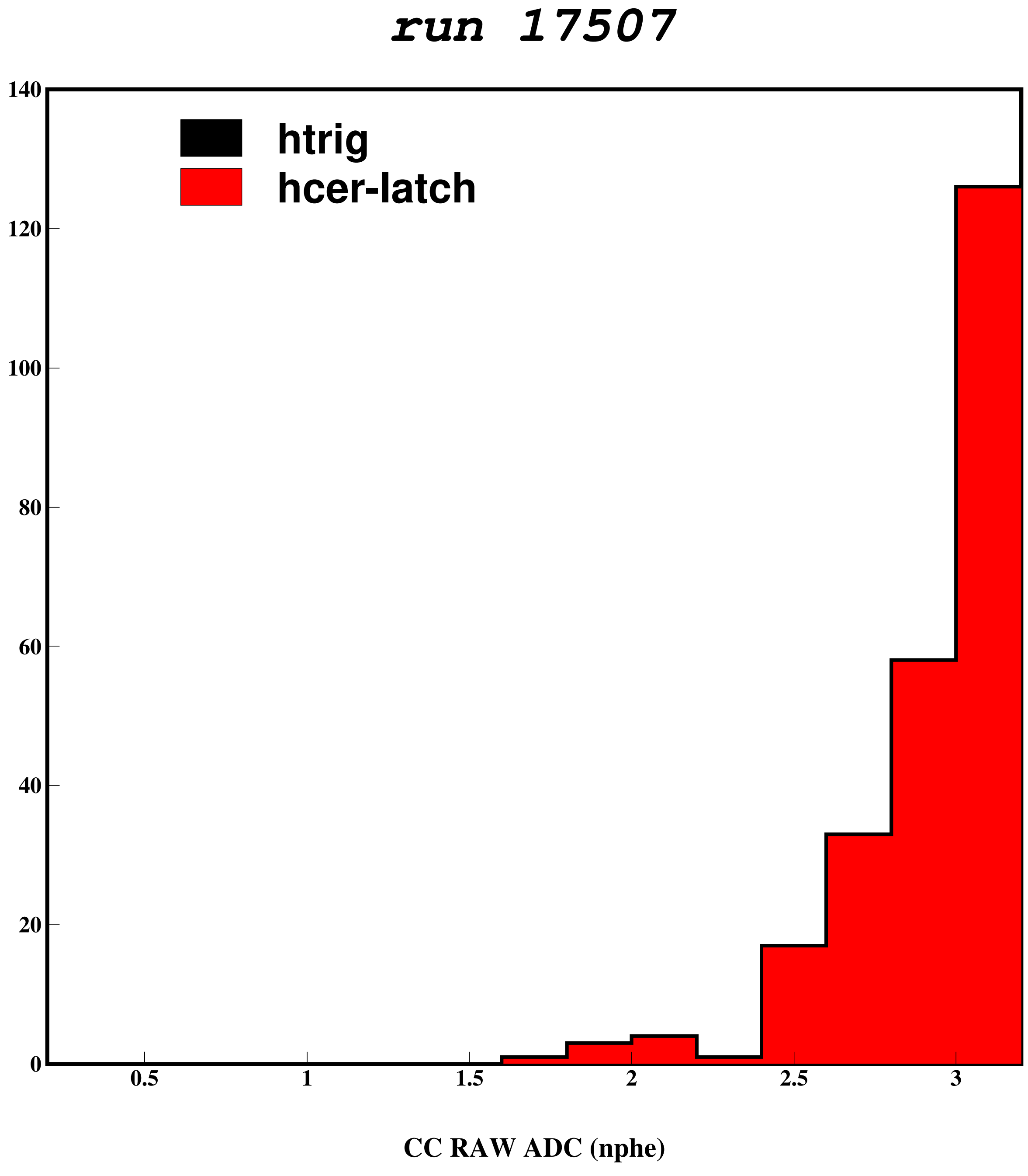
run 17502
htrig
hcer-latch
0
1
2
3
4
5
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

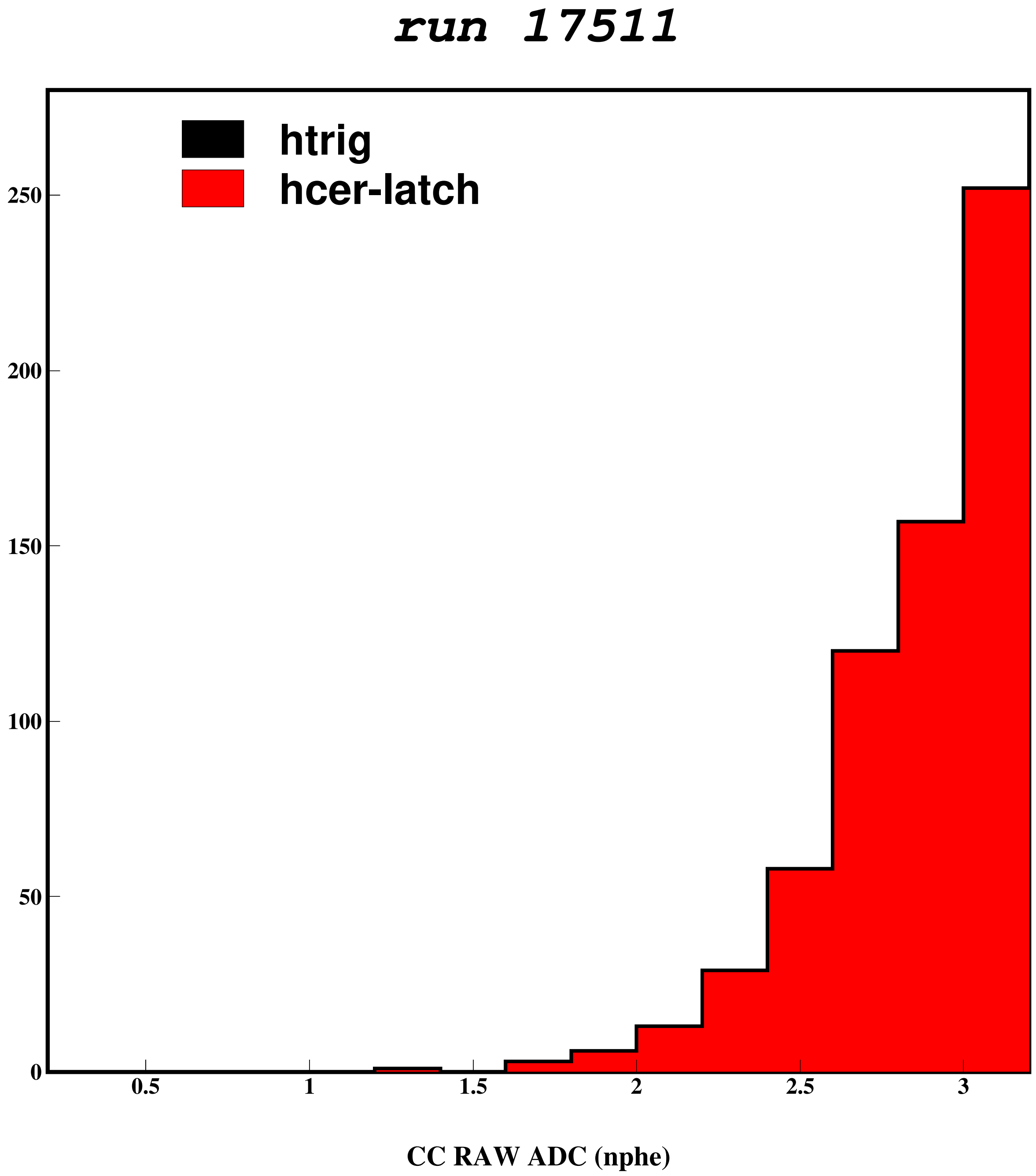


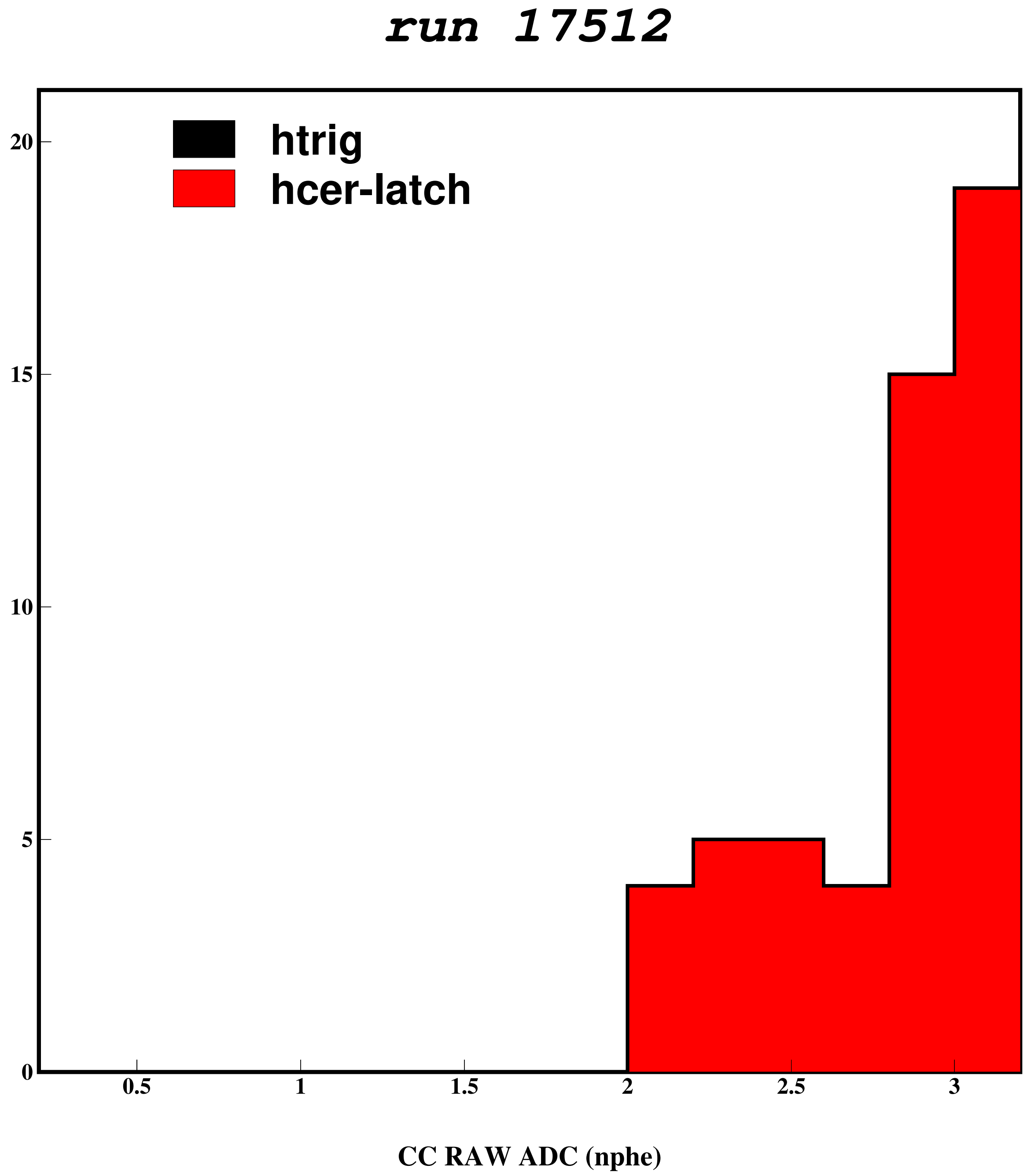
run 17505
htrig
hcer-latch
0
1
2
3
4
5
6
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

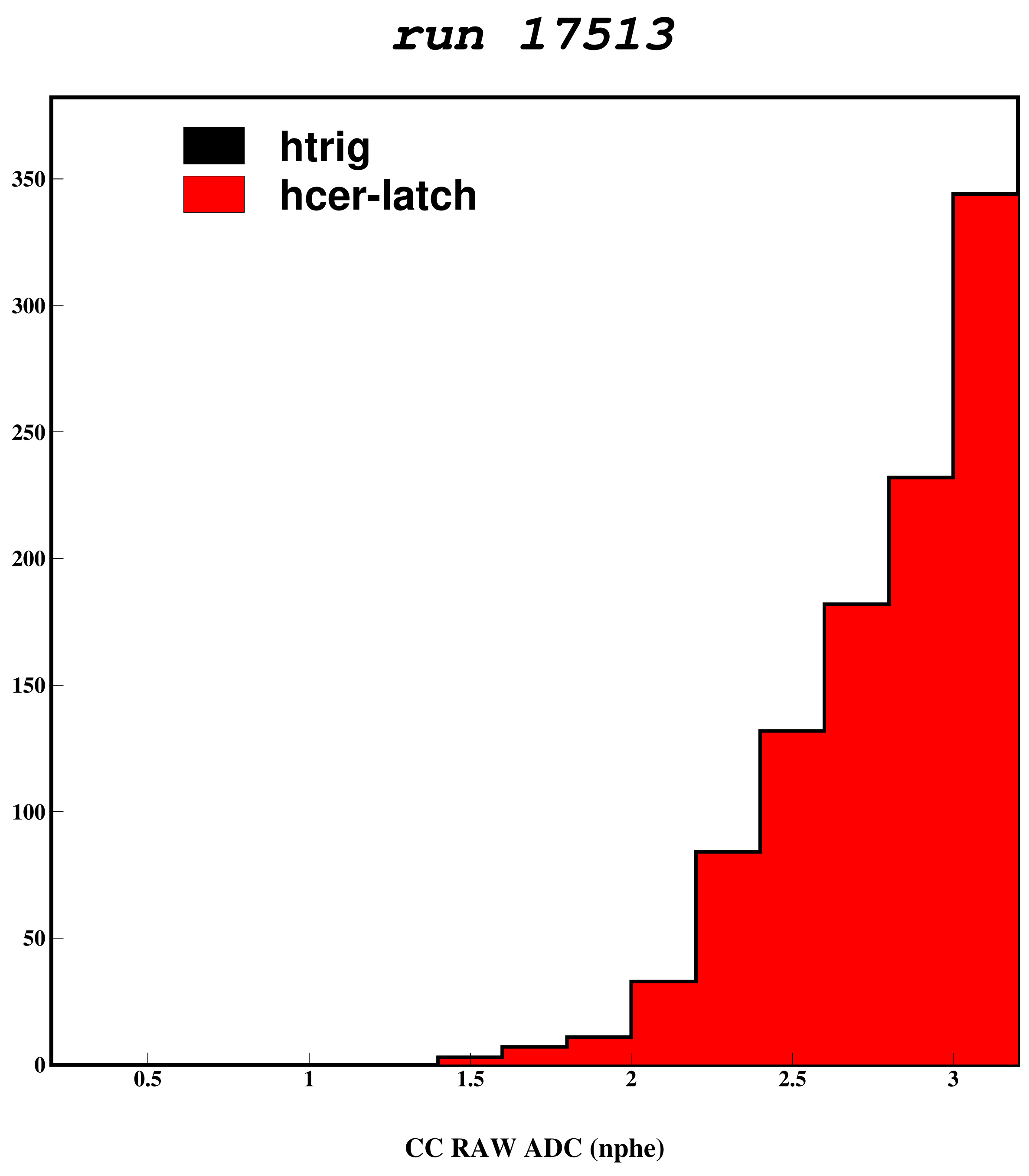


run 17506
htrig
hcer-latch
10
8
6
4
2
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

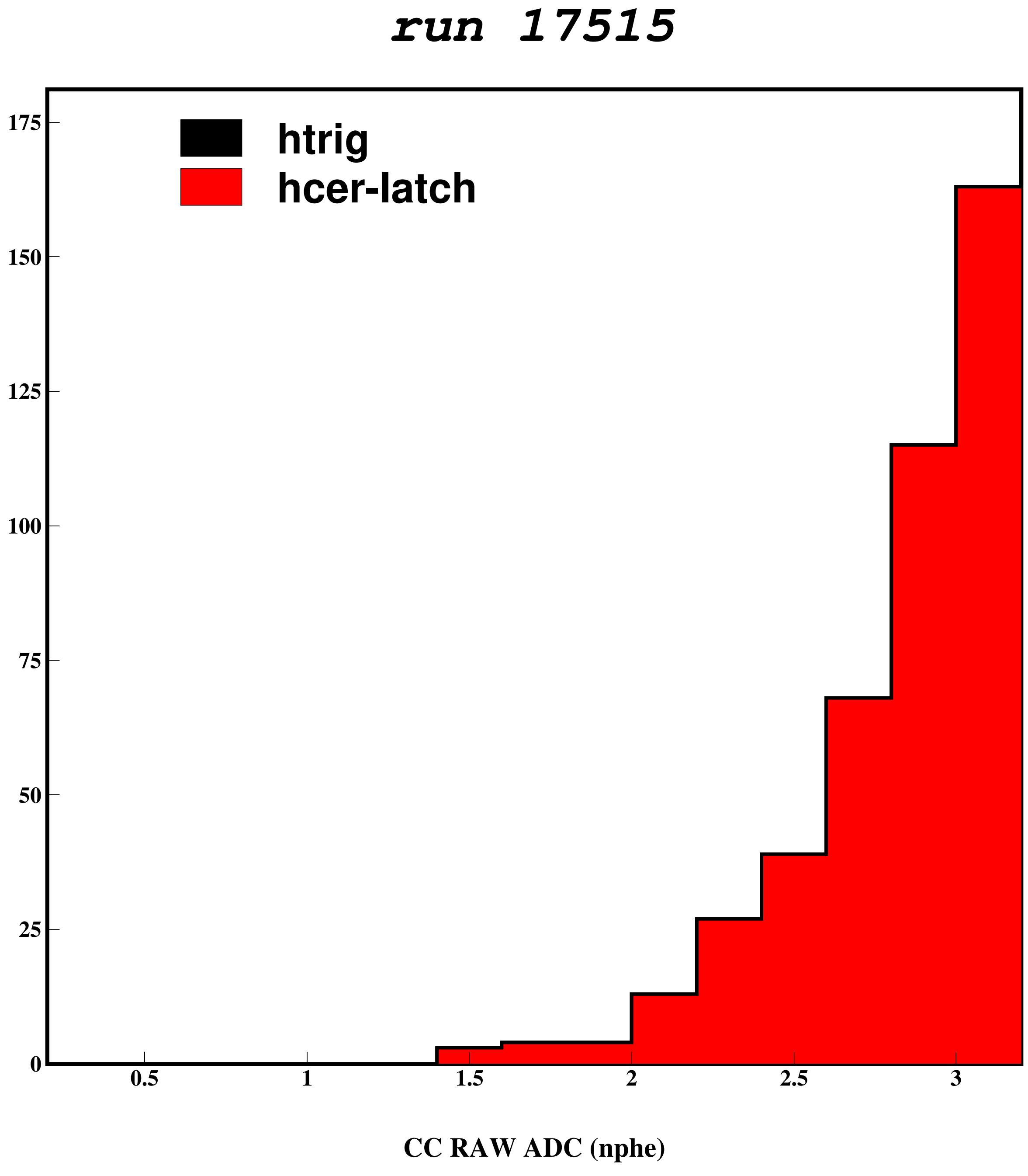

run 17507
htrig
hcer-latch
140
120
100
80
60
40
20
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)


run 17511
htrig
hcer-latch
0
50
100
150
200
250
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

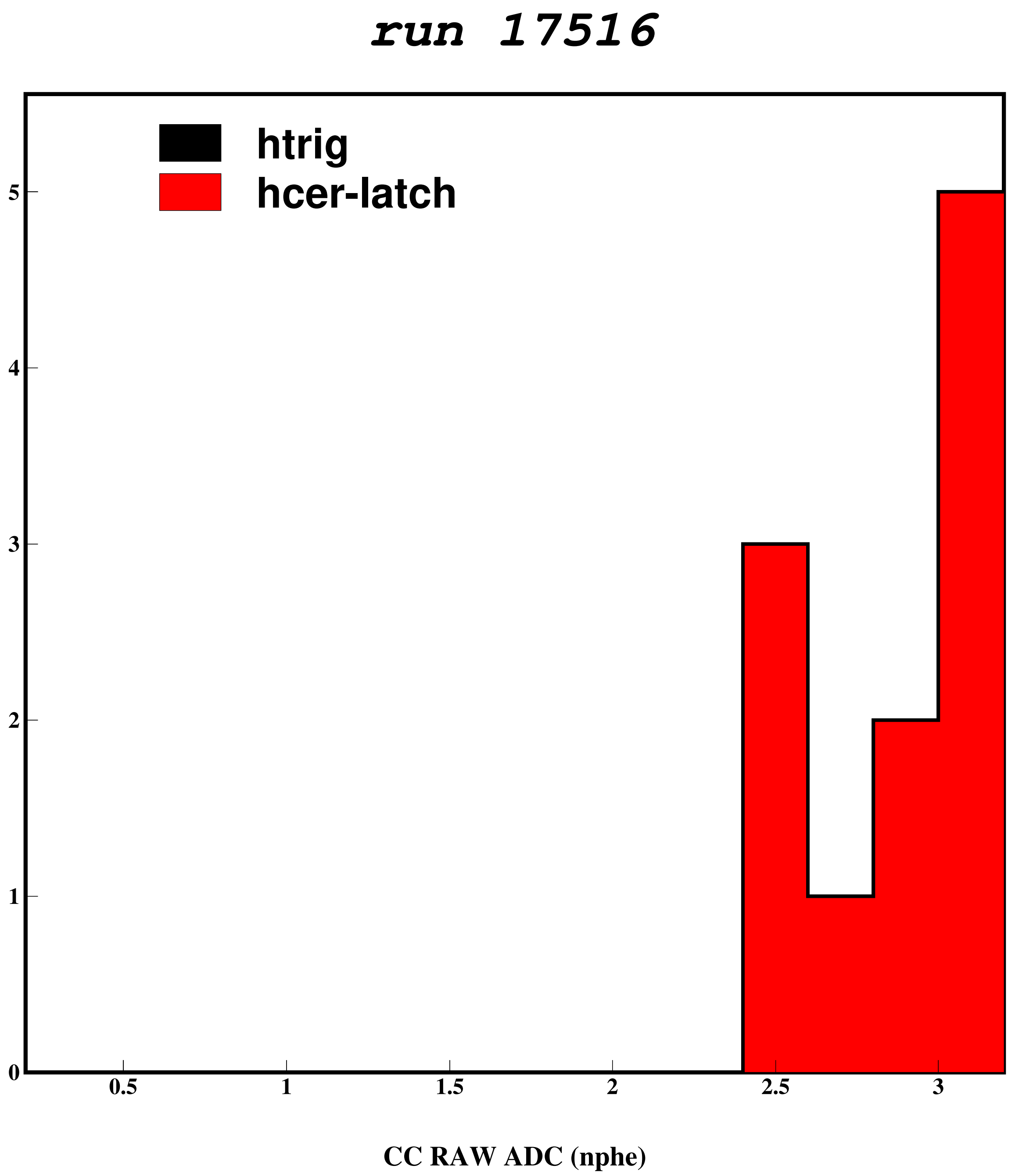

run 17512
htrig
hcer-latch
0
5
10
15
20
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

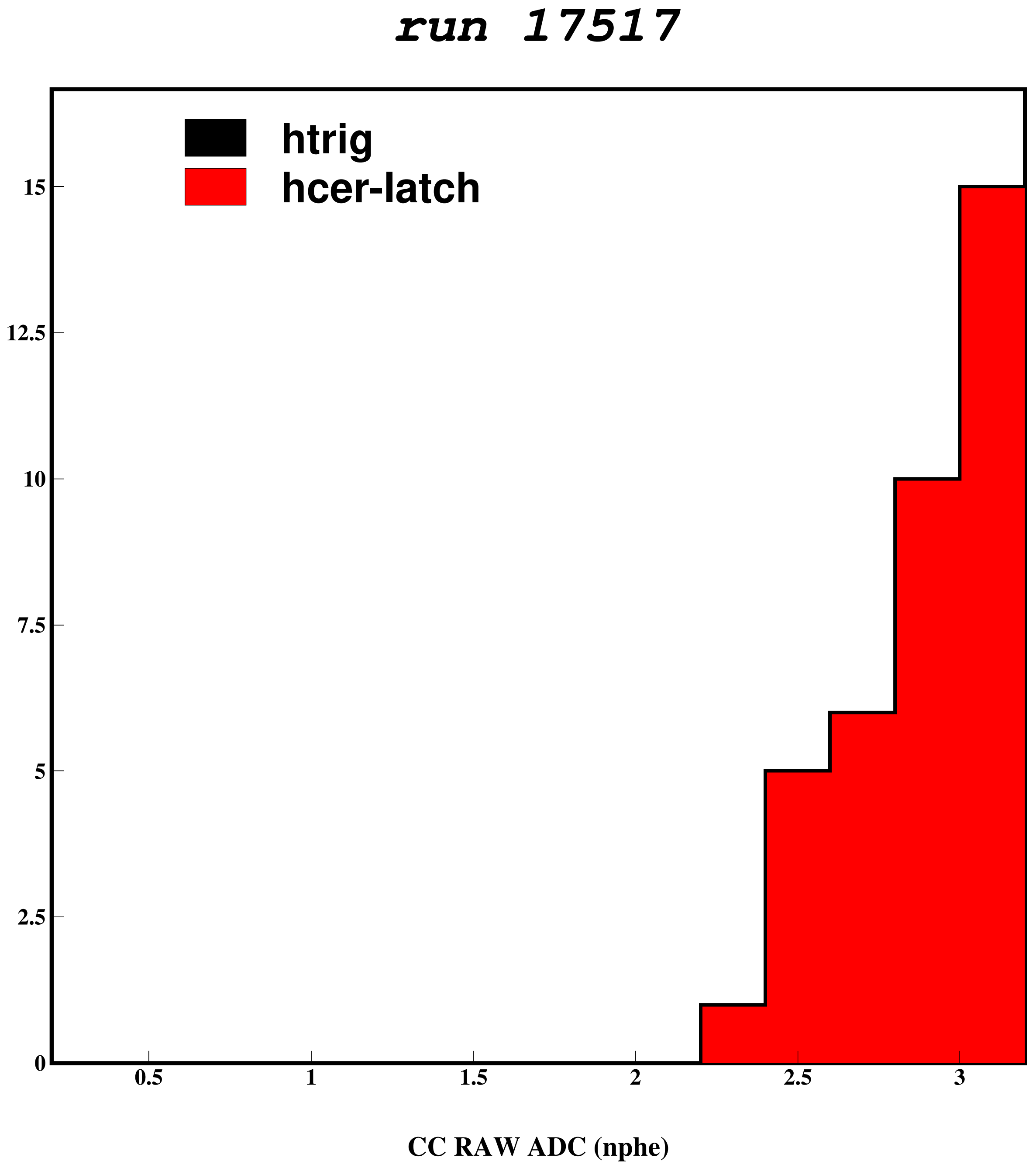


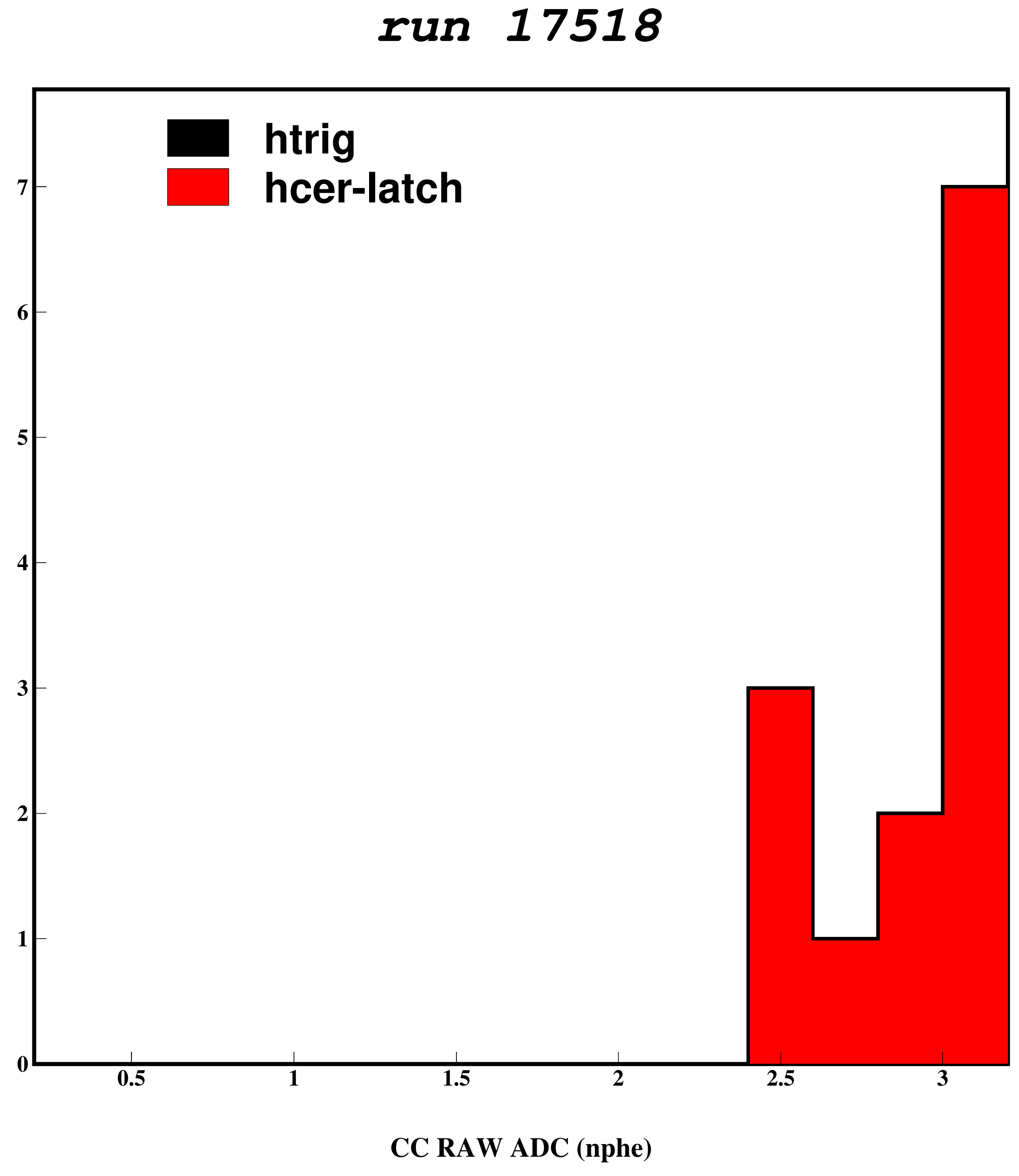
run 17513
htrig
hcer-latch
350
300
250
200
150
100
50
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

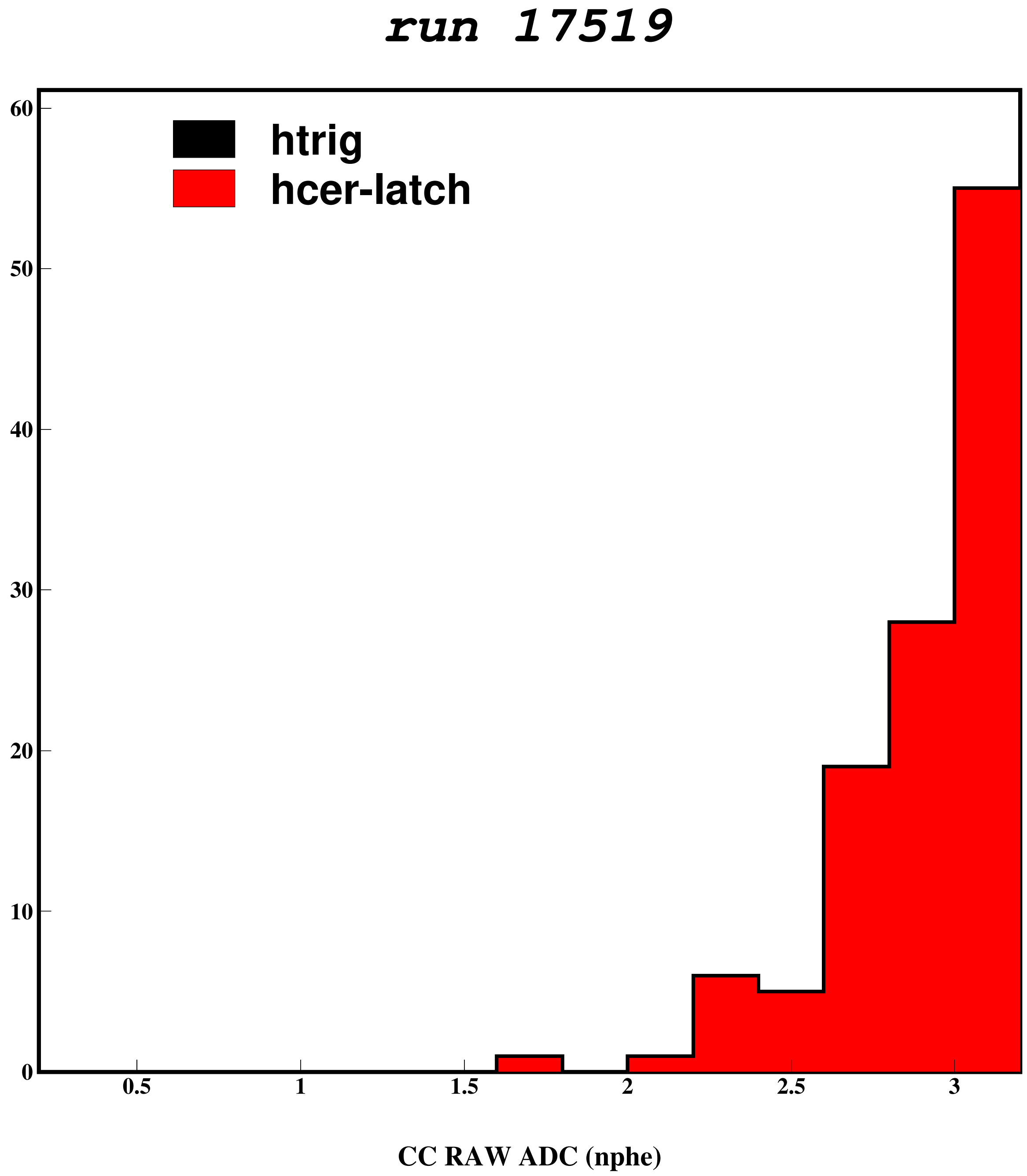


run 17515
htrig
hcer-latch
175
150
125
100
75
50
25
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

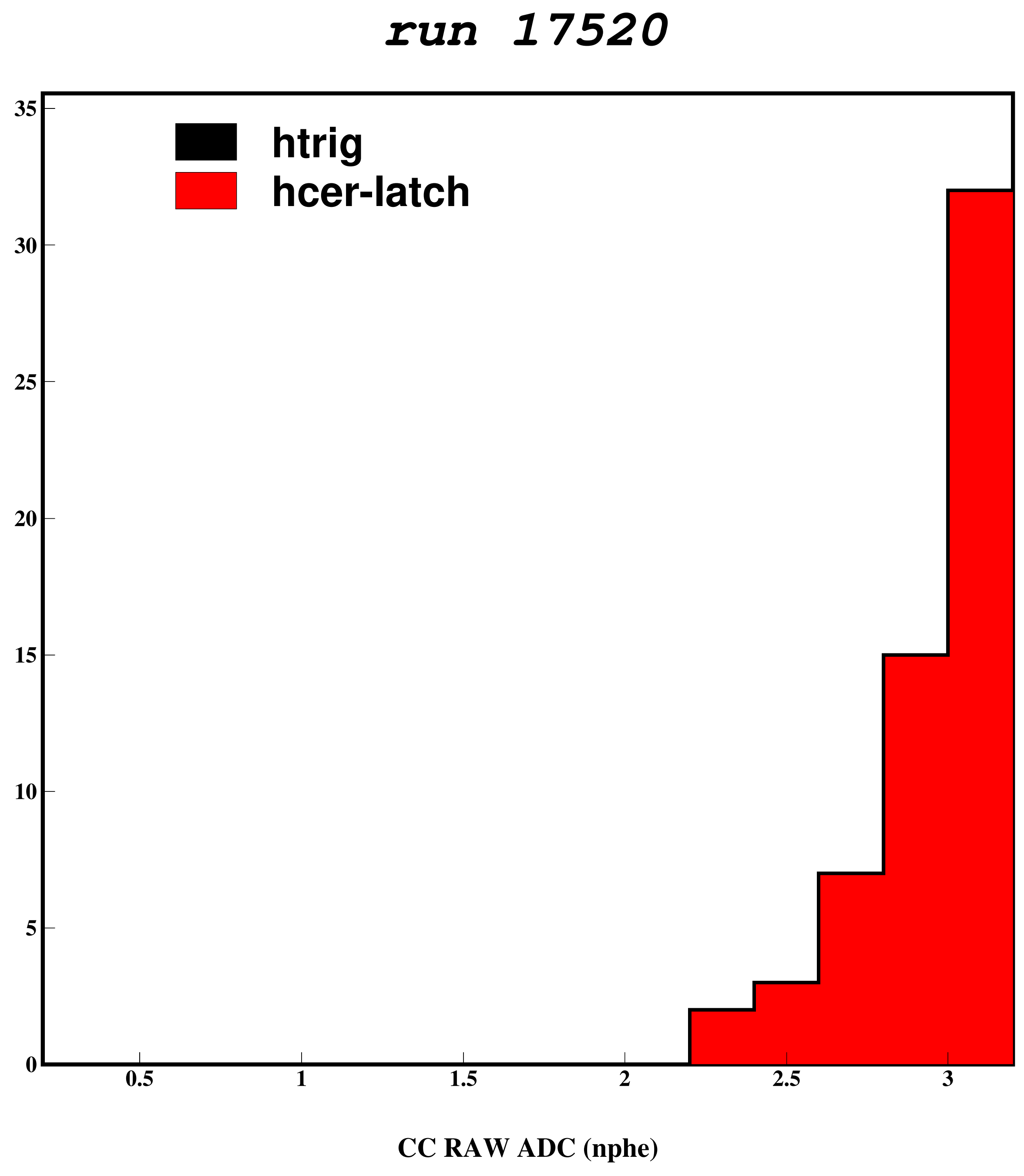

run 17516
htrig
hcer-latch
0
1
2
3
4
5
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

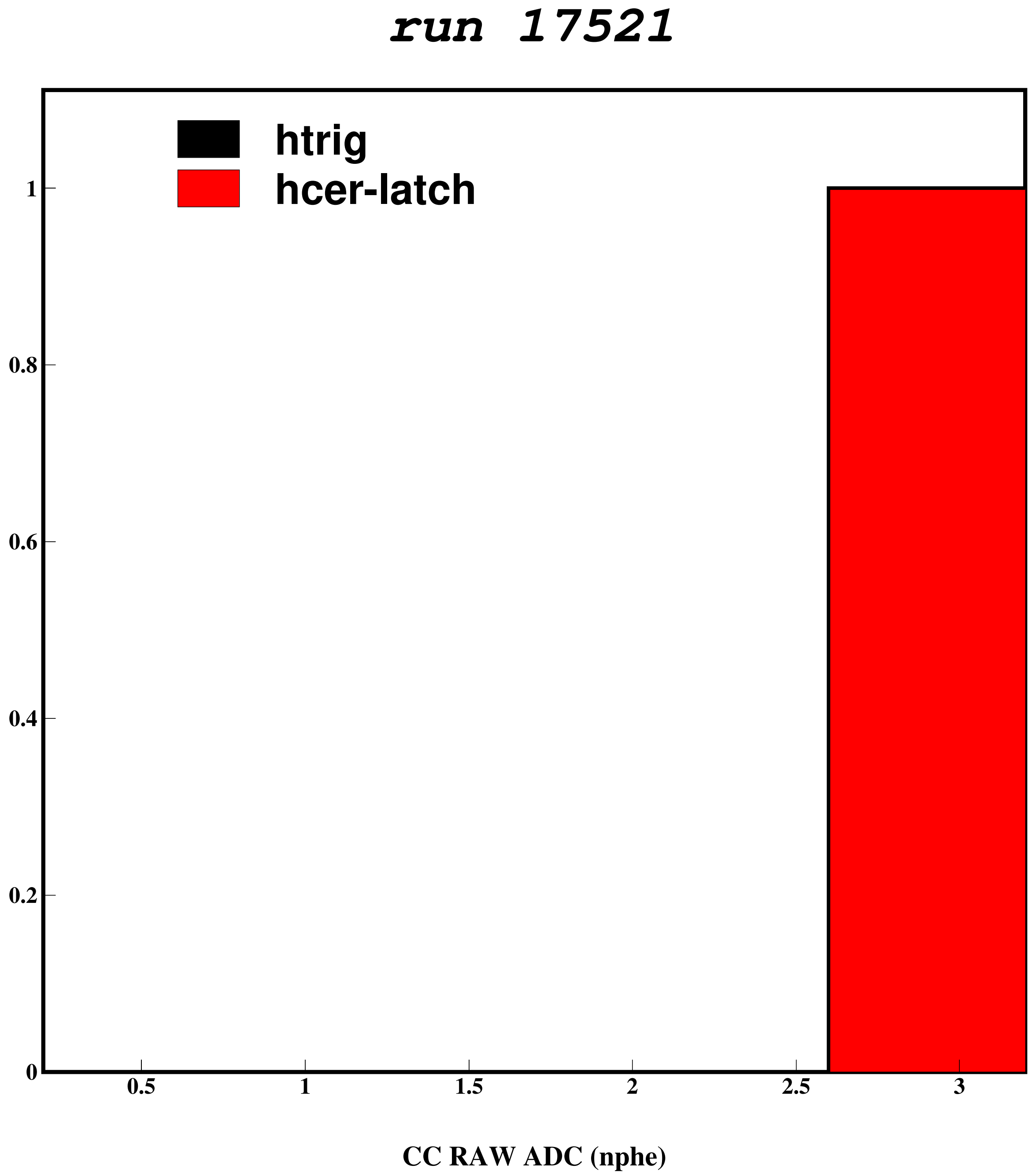

run 17517
htrig
hcer-latch
15
12.5
10
7.5
5
2.5
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

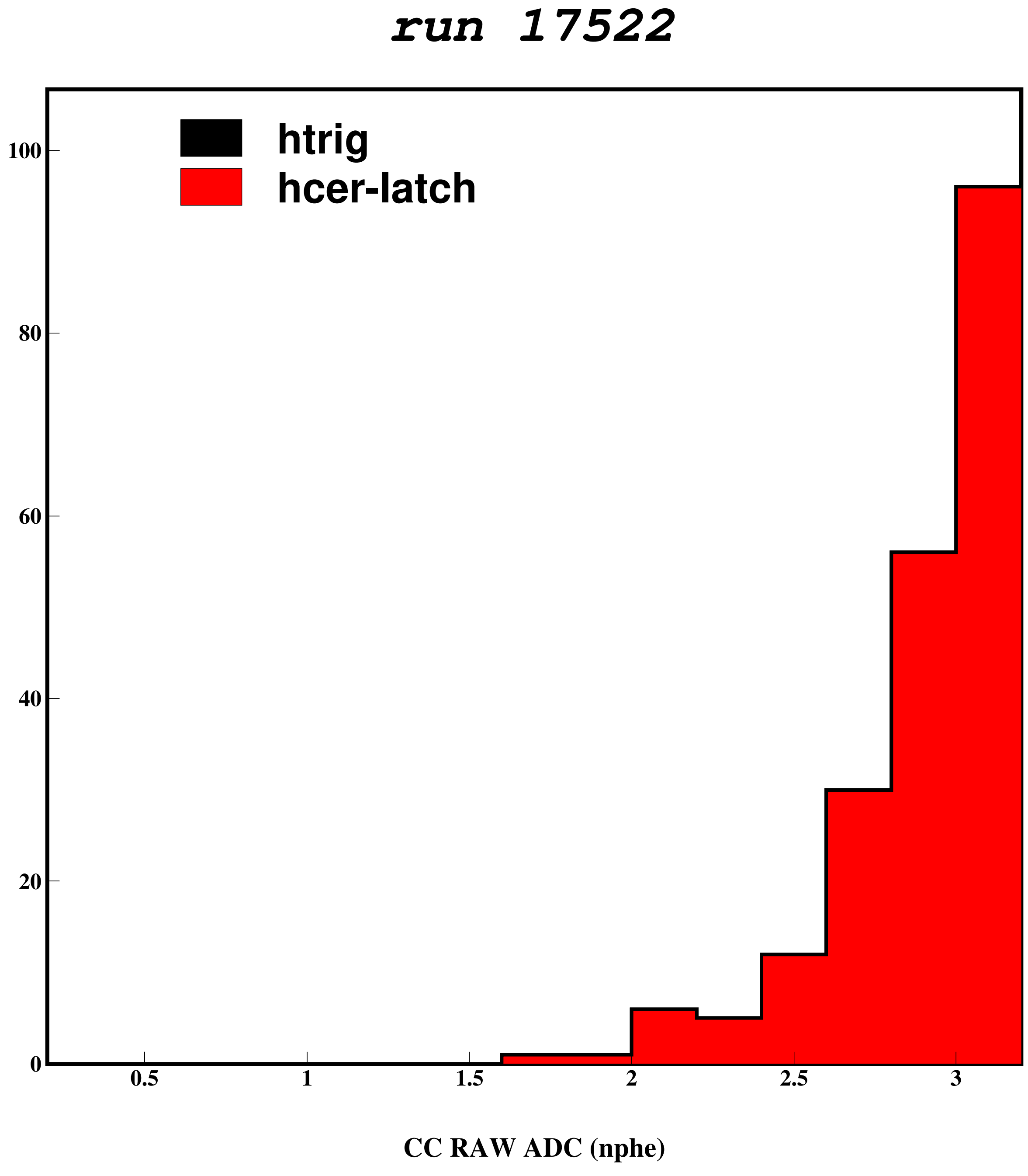

run 17518
htrig
hcer-latch
0
1
2
3
4
5
6
7
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

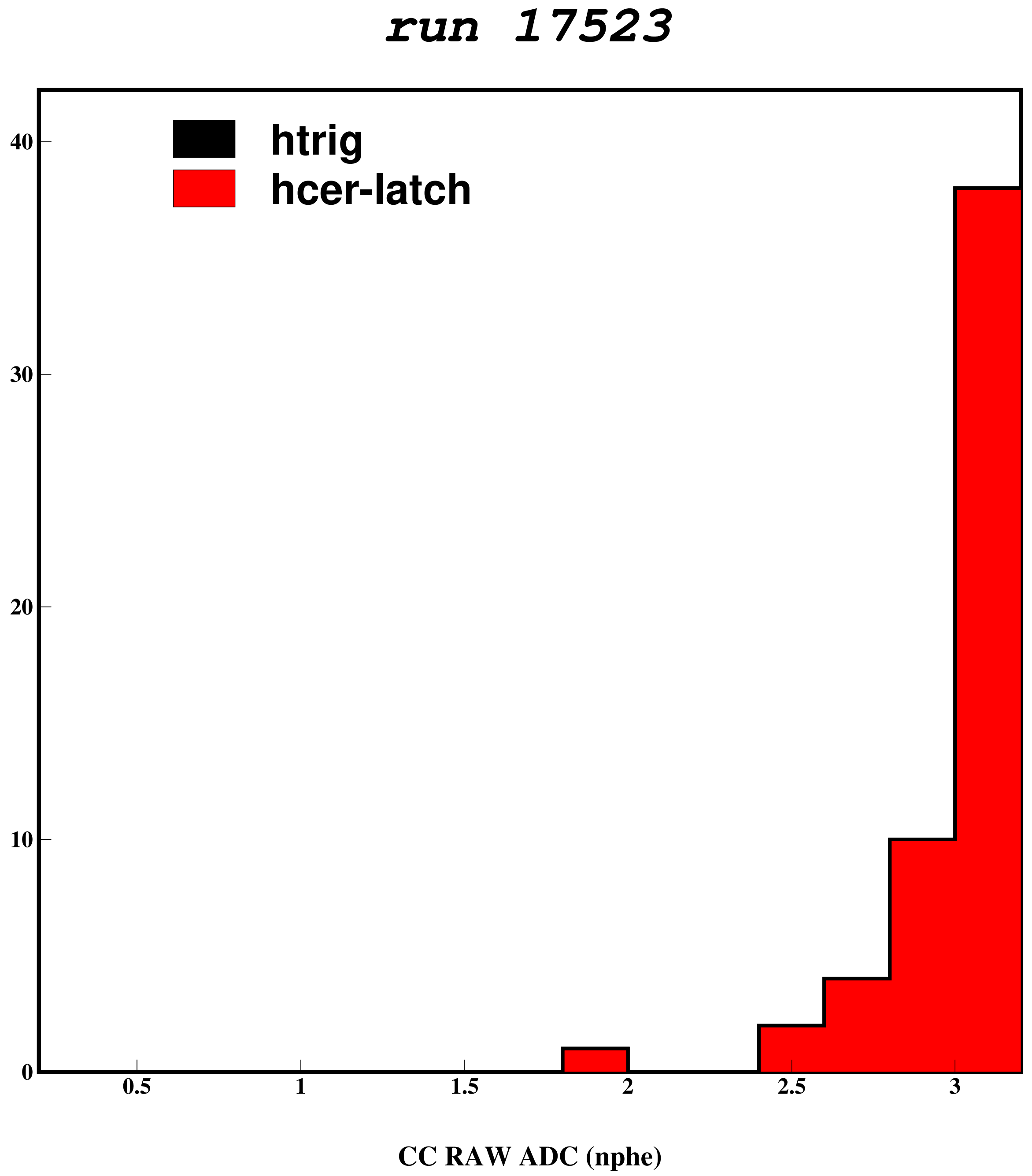


run 17519
htrig
hcer-latch
0
10
20
30
40
50
60
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

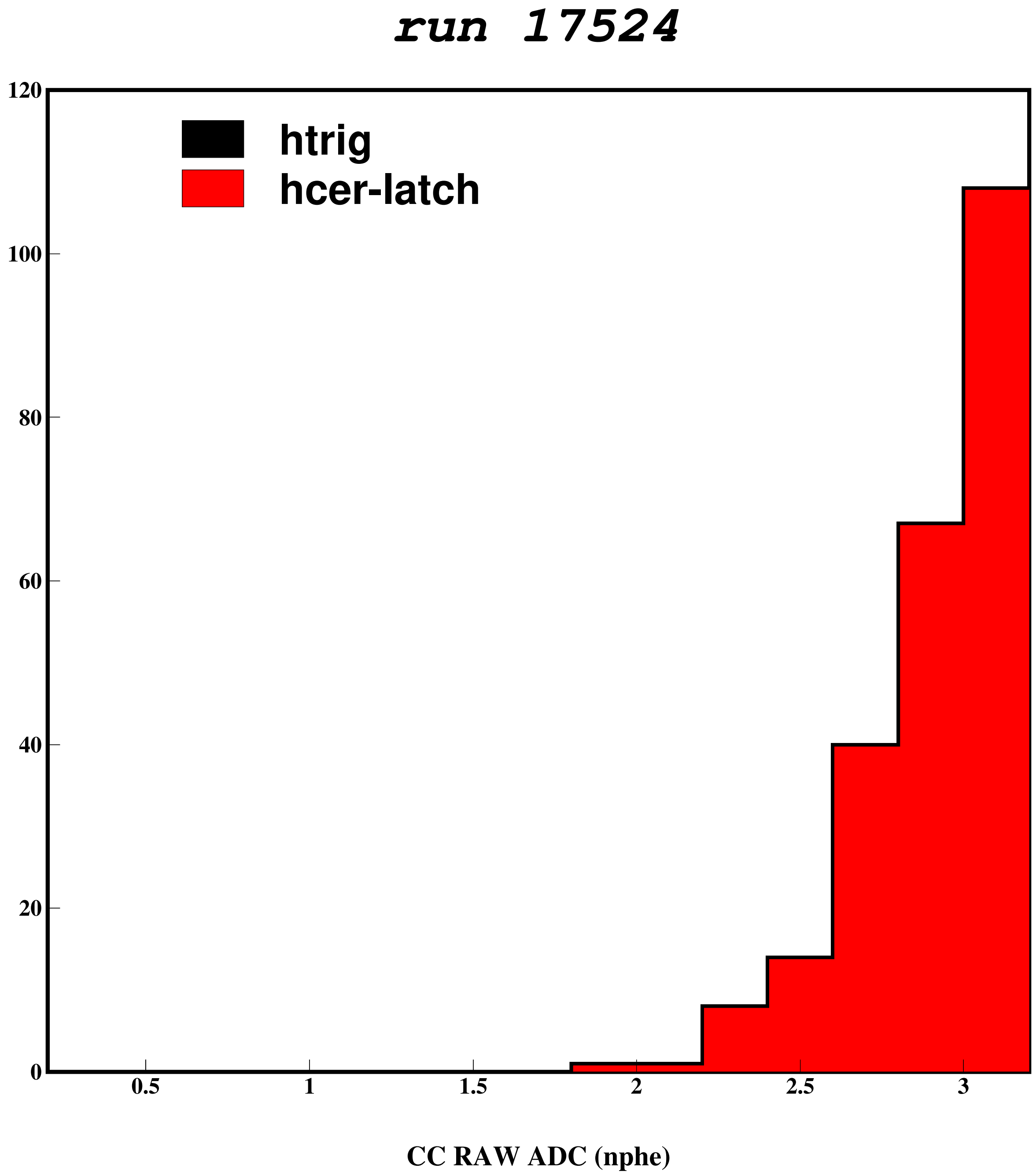

run 17520
htrig
hcer-latch
0
5
10
15
20
25
30
35
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)


run 17521
htrig
hcer-latch
1
0.8
0.6
0.4
0.2
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

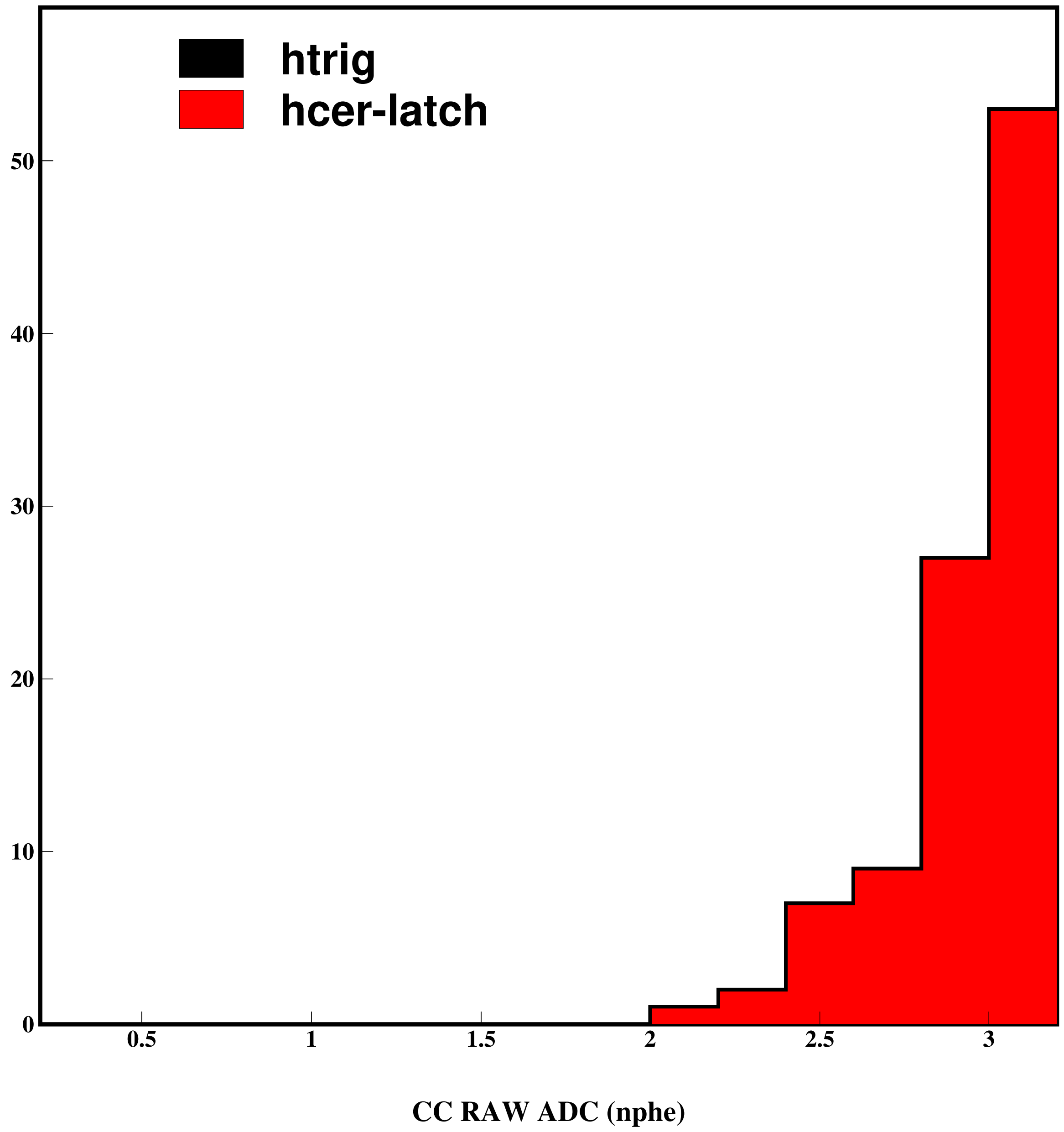

run 17522
htrig
hcer-latch
100
80
60
40
20
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)



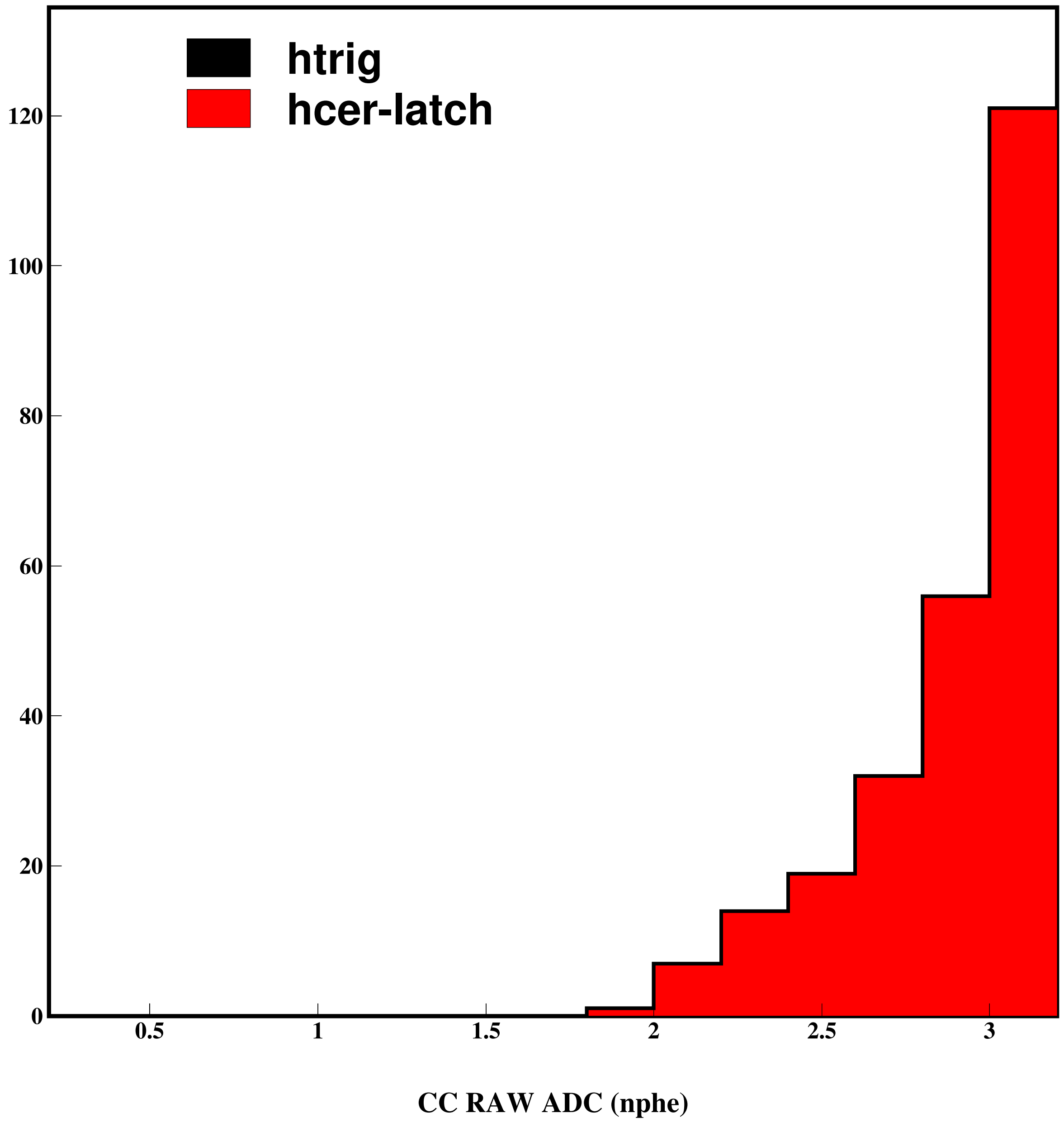
run 17523
htrig
hcer-latch
40
30
20
10
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)


run 17524
htrig
hcer-latch
120
100
80
60
40
20
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

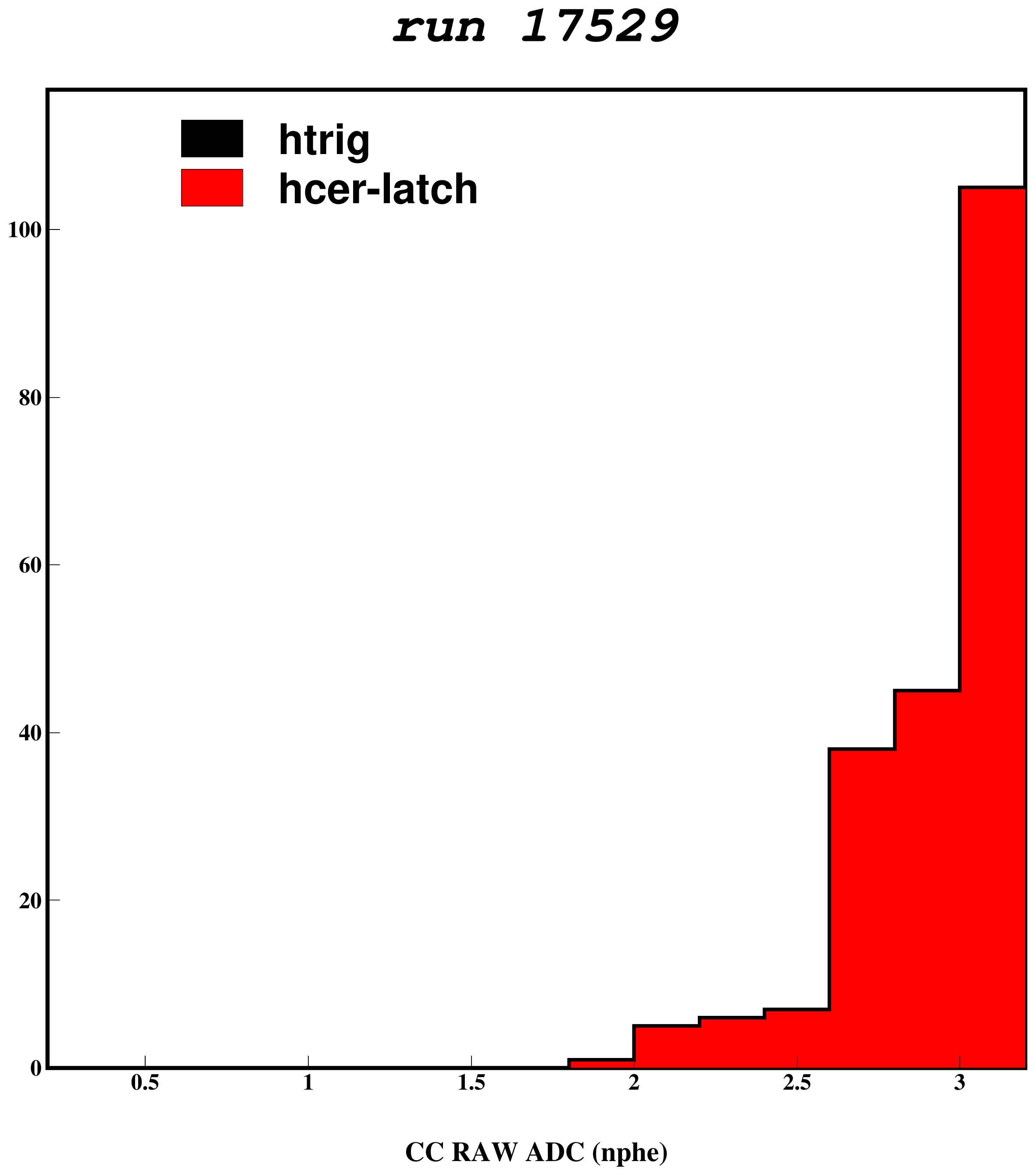
run 17525

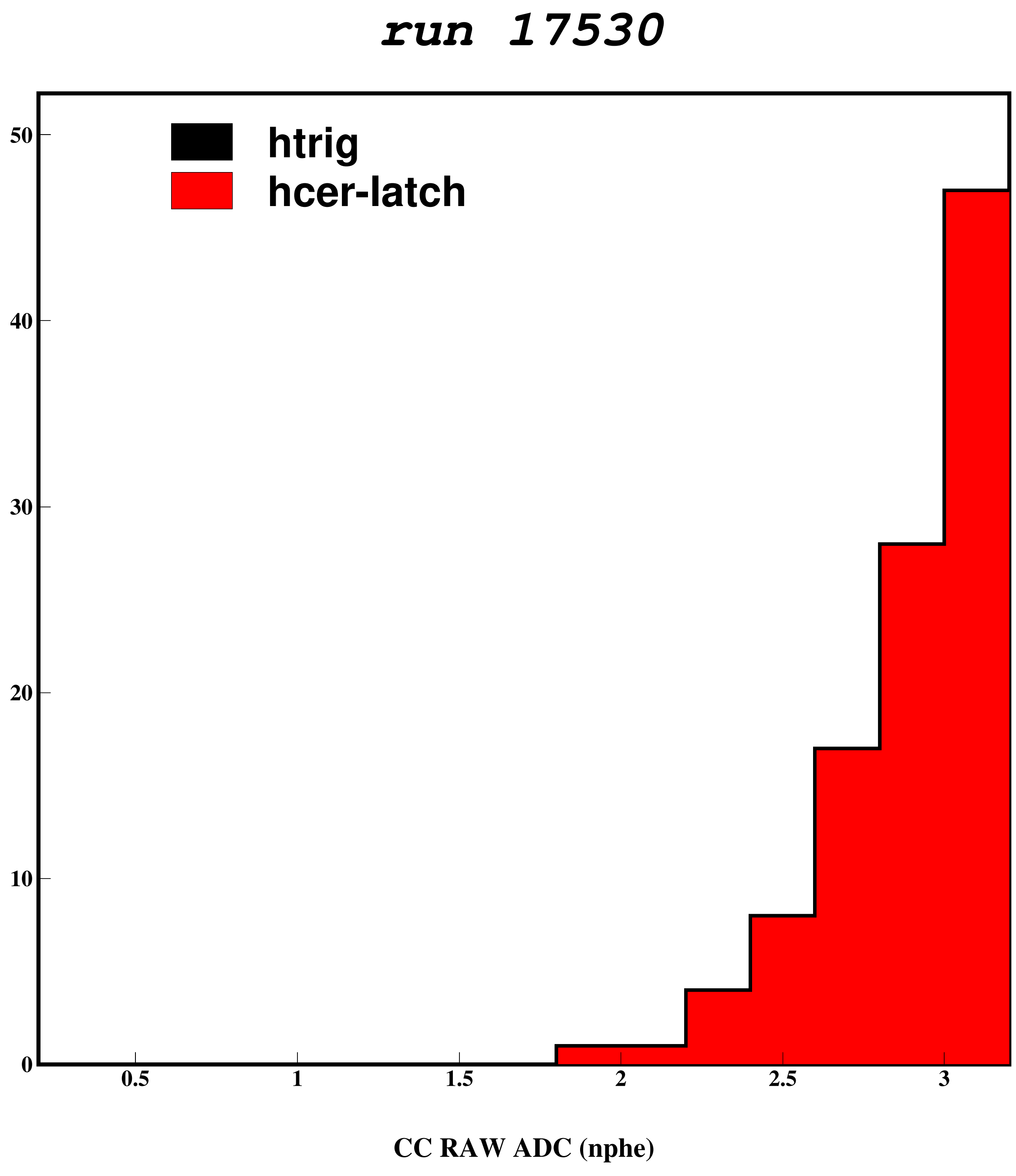


run 17526

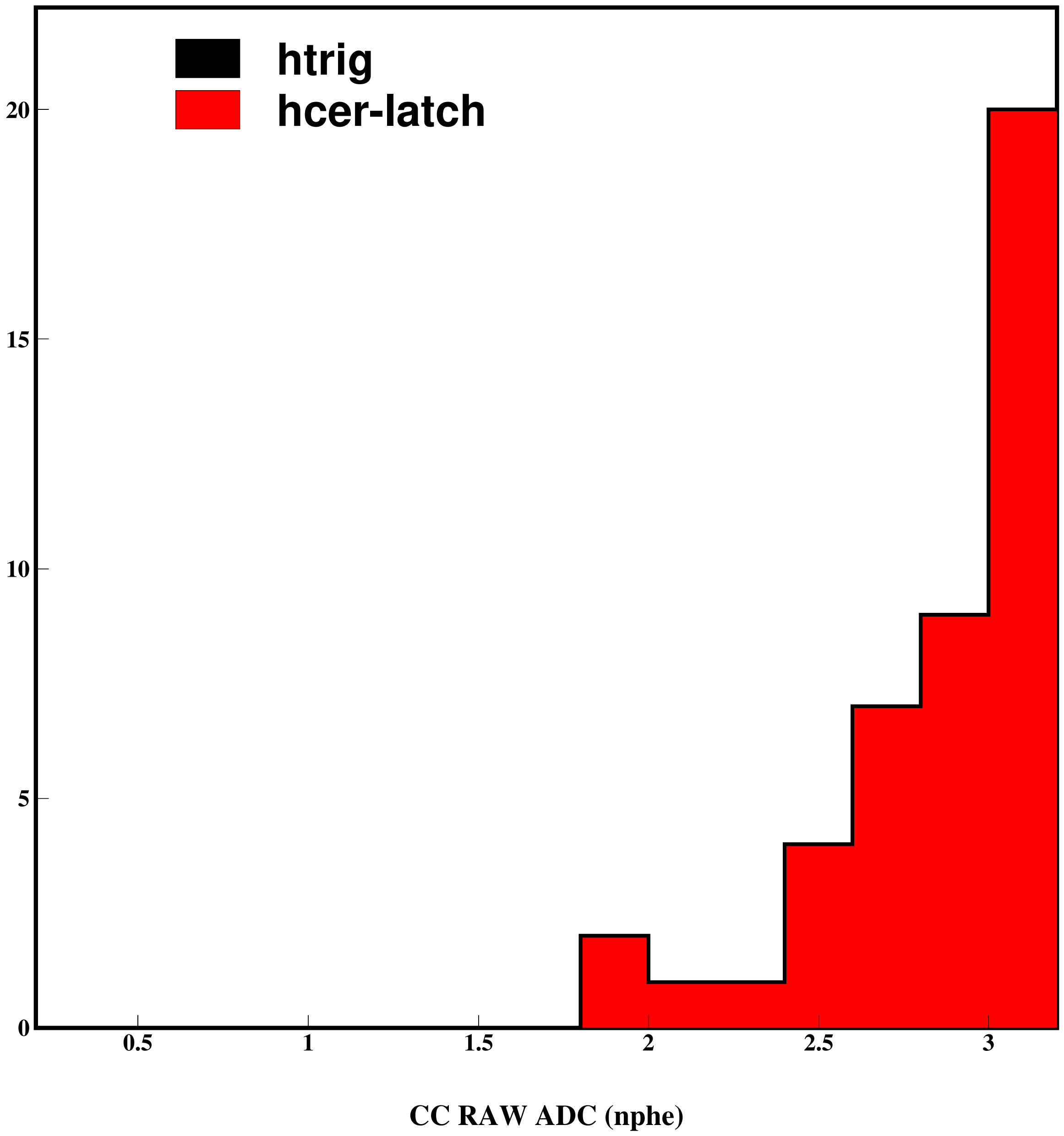


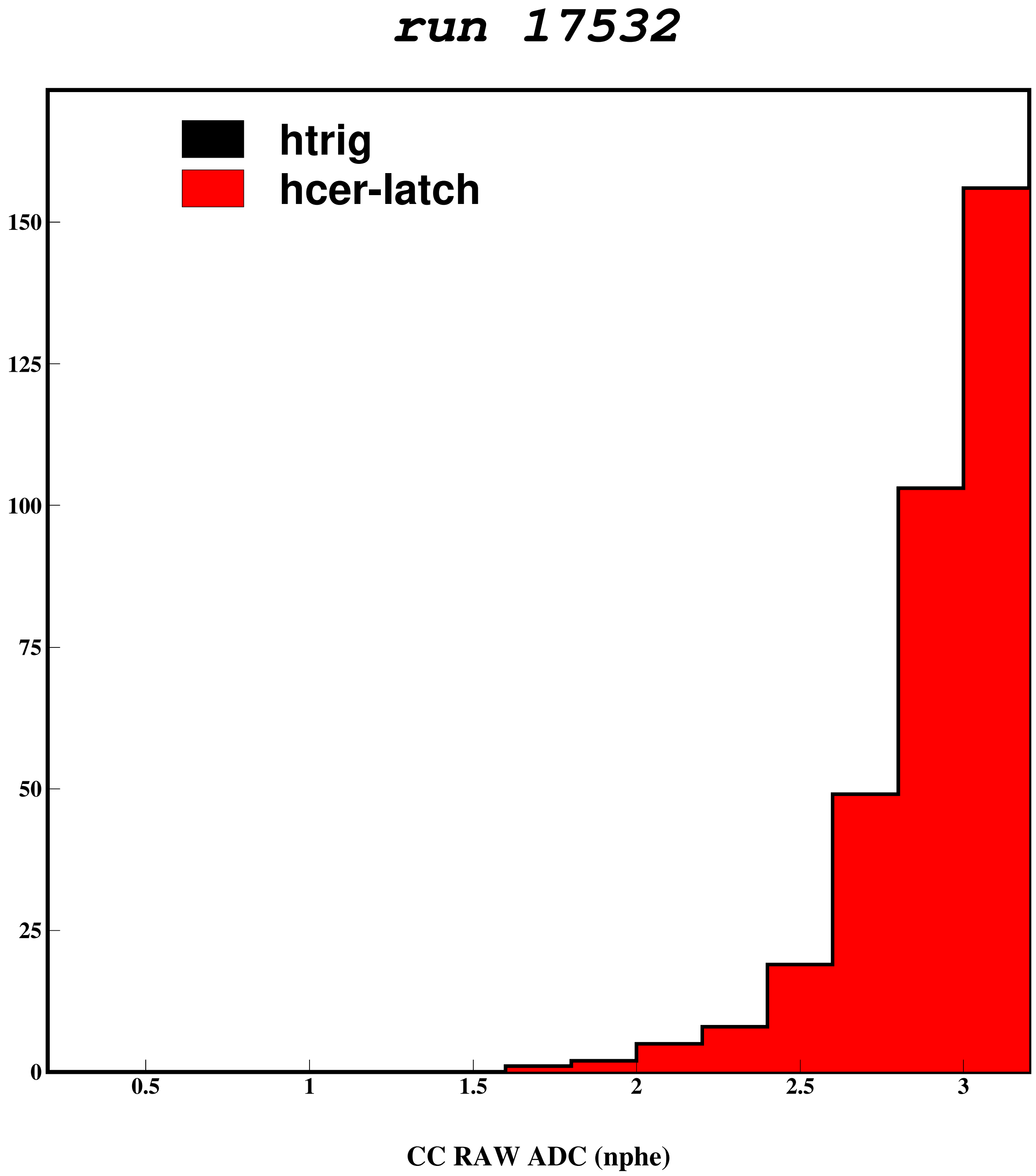
htrig
hcer-latch
120
100
80
60
40
20
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

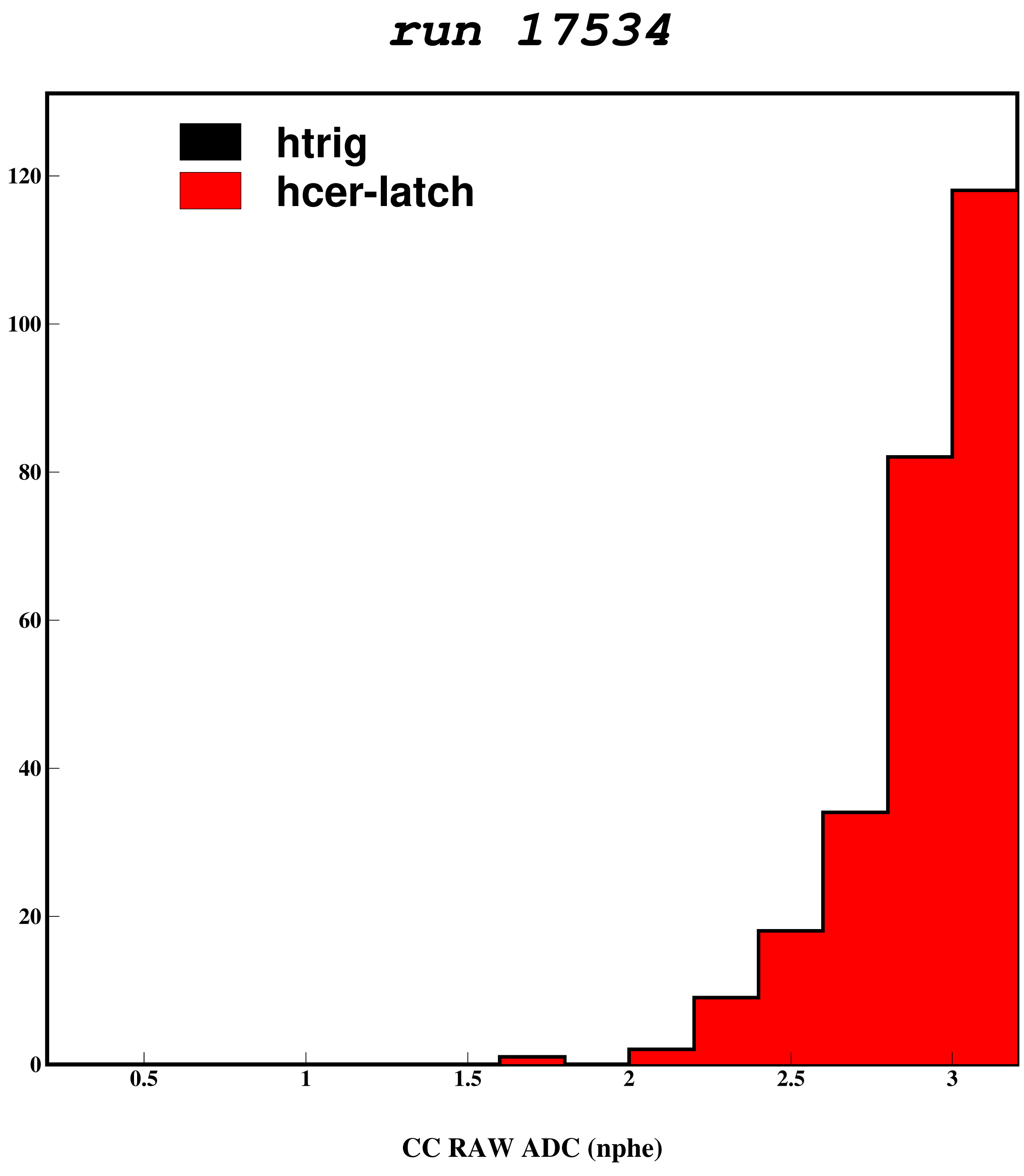

run 17529
htrig
hcer-latch
0
20
40
60
80
100
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

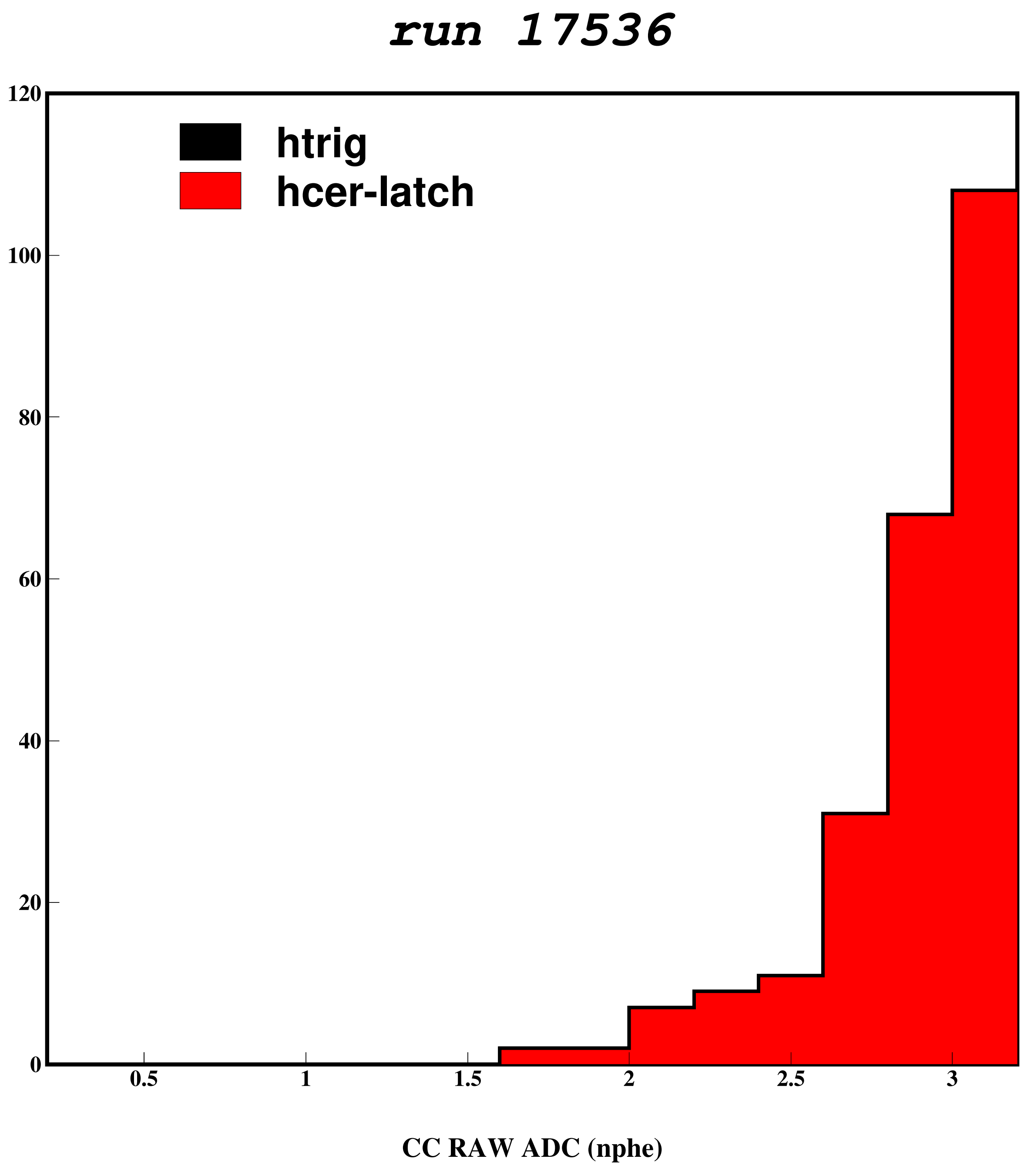

run 17530
htrig
hcer-latch
0
10
20
30
40
50
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)

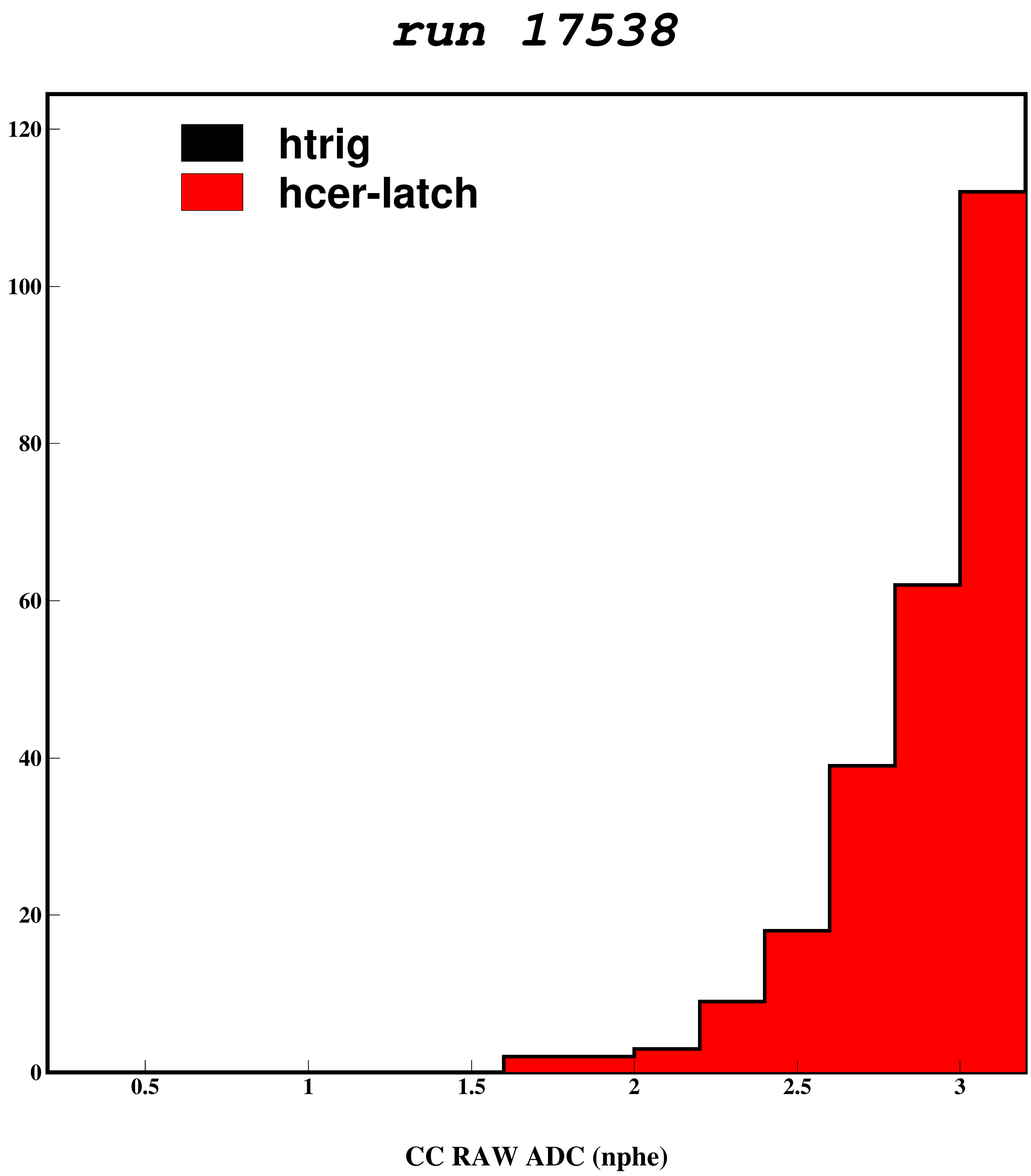
run 17531




run 17532
htrig
hcer-latch
150
125
100
75
50
25
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)


run 17534
htrig
hcer-latch
120
100
80
60
40
20
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)


run 17536
120
100
80
60
40
20
0
0.5
1
1.5
2
2.5
3
htrig
hcer-latch
CC RAW ADC (nphe)


run 17538
htrig
hcer-latch
120
100
80
60
40
20
0
0.5
1
1.5
2
2.5
3
CC RAW ADC (nphe)